



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL21A106KAYNNNG

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 10 µF, 25V, ±10%, X5R, 0805

A. Samsung Part Number

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1	Series	Samsung Multi-layer Ceramic Capacitor							
2	Size	0805 (inch c	ode)	L: 2	2.0 ± 0.2	2 mm	W:	1.25 ± 0.2	mm
3	Dielectric	X5R		(8 Inne	er electrode		Ni	
4	Capacitance	10 μF			Teri	mination		Cu	
(5)	Capacitance	±10 %			Plat	ing		Sn 100%	(Pb Free)
	tolerance			(9 Pro	duct		Normal	
6	Rated Voltage	25 V		(⑩ S ре	cial		Reserved for	r future use
7	Thickness	1.25 ± 0.2	mm	(Pac	kaging		Embossed T	ype, 7" reel(3000EA)

B. Samsung Reliablility Test and Judgement condition

	Performance	Test condition					
Capacitance	Within specified tolerance	1kltz±10% 1.0±0.2Vrms					
Tan δ (DF)	0.1 max.						
Insulation	10,000Mohm or 100Mohm⋅ <i>μ</i> F	Rated Voltage 60~120 sec.					
Resistance	Whichever is Smaller						
Appearance	No abnormal exterior appearance	Microscope (×10)					
Withstanding	No dielectric breakdown or	250% of the rated voltage					
Voltage	mechanical breakdown						
Temperature	X5R						
Characterisitcs	(From -55℃ to 85℃, Capacitance change shoud be within ±15%)						
Adhesive Strength	No peeling shall be occur on the	500g·F, for 10±1 sec.					
of Termination	terminal electrode						
Bending Strength	Capacitance change : within ±12.5%	Bending to the limit (1mm)					
		with 1.0mm/sec.					
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder					
	is to be soldered newly	245±5°C, 3±0.3sec.					
		(preheating : 80~120 ℃ for 10~30sec.)					
Resistance to	Capacitance change: within ±7.5%	Solder pot : 270±5℃, 10±1sec.					
Soldering heat	Tan δ, IR : initial spec.						

	Performance	Test condition				
Vibration Test	Capacitance change: within ±5%	Amplitude : 1.5mm				
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)				
		2hours × 3 direction (x, y, z)				
Moisture	Capacitance change: within ±12.5%	With rated voltage				
Resistance	Tan δ: 0.2 max	40±2℃, 90~95%RH, 500+12/-0hrs				
	IR: 12.5MΩ·μF or Over					
High Temperature	Capacitance change: within ±12.5%	With 100% of the rated voltage				
Resistance	Tan δ: 0.2 max	Max. operating temperature				
Resistance	IR: 25MΩ·μF or Over	ivida. Operating temperature				
	, , , ,	1000+48/-0hrs				
Temperature	Capacitance change: within ±7.5%	1 cycle condition				
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25 °C				
		$ ightarrow$ Max. operating temperature $ ightarrow$ 25 $^{\circ}$ C				
		5 cycle test				

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}$ C, 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.