International TOR Rectifier

IRF2807PbF

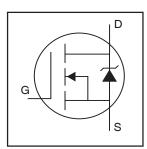
HEXFET® Power MOSFET

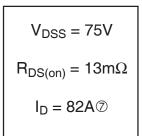
- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

Description

Advanced HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.







Absolute Maximum Ratings

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	82⑦	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	58	A
I _{DM}	Pulsed Drain Current ①	280	
P _D @T _C = 25°C	Power Dissipation	230	W
	Linear Derating Factor	1.5	W/°C
V _{GS}	Gate-to-Source Voltage	± 20	V
I _{AR}	Avalanche Current①	43	А
E _{AR}	Repetitive Avalanche Energy①	23	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.9	V/ns
TJ	Operating Junction and	-55 to + 175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	
	Mounting torque, 6-32 or M3 srew	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case		0.65	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient		62	

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	75			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.074		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			13	mΩ	V _{GS} = 10V, I _D = 43A ④
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
9fs	Forward Transconductance	38			S	V _{DS} = 50V, I _D = 43A ⁽⁴⁾
I _{DSS}	Drain-to-Source Leakage Current			25	μA	$V_{DS} = 75V$, $V_{GS} = 0V$
1055	Brain to Godroo Edunage Garroni			250	μΛ	$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
1	Gate-to-Source Forward Leakage			100	nA	$V_{GS} = 20V$
I _{GSS}	Gate-to-Source Reverse Leakage			-100	l IIA	V _{GS} = -20V
Qg	Total Gate Charge			160		$I_D = 43A$
Q _{gs}	Gate-to-Source Charge			29	nC	$V_{DS} = 60V$
Q _{gd}	Gate-to-Drain ("Miller") Charge			55		V_{GS} = 10V, See Fig. 6 and 13
t _{d(on)}	Turn-On Delay Time		13			$V_{DD} = 38V$
t _r	Rise Time		64		ns	$I_D = 43A$
t _{d(off)}	Turn-Off Delay Time		49		115	$R_G = 2.5\Omega$
t _f	Fall Time		48			$V_{GS} = 10V$, See Fig. 10 $\textcircled{4}$
L _D	Internal Drain Inductance		4.5		nH	Between lead,
						6mm (0.25in.)
L _S	Internal Source Inductance		7.5			from package
						and center of die contact
C _{iss}	Input Capacitance		3820			V _{GS} = 0V
Coss	Output Capacitance		610			$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		130		pF	f = 1.0MHz, See Fig. 5
E _{AS}	Single Pulse Avalanche Energy ^②		1280 ©	340⑥	mJ	$I_{AS} = 50A, L = 370\mu H$

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			000		MOSFET symbol
	(Body Diode)	82	82⑦	A	showing the	
I _{SM}	Pulsed Source Current			200] ^	integral reverse
	(Body Diode)①		280	280	p-n junction diode.	
V_{SD}	Diode Forward Voltage			1.2	V	$T_J = 25^{\circ}C$, $I_S = 43A$, $V_{GS} = 0V$ ④
t _{rr}	Reverse Recovery Time		100	150	ns	T _J = 25°C, I _F = 43A
Q _{rr}	Reverse Recovery Charge		410	610	nC	di/dt = 100A/µs ④
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)				

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\begin{tabular}{ll} \hline \& Starting $T_J = 25^\circ C$, $L = 370 \mu H$ \\ R_G = 25 \Omega, I_{AS} = 43 A, V_{GS} = 10 V (See Figure 12) \\ \hline \end{tabular}$
- $\label{eq:loss} \begin{array}{l} \mbox{ } 3 \mbox{ } I_{SD} \leq 43A, \mbox{ } di/dt \leq 300A/\mu s, \mbox{ } V_{DD} \leq V_{(BR)DSS}, \\ \mbox{ } T_{J} \leq 175^{\circ} \mbox{C} \end{array}$
- 4 Pulse width $\leq 400 \mu s$; duty cycle $\leq 2\%$.
- ⑤ This is a typical value at device destruction and represents operation outside rated limits.
- 6 This is a calculated value limited to $T_J = 175^{\circ}C$.
- ② Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.

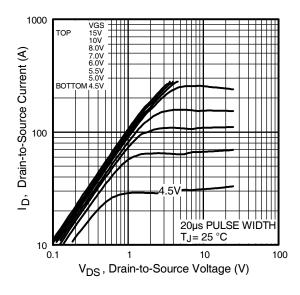


Fig 1. Typical Output Characteristics

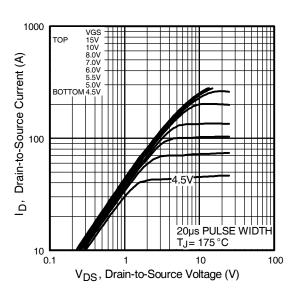


Fig 2. Typical Output Characteristics

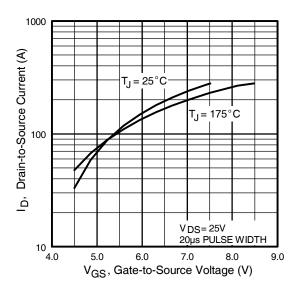


Fig 3. Typical Transfer Characteristics

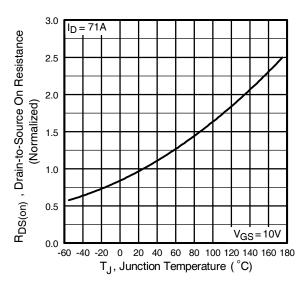


Fig 4. Normalized On-Resistance Vs. Temperature

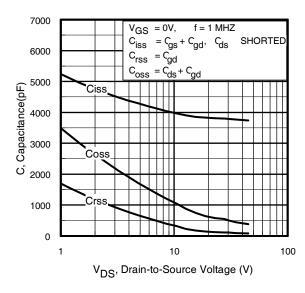


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

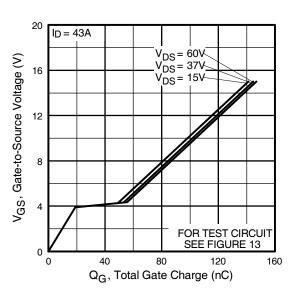


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

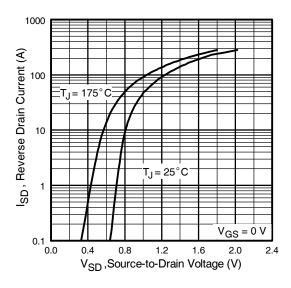


Fig 7. Typical Source-Drain Diode Forward Voltage

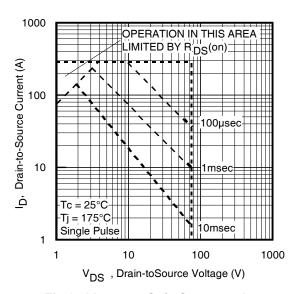


Fig 8. Maximum Safe Operating Area

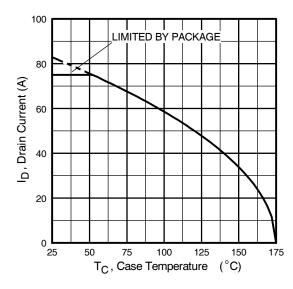


Fig 9. Maximum Drain Current Vs. Case Temperature

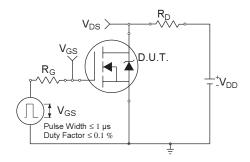


Fig 10a. Switching Time Test Circuit

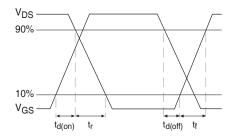


Fig 10b. Switching Time Waveforms

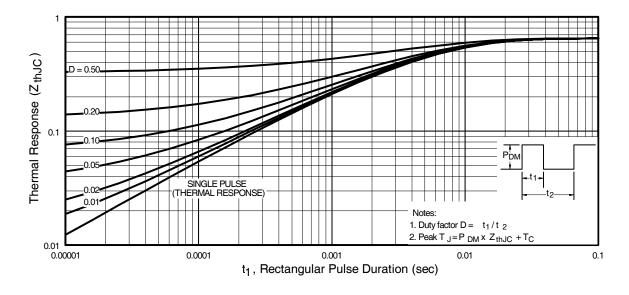


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

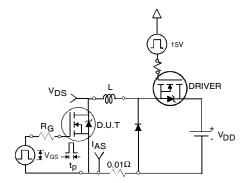


Fig 12a. Unclamped Inductive Test Circuit

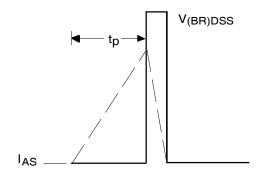


Fig 12b. Unclamped Inductive Waveforms

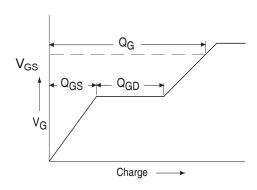


Fig 13a. Basic Gate Charge Waveform

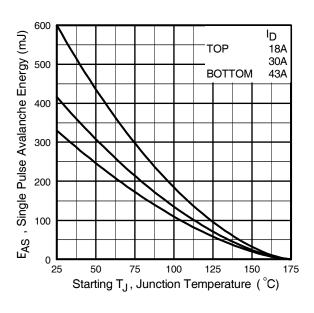


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

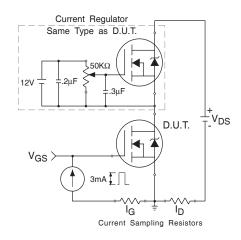
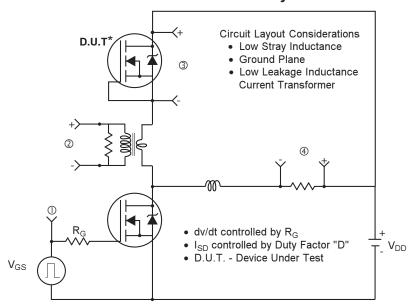
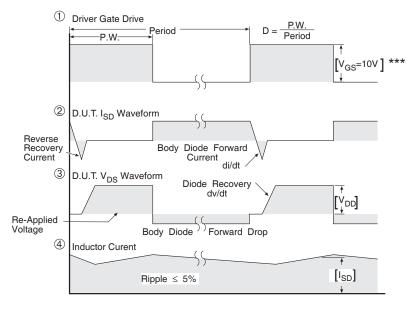


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



* Reverse Polarity of D.U.T for P-Channel

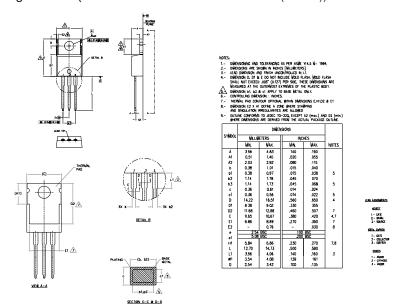


^{***} V_{GS} = 5.0V for Logic Level and 3V Drive Devices

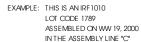
Fig 14. For N-channel HEXFET® power MOSFETs

International **TOR** Rectifier

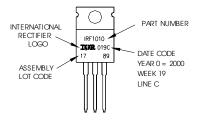
TO-220AB Package Outline(Dimensions are shown in millimeters (inches))



TO-220AB Part Marking Information



Note: "P" in assembly line position indicates "Lead - Free"



TO-220AB package is not recommended for Surface Mount Application.

Notes:

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- 1. For an Automotive Qualified version of this part please see http://www.irf.com/product-info/auto/
- 2. For the most current drawing please refer to IR website at http://www.irf.com/package/

Data and specifications subject to change without notice. This product has been designed and qualified for the Industrial market.

Qualification Standards can be found on IR's Web site.



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