

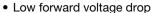
Vishay Semiconductors

Schottky Rectifier, 3.0 A



PRODUCT SUMMARY				
Package	SMC			
I _{F(AV)}	3.0 A			
V_{R}	60 V			
V _F at I _F	0.52 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	5.0 mJ			

FEATURES





 Guard ring for enhanced ruggedness and long term reliability

RoHS COMPLIANT

Halogen-free according to IEC 61249-2-21 definition

HALOGEN FREE

- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC

DESCRIPTION

The VS-30BQ060-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	3.0	A	
V _{RRM}		60	V	
I _{FSM}	t _p = 5 μs sine	1200	A	
V _F	3.0 Apk, T _J = 125 °C	0.52	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-30BQ060-M3	UNITS
Maximum DC reverse voltage	V_{R}	60	V
Maximum working peak reverse voltage	V_{RWM}	00	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Marian and Control of the Control of		50 % duty cycle at T _L = 123 °C, rectangular waveform		3.0	
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 113 °C,	rectangular waveform	4.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	1200	А
non-repetitive surge current at T _C = 25 °C	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	130	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 10 mH		5.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 1.0		Α	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	3 A	- T _J = 25 °C	0.58	V
Maximum forward valtage drop		6 A		0.76	
Maximum forward voltage drop		3 A	- T _J = 125 °C	0.52	
		6 A		0.66	
Maximum reverse leakage current	1	T _J = 25 °C	V _B = Rated V _B	0.5	mA
Waximum reverse leakage current	I _{RM}	T _J = 125 °C	VR = nateu VR	20	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to1 MHz), 25 °C		180	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}}$ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		- 55 to 150	9
Maximum storage temperature range	T _{Stg}		- 55 10 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC an austica	12	90 AM
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	°C/W
A			0.24	g
Approximate weight			0.008	OZ.
Marking device		Case style SMC (similar to DO-214AB)	31	1

Notes

 $^{^{(1)} \ \ \, \}frac{dP_{tot}}{dT_J} \! < \! \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB

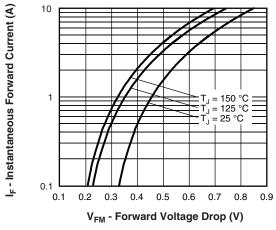


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

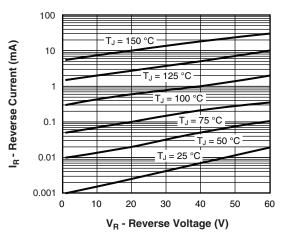


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

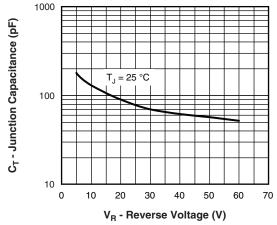


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

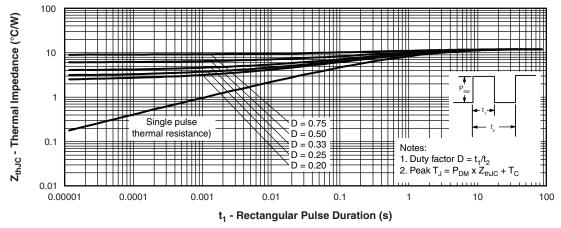


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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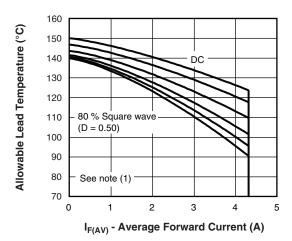


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

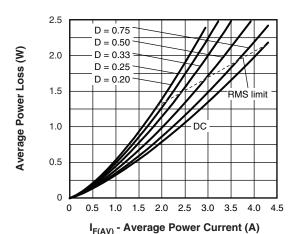


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

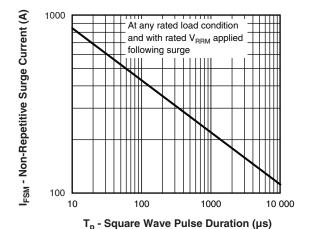


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

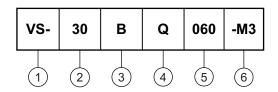
(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

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ORDERING INFORMATION TABLE





Vishay Semiconductors product suffix

2 - Current rating

3 - B = SMC

4 - Q = Schottky "Q" series

5 - Voltage rating (060 = 60 V)

6 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	EED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30BQ060-M3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95402</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information <u>www.vishay.com/doc?95404</u>				





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