74HC40103

8-bit synchronous binary down counter

Rev. 6 — 26 March 2024 Product data sheet

1. General description

The 74HC40103 is an 8-bit synchronous down counter. It has control inputs for enabling or disabling the clock (CP), for clearing the counter to its maximum count and for presetting the counter either synchronously or asynchronously. In normal operation, the counter is decremented by one count on each positive-going transition of the clock (CP). Counting is inhibited when the terminal enable input ($\overline{TE}$) is HIGH. The terminal count output ($\overline{TC}$) goes LOW when the count reaches zero if $\overline{TE}$ is LOW, and remains LOW for one full clock period. When the synchronous preset enable input ($\overline{PE}$) is LOW, data at the jam input (P0 to P7) is clocked into the counter on the next positive-going clock transition regardless of the state of $\overline{TE}$. When the asynchronous preset enable input ($\overline{PL}$) is LOW, data at the jam input (P0 to P7) is asynchronously forced into the counter regardless of the state of $\overline{PE}$, $\overline{TE}$, or CP. The jam inputs (P0 to P7) represent a single 8-bit binary word. When the master reset input ($\overline{MR}$) is LOW, the counter is asynchronously cleared to its maximum count (decimal 255) regardless of the state of any other input. If all control inputs except $\overline{TE}$ are HIGH at the time of zero count, the counters will jump to the maximum count, giving a counting sequence of 256 clock pulses long. Device may be cascaded using the $\overline{TE}$ input and the TC output, in either a synchronous or ripple mode. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}.

2. Features and benefits

- Cascadable
- Synchronous or asynchronous preset
- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low-power dissipation
- High noise immunity
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Complies with JEDEC standard no. 7A
- CMOS input levels
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +80 °C and from -40 °C to +125 °C

3. Applications

- Divide-by-n counters
- Programmable timers
- Interrupt timers
- Cycle/program counters.

4. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74HC40103D	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1
74HC40103PW	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	SOT403-1

5. Functional diagram

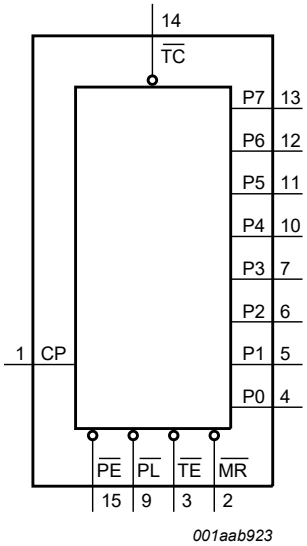


Fig. 1. Functional diagram

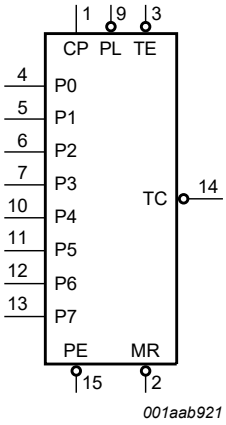


Fig. 2. Logic symbol

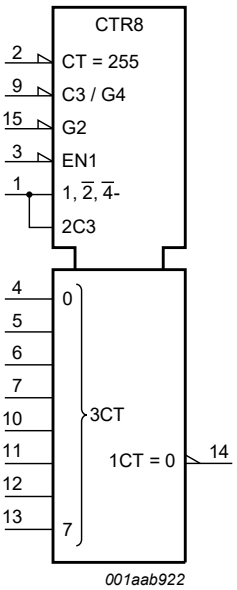


Fig. 3. IEC logic symbol

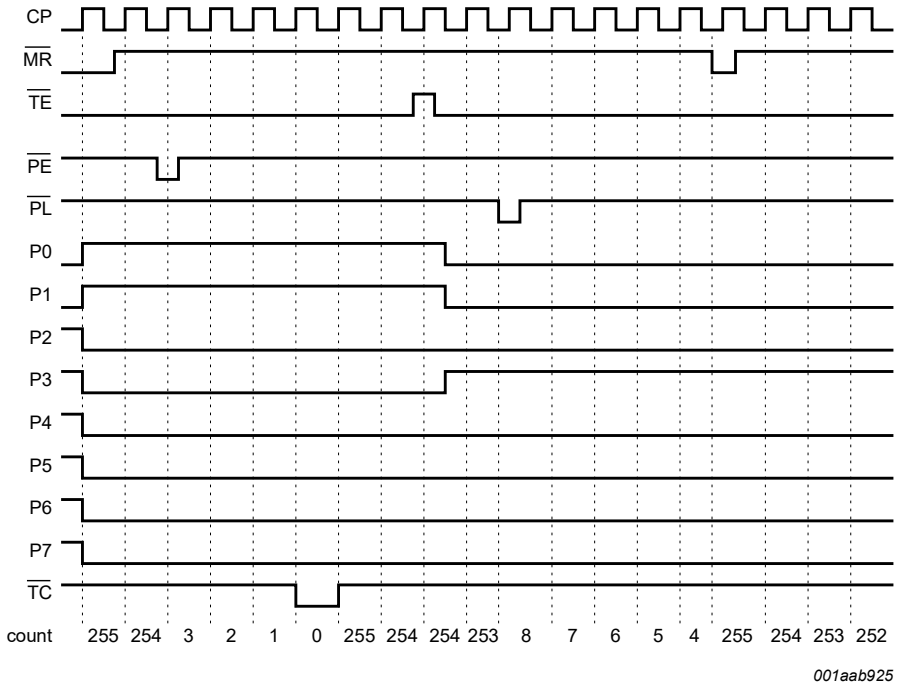


Fig. 4. Timing diagram

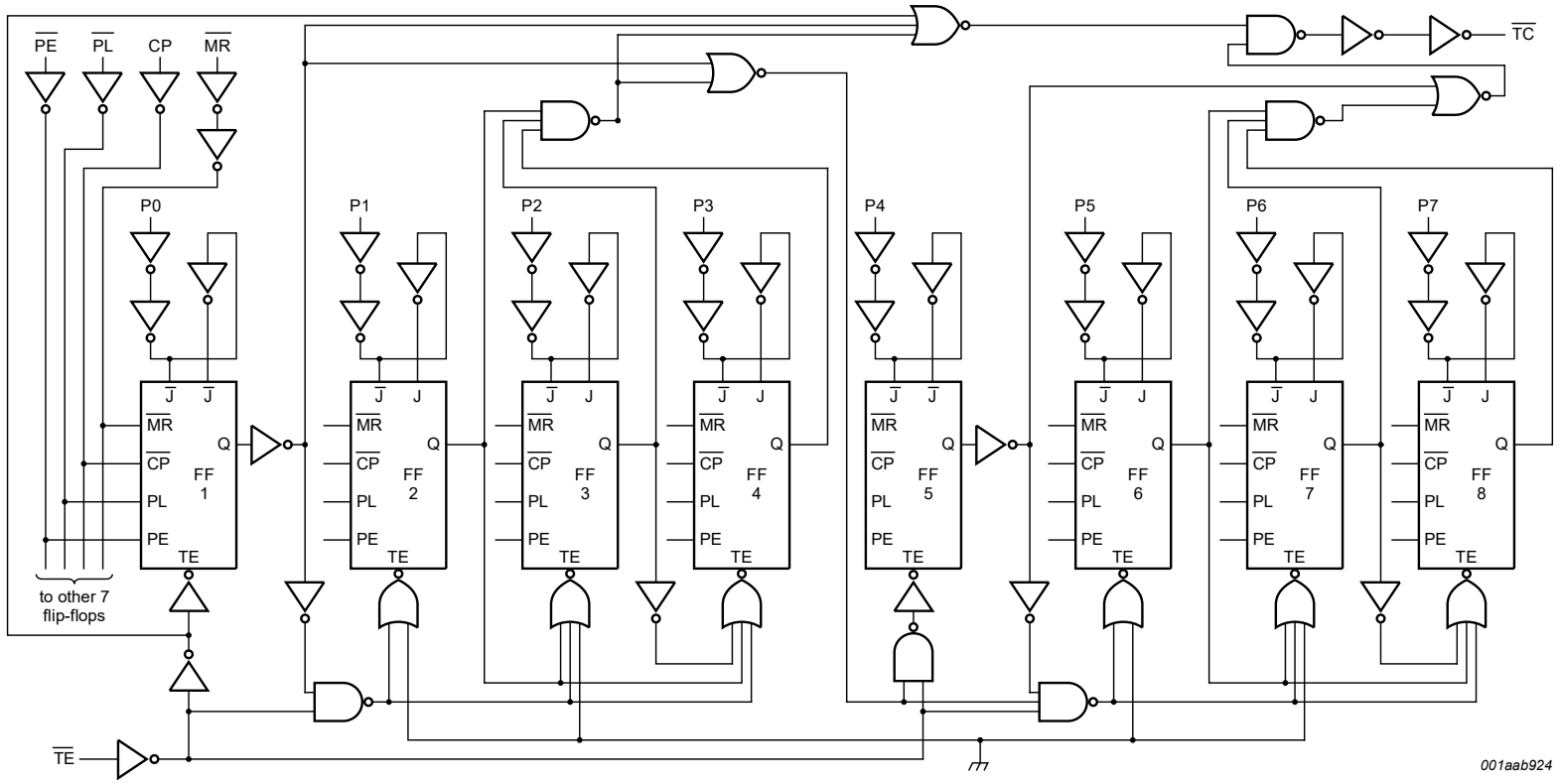
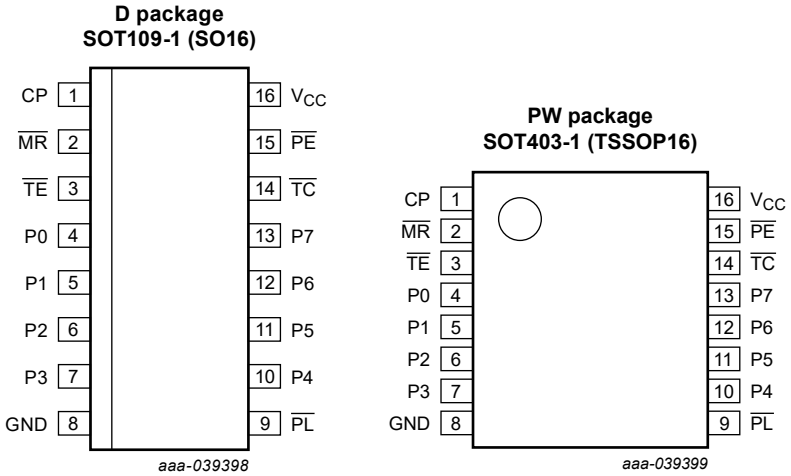


Fig. 5. Logic diagram

6. Pinning information

6.1. Pinning



6.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
CP	1	clock input (LOW-to-HIGH, edge-triggered)
$\overline{MR}$	2	asynchronous master reset input (active LOW)
$\overline{TE}$	3	terminal enable input (active LOW)
P0	4	jam input 0
P1	5	jam input 1
P2	6	jam input 2
P3	7	jam input 3
GND	8	ground (0 V)
$\overline{PL}$	9	asynchronous preset enable input (active LOW)
P4	10	jam input 4
P5	11	jam input 5
P5	12	jam input 6
P7	13	jam input 7
$\overline{TC}$	14	terminal count output (active LOW)
$\overline{PE}$	15	synchronous preset enable input (active LOW)
V_{CC}	16	positive supply voltage

7. Functional description

Table 3. Function table

H = HIGH voltage level;L = LOW voltage level;X = don't care.

Control inputs				Preset mode	Action [1]
$\overline{MR}$	$\overline{PL}$	$\overline{PE}$	$\overline{TE}$		
L	X	X	X	asynchronous	clear to maximum count
H	L	X	X	asynchronous	preset asynchronously
	H	L	X	synchronous	preset on next LOW-to HIGH clock transition
		H	L	synchronous	count down
			H	synchronous	inhibit counter

[1] Clock connected to CP.
Synchronous operation: changes occur on the LOW-to-HIGH CP transition.
Jam inputs: MSD = P7, LSD = P0.

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CC}	supply voltage			-0.5	+7	V
I_{IK}	input clamping current	$V_I < -0.5$ V or $V_I > V_{CC} + 0.5$ V	[1]	-	±20	mA
I_{OK}	output clamping current	$V_O < -0.5$ V or $V_O > V_{CC} + 0.5$ V	[1]	-	±20	mA
I_O	output current	$V_O = -0.5$ V to $V_{CC} + 0.5$ V		-	±25	mA
I_{CC}	supply current			-	+50	mA
I_{GND}	ground current			-50	-	mA
T_{stg}	storage temperature			-65	+150	°C
P_{tot}	total power dissipation		[2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
[2] For SOT109-1 (SO16) package: P_{tot} derates linearly with 12.4 mW/K above 110 °C.
For SOT403-1 (TSSOP16) package: P_{tot} derates linearly with 8.5 mW/K above 91 °C.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CC}	supply voltage		2.0	5.0	6.0	V
V_I	input voltage		0	-	V_{CC}	V
V_O	output voltage		0	-	V_{CC}	V
$\Delta t/\Delta V$	input transition rise and fall rates	V_{CC} = 2.0 V	-	-	625	ns
		V_{CC} = 4.5 V	-	1.67	139	ns
		V_{CC} = 6.0 V	-	-	83	ns
T_{amb}	ambient temperature		-40	-	+125	°C

10. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = 25 °C						
V_{IH}	HIGH-level input voltage	V_{CC} = 2.0 V	1.5	1.2	-	V
		V_{CC} = 4.5 V	3.15	2.4	-	V
		V_{CC} = 6.0 V	4.2	3.2	-	V
V_{IL}	LOW-level input voltage	V_{CC} = 2.0 V	-	0.8	0.5	V
		V_{CC} = 4.5 V	-	2.1	1.35	V
		V_{CC} = 6.0 V	-	2.8	1.8	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}				
		I_O = -20 µA; V_{CC} = 2.0 V	1.9	2.0	-	V
		I_O = -20 µA; V_{CC} = 4.5 V	4.4	4.5	-	V
		I_O = -20 µA; V_{CC} = 6.0 V	5.9	6.0	-	V
		I_O = -4 mA; V_{CC} = 4.5 V	3.98	4.32	-	V
		I_O = -5.2 mA; V_{CC} = 6.0 V	5.48	5.81	-	V
V_{OL}	LOW-level output voltage	V_I = V_{IH} or V_{IL}				
		I_O = 20 µA; V_{CC} = 2.0 V	-	0	0.1	V
		I_O = 20 µA; V_{CC} = 4.5 V	-	0	0.1	V
		I_O = 20 µA; V_{CC} = 6.0 V	-	0	0.1	V
		I_O = 4 mA; V_{CC} = 4.5 V	-	0.15	0.26	V
		I_O = 5.2 mA; V_{CC} = 6.0 V	-	0.16	0.26	V
I_I	input leakage current	V_I = V_{CC} or GND; V_{CC} = 6.0 V	-	-	±0.1	µA
I_{CC}	supply current	V_I = V_{CC} or GND; I_O = 0 A; V_{CC} = 6.0 V	-	-	8.0	µA
C_I	input capacitance		-	3.5	-	pF
T_{amb} = -40 °C to +85 °C						
V_{IH}	HIGH-level input voltage	V_{CC} = 2.0 V	1.5	-	-	V
		V_{CC} = 4.5 V	3.15	-	-	V
		V_{CC} = 6.0 V	4.2	-	-	V
V_{IL}	LOW-level input voltage	V_{CC} = 2.0 V	-	-	0.5	V
		V_{CC} = 4.5 V	-	-	1.35	V
		V_{CC} = 6.0 V	-	-	1.8	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}				
		I_O = -20 µA; V_{CC} = 2.0 V	1.9	-	-	V
		I_O = -20 µA; V_{CC} = 4.5 V	4.4	-	-	V
		I_O = -20 µA; V_{CC} = 6.0 V	5.9	-	-	V
		I_O = -4 mA; V_{CC} = 4.5 V	3.84	-	-	V
		I_O = -5.2 mA; V_{CC} = 6.0 V	5.34	-	-	V

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_O = 20 µA; V_{CC} = 2.0 V	-	-	0.1	V
		I_O = 20 µA; V_{CC} = 4.5 V	-	-	0.1	V
		I_O = 20 µA; V_{CC} = 6.0 V	-	-	0.1	V
		I_O = 4 mA; V_{CC} = 4.5 V	-	-	0.33	V
		I_O = 5.2 mA; V_{CC} = 6.0 V	-	-	0.33	V
I_I	input leakage current	$V_I = V_{CC}$ or GND; V_{CC} = 6.0 V	-	-	±1.0	µA
I_{CC}	supply current	$V_I = V_{CC}$ or GND; I_O = 0 A; V_{CC} = 6.0 V	-	-	80	µA
T_{amb} = -40 °C to +125 °C						
V_{IH}	HIGH-level input voltage	V_{CC} = 2.0 V	1.5	-	-	V
		V_{CC} = 4.5 V	3.15	-	-	V
		V_{CC} = 6.0 V	4.2	-	-	V
V_{IL}	LOW-level input voltage	V_{CC} = 2.0 V	-	-	0.5	V
		V_{CC} = 4.5 V	-	-	1.35	V
		V_{CC} = 6.0 V	-	-	1.8	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_O = -20 µA; V_{CC} = 2.0 V	1.9	-	-	V
		I_O = -20 µA; V_{CC} = 4.5 V	4.4	-	-	V
		I_O = -20 µA; V_{CC} = 6.0 V	5.9	-	-	V
		I_O = -4 mA; V_{CC} = 4.5 V	3.7	-	-	V
		I_O = -5.2 mA; V_{CC} = 6.0 V	5.2	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_O = 20 µA; V_{CC} = 2.0 V	-	-	0.1	V
		I_O = 20 µA; V_{CC} = 4.5 V	-	-	0.1	V
		I_O = 20 µA; V_{CC} = 6.0 V	-	-	0.1	V
		I_O = 4 mA; V_{CC} = 4.5 V	-	-	0.4	V
		I_O = 5.2 mA; V_{CC} = 6.0 V	-	-	0.4	V
I_I	input leakage current	$V_I = V_{CC}$ or GND; V_{CC} = 6.0 V	-	-	±1.0	µA
I_{CC}	supply current	$V_I = V_{CC}$ or GND; I_O = 0 A; V_{CC} = 6.0 V	-	-	160	µA

11. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; $t_r = t_f$ = 6 ns; C_L = 50 pF; for test circuit see Fig. 12.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = 25 °C						
t_{pd}	propagation delay	CP to $\overline{TC}$; see Fig. 6 [1]				
		V_{CC} = 2.0 V	-	96	300	ns
		V_{CC} = 4.5 V	-	35	60	ns
		V_{CC} = 6.0 V	-	28	51	ns
		V_{CC} = 5.0 V; C_L = 15 pF	-	30	-	ns
		$\overline{TE}$ to $\overline{TC}$; see Fig. 7				
		V_{CC} = 2.0 V	-	50	175	ns
		V_{CC} = 4.5 V	-	18	35	ns
		V_{CC} = 6.0 V	-	14	30	ns
		$\overline{PL}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	102	315	ns
		V_{CC} = 4.5 V	-	37	63	ns
		V_{CC} = 6.0 V	-	30	53	ns
t_{PHL}	HIGH to LOW propagation delay	$\overline{MR}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	83	275	ns
		V_{CC} = 4.5 V	-	30	55	ns
		V_{CC} = 6.0 V	-	24	47	ns
t_t	transition time	see Fig. 7 [2]				
		V_{CC} = 2.0 V	-	19	75	ns
		V_{CC} = 4.5 V	-	7	15	ns
		V_{CC} = 6.0 V	-	6	13	ns
t_W	pulse width	CP HIGH or LOW; see Fig. 6				
		V_{CC} = 2.0 V	165	22	-	ns
		V_{CC} = 4.5 V	33	8	-	ns
		V_{CC} = 6.0 V	28	6	-	ns
		$\overline{MR}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	125	39	-	ns
		V_{CC} = 4.5 V	25	14	-	ns
		V_{CC} = 6.0 V	21	11	-	ns
		$\overline{PL}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	125	33	-	ns
		V_{CC} = 4.5 V	25	12	-	ns
		V_{CC} = 6.0 V	21	10	-	ns
t_{rec}	recovery time	$\overline{MR}$ to CP, $\overline{PL}$ to CP; see Fig. 9				
		V_{CC} = 2.0 V	50	14	-	ns
		V_{CC} = 4.5 V	10	5	-	ns
		V_{CC} = 6.0 V	9	4	-	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_{su}	set-up time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	75	22	-	ns
		V_{CC} = 4.5 V	15	8	-	ns
		V_{CC} = 6.0 V	13	6	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	150	44	-	ns
		V_{CC} = 4.5 V	30	16	-	ns
		V_{CC} = 6.0 V	26	13	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	75	22	-	ns
		V_{CC} = 4.5 V	15	8	-	ns
		V_{CC} = 6.0 V	13	6	-	ns
t_h	hold time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-14	-	ns
		V_{CC} = 4.5 V	0	-5	-	ns
		V_{CC} = 6.0 V	0	-4	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	0	-30	-	ns
		V_{CC} = 4.5 V	0	-11	-	ns
		V_{CC} = 6.0 V	0	-9	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-17	-	ns
		V_{CC} = 4.5 V	0	-6	-	ns
		V_{CC} = 6.0 V	0	-5	-	ns
f_{max}	maximum frequency	see Fig. 6				
		V_{CC} = 2.0 V	3.0	10	-	MHz
		V_{CC} = 4.5 V	15	29	-	MHz
		V_{CC} = 6.0 V	18	35	-	MHz
		V_{CC} = 5.0 V; C_L = 15 pF	-	32	-	MHz
C_{PD}	power dissipation capacitance	V_I = GND to V_{CC} [3]	-	24	-	pF
T_{amb} = -40 °C to +85 °C						
t_{pd}	propagation delay	CP to $\overline{TC}$; see Fig. 6 [1]				
		V_{CC} = 2.0 V	-	-	375	ns
		V_{CC} = 4.5 V	-	-	75	ns
		V_{CC} = 6.0 V	-	-	64	ns
		$\overline{TE}$ to $\overline{TC}$; see Fig. 7				
		V_{CC} = 2.0 V	-	-	220	ns
		V_{CC} = 4.5 V	-	-	44	ns
		V_{CC} = 6.0 V	-	-	37	ns
		$\overline{PL}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	-	395	ns
		V_{CC} = 4.5 V	-	-	79	ns
		V_{CC} = 6.0 V	-	-	40	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_{PHL}	HIGH to LOW propagation delay	$\overline{MR}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	-	345	ns
		V_{CC} = 4.5 V	-	-	69	ns
		V_{CC} = 6.0 V	-	-	59	ns
t_t	transition time	see Fig. 7 [2]				
		V_{CC} = 2.0 V	-	-	95	ns
		V_{CC} = 4.5 V	-	-	19	ns
		V_{CC} = 6.0 V	-	-	16	ns
t_W	pulse width	CP HIGH or LOW; see Fig. 6				
		V_{CC} = 2.0 V	205	-	-	ns
		V_{CC} = 4.5 V	41	-	-	ns
		V_{CC} = 6.0 V	35	-	-	ns
		$\overline{MR}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	155	-	-	ns
		V_{CC} = 4.5 V	31	-	-	ns
		V_{CC} = 6.0 V	26	-	-	ns
		$\overline{PL}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	155	-	-	ns
		V_{CC} = 4.5 V	31	-	-	ns
		V_{CC} = 6.0 V	26	-	-	ns
t_{rec}	recovery time	$\overline{MR}$ to CP, $\overline{PL}$ to CP; see Fig. 9				
		V_{CC} = 2.0 V	65	-	-	ns
		V_{CC} = 4.5 V	13	-	-	ns
		V_{CC} = 6.0 V	11	-	-	ns
t_{su}	set-up time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	95	-	-	ns
		V_{CC} = 4.5 V	19	-	-	ns
		V_{CC} = 6.0 V	16	-	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	190	-	-	ns
		V_{CC} = 4.5 V	38	-	-	ns
		V_{CC} = 6.0 V	33	-	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	95	-	-	ns
		V_{CC} = 4.5 V	19	-	-	ns
		V_{CC} = 6.0 V	16	-	-	ns

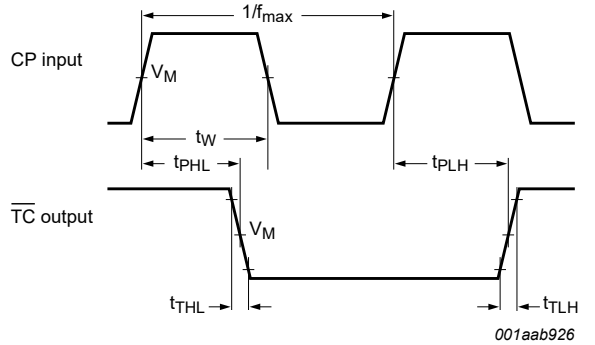
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_h	hold time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns
f_{max}	maximum frequency	see Fig. 6				
		V_{CC} = 2.0 V	2.4	-	-	MHz
		V_{CC} = 4.5 V	12	-	-	MHz
		V_{CC} = 6.0 V	14	-	-	MHz
T_{amb} = -40 °C to +125 °C						
t_{pd}	propagation delay	CP to $\overline{TC}$; see Fig. 6 [1]				
		V_{CC} = 2.0 V	-	-	450	ns
		V_{CC} = 4.5 V	-	-	90	ns
		V_{CC} = 6.0 V	-	-	77	ns
		$\overline{TE}$ to $\overline{TC}$; see Fig. 7				
		V_{CC} = 2.0 V	-	-	265	ns
		V_{CC} = 4.5 V	-	-	53	ns
		V_{CC} = 6.0 V	-	-	45	ns
		$\overline{PL}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	-	475	ns
		V_{CC} = 4.5 V	-	-	95	ns
		V_{CC} = 6.0 V	-	-	81	ns
t_{PHL}	HIGH to LOW propagation delay	$\overline{MR}$ to $\overline{TC}$; see Fig. 8				
		V_{CC} = 2.0 V	-	-	415	ns
		V_{CC} = 4.5 V	-	-	83	ns
		V_{CC} = 6.0 V	-	-	71	ns
t_t	transition time	see Fig. 7 [2]				
		V_{CC} = 2.0 V	-	-	110	ns
		V_{CC} = 4.5 V	-	-	22	ns
		V_{CC} = 6.0 V	-	-	19	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_W	pulse width	CP HIGH or LOW; see Fig. 6				
		V_{CC} = 2.0 V	250	-	-	ns
		V_{CC} = 4.5 V	50	-	-	ns
		V_{CC} = 6.0 V	43	-	-	ns
		$\overline{MR}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	190	-	-	ns
		V_{CC} = 4.5 V	38	-	-	ns
		V_{CC} = 6.0 V	32	-	-	ns
		$\overline{PL}$ LOW; see Fig. 8				
		V_{CC} = 2.0 V	190	-	-	ns
		V_{CC} = 4.5 V	38	-	-	ns
		V_{CC} = 6.0 V	32	-	-	ns
t_{rec}	recovery time	$\overline{MR}$ to CP, $\overline{PL}$ to CP; see Fig. 9				
		V_{CC} = 2.0 V	75	-	-	ns
		V_{CC} = 4.5 V	15	-	-	ns
		V_{CC} = 6.0 V	13	-	-	ns
t_{su}	set-up time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	110	-	-	ns
		V_{CC} = 4.5 V	22	-	-	ns
		V_{CC} = 6.0 V	19	-	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	225	-	-	ns
		V_{CC} = 4.5 V	45	-	-	ns
		V_{CC} = 6.0 V	38	-	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	110	-	-	ns
		V_{CC} = 4.5 V	22	-	-	ns
		V_{CC} = 6.0 V	19	-	-	ns
t_h	hold time	$\overline{PE}$ to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns
		$\overline{TE}$ to CP; see Fig. 11				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns
		Pn to CP; see Fig. 10				
		V_{CC} = 2.0 V	0	-	-	ns
		V_{CC} = 4.5 V	0	-	-	ns
		V_{CC} = 6.0 V	0	-	-	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
f_{max}	maximum frequency	see Fig. 6				
		V_{CC} = 2.0 V	2.0	-	-	MHz
		V_{CC} = 4.5 V	10	-	-	MHz
		V_{CC} = 6.0 V	12	-	-	MHz

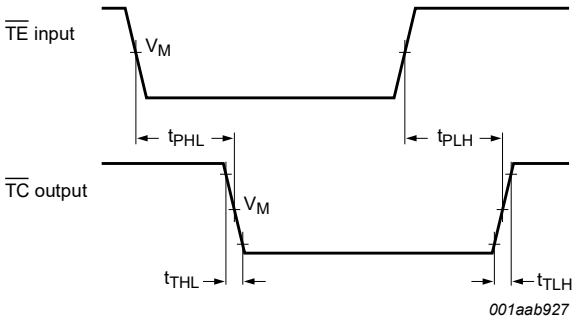
[1] t_{pd} is the same as t_{PHL}, t_{PLH}.
[2] t_t is the same as t_{THL}, t_{TLH}.
[3] C_{PD} is used to determine the dynamic power dissipation (P_D in µW).
$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$ where:
f_i = input frequency in MHz;
f_o = output frequency in MHz;
C_L = output load capacitance in pF;
V_{CC} = supply voltage in V;
N = number of inputs switching;
$\sum(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

11.1. Waveforms and test circuit



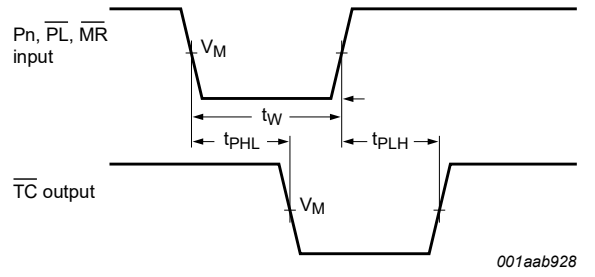
$V_M = 0.5 \times V_I$

Fig. 6. Clock input (CP) to $\overline{TC}$ propagation delays, clock pulse width, output transition times and the maximum clock pulse frequency



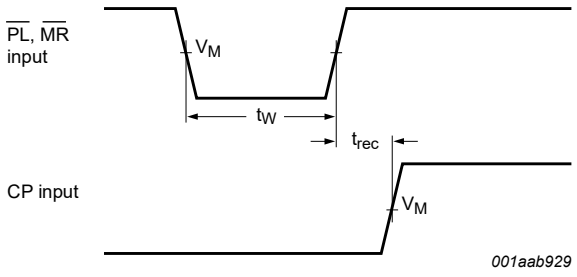
$V_M = 0.5 \times V_I$

Fig. 7. $\overline{TE}$ to $\overline{TC}$ propagation delays



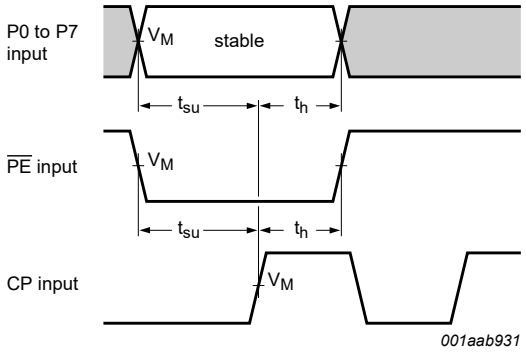
$V_M = 0.5 \times V_I$

Fig. 8. $\overline{PL}$, $\overline{MR}$, Pn to $\overline{TC}$ propagation delays



$V_M = 0.5 \times V_I$

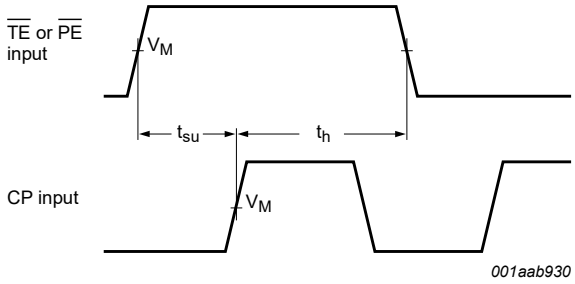
Fig. 9. Removal time for $\overline{MR}$ and $\overline{PL}$



The shaded areas indicate when the input is permitted to change for predictable output performance.

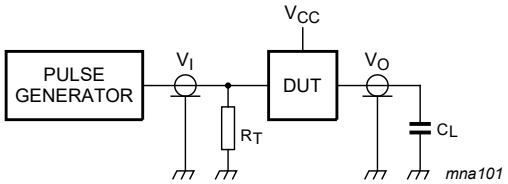
$V_M = 0.5 \times V_I$

Fig. 10. Hold and set-up times for Pn, $\overline{PE}$ to CP



$V_M = 0.5 \times V_I$

Fig. 11. Hold and set-up times for $\overline{MR}$ or $\overline{PE}$ to CP



Test data is given in Table 8.

Definitions for test circuit:

R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator;

C_L = Load capacitance including jig and probe capacitance.

Fig. 12. Test circuit for measuring switching times

Table 8. Test data

Supply	Input		Load
V_{CC}	V_I	t_r, t_f	C_L
2.0 V	V_{CC}	6 ns	50 pF
4.5 V	V_{CC}	6 ns	50 pF
6.0 V	V_{CC}	6 ns	50 pF
5.0 V	V_{CC}	6 ns	15 pF

12. Application information

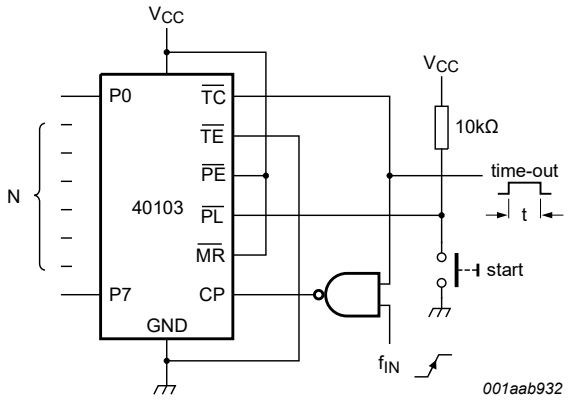


Fig. 13. Programmable timer

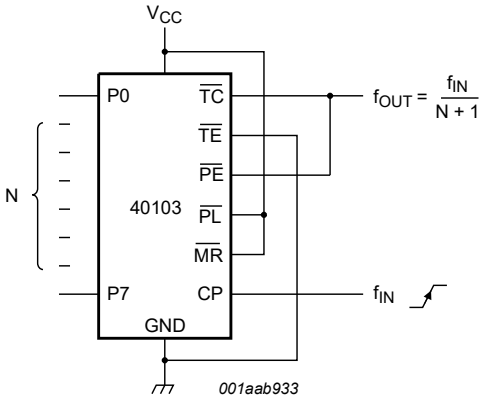
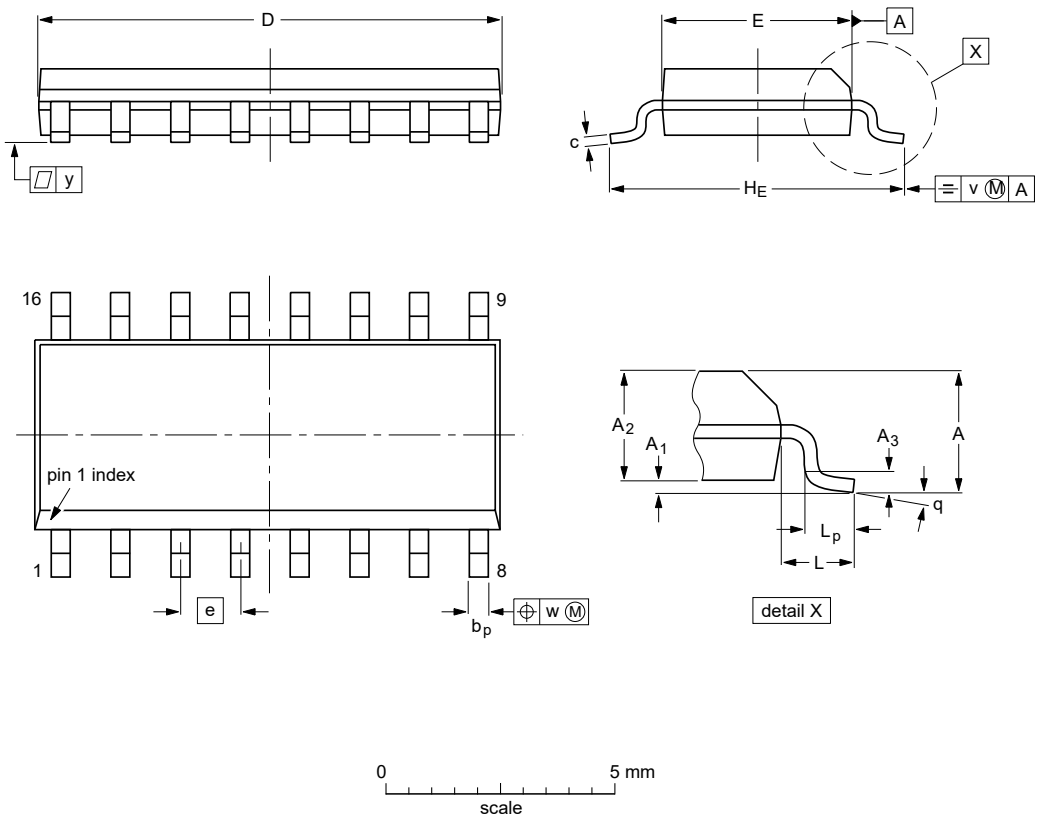


Fig. 14. Divide-by-N counter

13. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm **SOT109-1**



Dimensions (inch dimensions are derived from the original mm dimensions)

Unit		A	A_1	A_2	A_3	b_p	c	$D^{(1)}$	$E^{(1)}$	e	H_E	L	L_p	v	w	y	θ
mm	max	1.75	0.25			0.51	0.25	10.0	4.0		6.2		1.27				8°
	nom				0.25					1.27		1.05		0.2	0.25	0.1	
	min		0.10	1.25		0.31	0.10	9.8	3.8		5.8		0.4				0°
inches	max	0.069	0.010			0.020	0.010	0.394	0.16		0.244		0.05				8°
	nom				0.01					0.05		0.041		0.008	0.01	0.004	
	min		0.004	0.049		0.012	0.004	0.386	0.15		0.228		0.016				0°

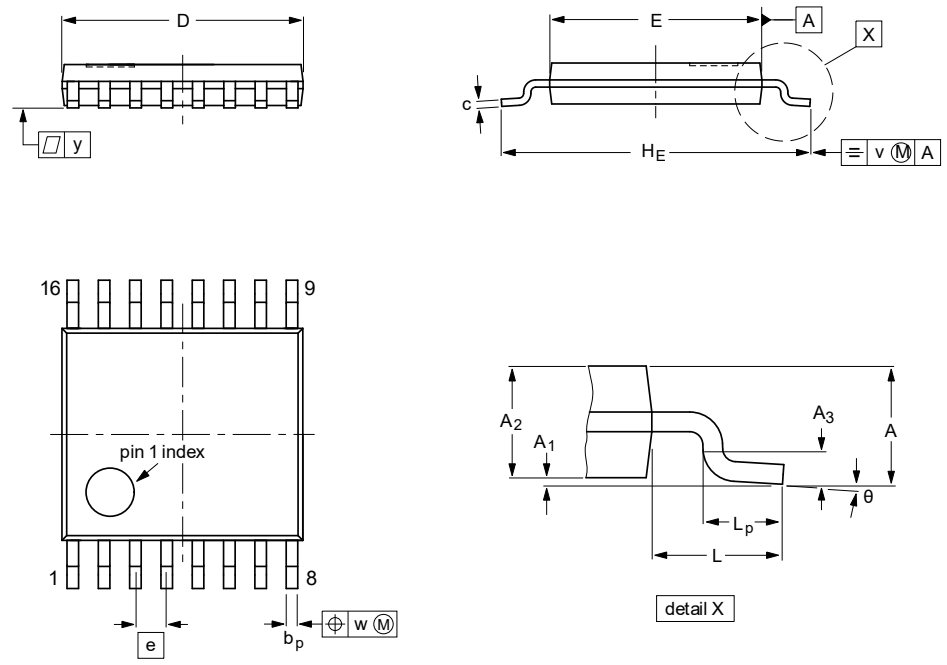
Note
1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

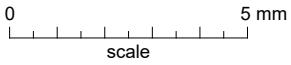
sot109-1_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT109-1		MS-012				~~03-02-19~~ 23-10-27

Fig. 15. Package outline SOT109-1 (SO16)

TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm **SOT403-1**





Dimensions (mm are the original dimensions)

Unit		A	A_1	A_2	A_3	b_p	c	$D^{(1)}$	$E^{(2)}$	e	H_E	L	L_p	v	w	y	θ
mm	max	1.20	0.15	1.05		0.30	0.2	5.1	4.5		6.6		0.75				8°
	nom				0.25					0.65		1		0.2	0.1	0.1	
	min		0.05	0.80		0.19	0.09	4.9	4.3		6.2		0.45				0°

Note
1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

sot403-1_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT403-1		MO-153				~~03-02-18~~ 23-10-27

Fig. 16. Package outline SOT403-1 (TSSOP16)

14. Abbreviations

Table 9. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model

15. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74HC40103 v.6	20240326	Product data sheet	-	74HC40103 v.5
Modifications:	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate. • Section 2: ESD specification updated according to the latest JEDEC standard. • Table 4: Derating values for P_{tot} total power dissipation updated. • Fig. 15, Fig. 16: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153			
74HC40103 v.5	20160421	Product data sheet	-	74HC40103 v.4
Modifications:	• Type number 74HC40103DB (SOT338-1) removed.			
74HC40103 v.4	20160127	Product data sheet	-	74HC40103 v.3
Modifications:	• Type number 74HC40103N (SOT38-4) removed.			
74HC40103 v.3	20041112	Product data sheet	-	74HC_HCT40103_CNV v.2
Modifications:	• The format of this data sheet has been redesigned to comply with the current presentation and information standard of Philips Semiconductors. • Removed type number 74HCT40103. • Inserted family specification.			
74HC_HCT40103_CNV v.2	19970918	Product specification	-	74HC_HCT40103 v.1
74HC_HCT40103 v.1	19901201	Product specification	-	-

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 26 March 2024