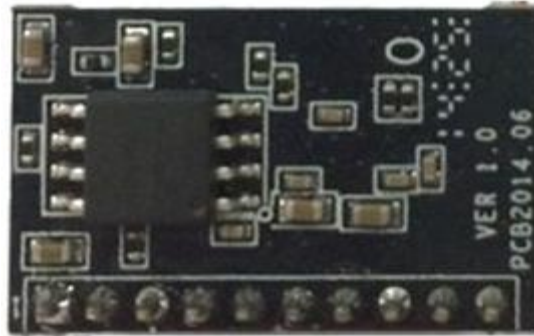


## HF-LPT100F & HF-LPT100S & HF-LPT100

### Low Power Tiny WiFi Module Difference



#### Overview of Difference

- ✧ **LPT100F** Support Maximum **5 Channel** PWM output
- ✧ **LPT100S** Support Maximum **4 Channel** PWM output.
- ✧ **LPT100** Support Maximum **3 Channel** PWM Output.
- ✧ **LPT100F** Support **Copper Line Antenna or IPEX conector** Output.
- ✧ **LPT100S** Support **Copper Line Antenna** Output.
- ✧ **LPT100** Support **IPEX connector**.
- ✧ The Other Hardware and Software Feature of **LPT100/LPT100S/LPT100F** is the same. We recommend to use **LPT100F** for design due to more IO control for **SDK(Software Develop Kit)**.

# 1. PRODUCT OVERVIEW

## 1.1. Hardware Introduction

### 1.1.1. Pins Definition

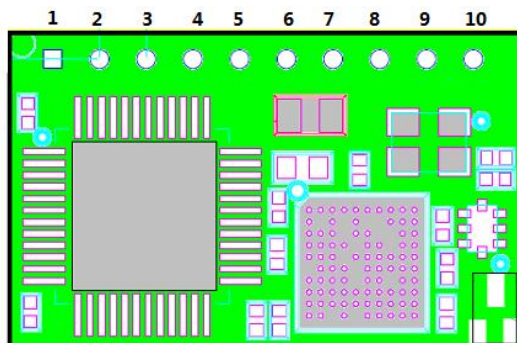


Figure 1. HF-LPT100F Pins Map

Table 2 HF-LPT100F Pins Definition

Pin	Description	Net Name	Signal Type	Comments
1	Ground	GND	Power	
2	+3.3V Power	DVDD	Power	3.3V@250mA
3	Restore Configuration	nReload	I,PU	Detailed functions see <Notes>
4	<b>PWM Channel 5</b>	<b>PWM_5</b>	<b>I/O</b>	<b>Can be configured as PWM output/GPIO23</b>
5	UART0	UART0_RX	I	GPIO5, No connect if not use.
6	UART0	UART0_TX	O	GPIO6, No connect if not use.
7	<b>PWM Channel 4</b>	<b>PWM_4</b>	<b>I/O</b>	<b>Can be configured as PWM output/GPIO20</b>
8	PWM Channel 3	PWM_3	I/O	Can be configured as WPS/GPIO18 No connect if not use.
9	PWM Channel 2	PWM_2	I/O	Can be configured as nReady/GPIO12. No connect if not use.
10	PWM Channel 1	PWM_1	I/O	Can be configured as nLink/GPIO11. Detailed functions see <Notes>

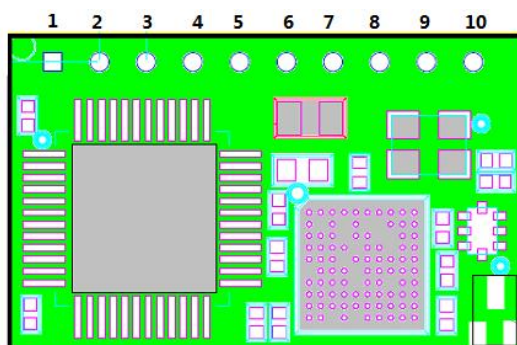


Figure 2. HF-LPT100S Pins Map

Table 2 HF-LPT100S Pins Definition

Pin	Description	Net Name	Signal Type	Comments
1	Ground	GND	Power	
2	+3.3V Power	DVDD	Power	3.3V@250mA
3	Restore Configuration	nReload	I,PU	Detailed functions see <Notes>
4	<b>Module Reset</b>	<b>EXT_RESETn</b>	<b>I,PU</b>	<b>“Low” effective reset input.</b>
5	UART0	UART0_RX	I	GPIO5, No connect if not use.
6	UART0	UART0_TX	O	GPIO6, No connect if not use.
7	<b>PWM Channel 4</b>	<b>PWM_4</b>	<b>I/O</b>	<b>Can be configured as PWM output,GPIO20</b>
8	PWM Channel 3	PWM_3	I/O	Can be configured as WPS/GPIO18 No connect if not use.
9	PWM Channel 2	PWM_2	I/O	Can be configured as nReady/GPIO12. No connect if not use.
10	PWM Channel 1	PWM_1	I/O	Can be configured as nLink/GPIO11. Detailed functions see <Notes>

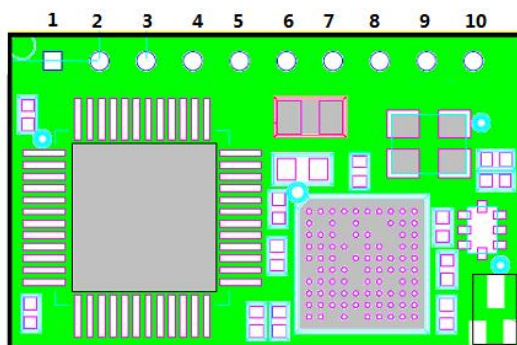


Figure 3. HF-LPT100 Pins Map

Table 2 HF-LPT100 Pins Definition

Pin	Description	Net Name	Signal Type	Comments
1	Ground	GND	Power	
2	+3.3V Power	DVDD	Power	3.3V@250mA
3	Restore Configuration	nReload	I,PU	<a href="#">Detailed functions see &lt;Notes&gt;</a>
4	<b>Module Reset</b>	<b>EXT_RESETn</b>	<b>I,PU</b>	<b>“Low” effective hardware reset input.</b>
5	UART0	UART0_RX	I	GPIO5, No connect if not use.
6	UART0	UART0_TX	O	GPIO6, No connect if not use.
7	<b>Power Control Switch</b>	<b>PWR_SW</b>	<b>I,PU</b>	<b>(Reserved)</b>
8	PWM Channel 3	PWM_3	I/O	Can be configured as WPS/GPIO18 No connect if not use.
9	PWM Channel 2	PWM_2	I/O	Can be configured as nReady/GPIO12. No connect if not use.
10	PWM Channel 1	PWM_1	I/O	Can be configured as nLink/GPIO11. <a href="#">Detailed functions see &lt;Notes&gt;</a>

**1.1.2. Order Information**

Base on customer detailed requirement, HF-LPT100 series modules provide different variants and physical type for detailed application.

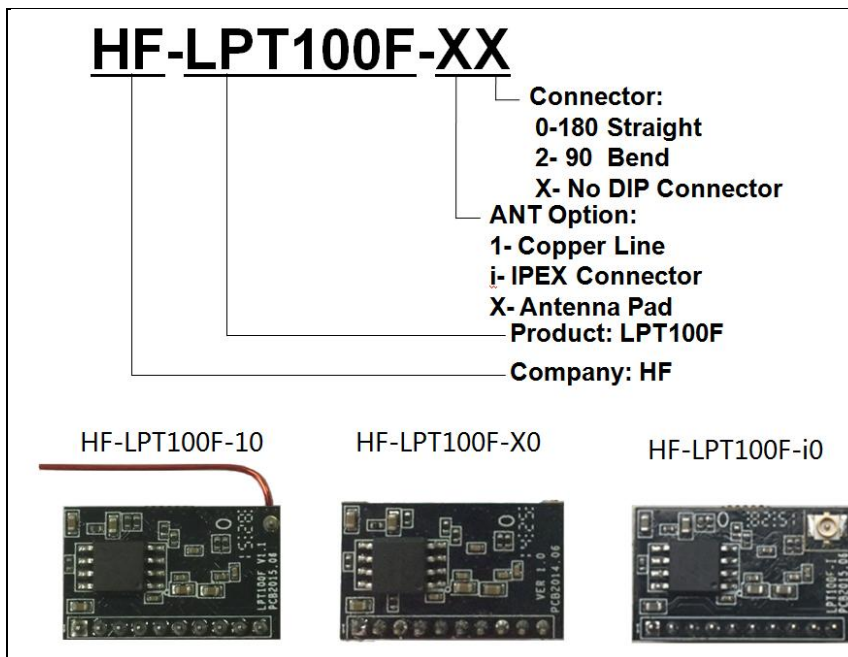


Figure 4. HF-LPT100F Order Information

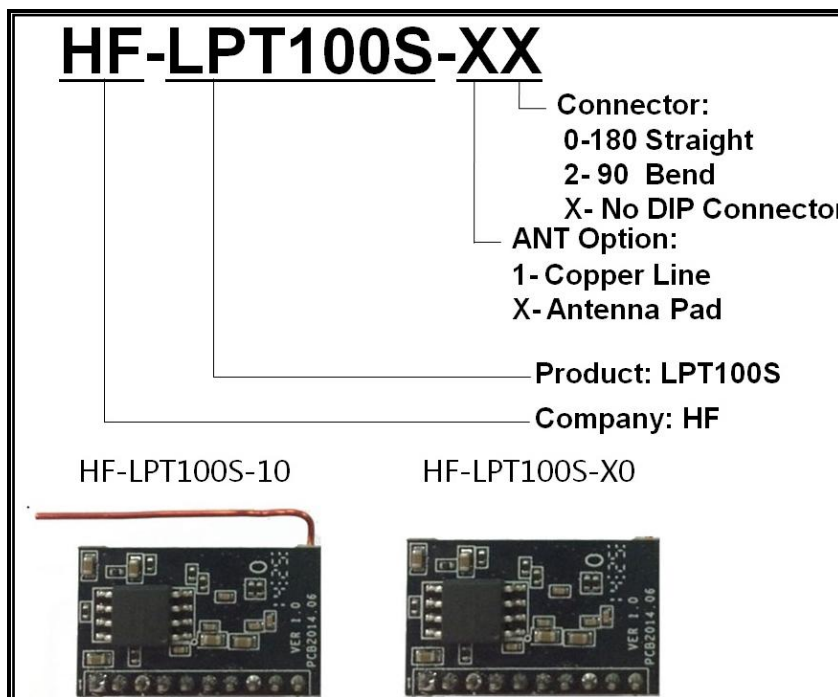


Figure 5. HF-LPT100S Order Information

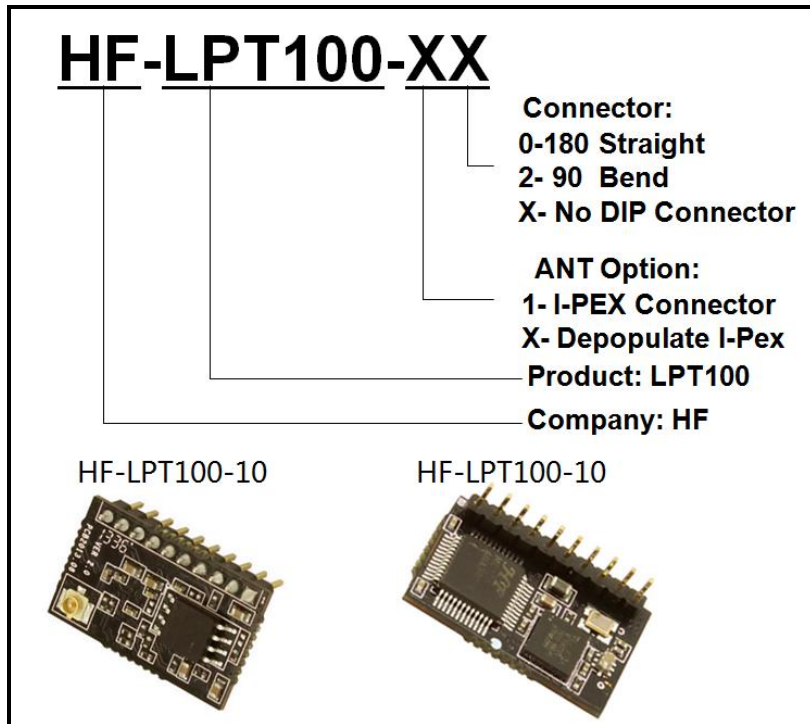


Figure 6. HF-LPT100 Order Information

### 1.1.3. Module Application

Please refer to LPT100 User Manual for module application.