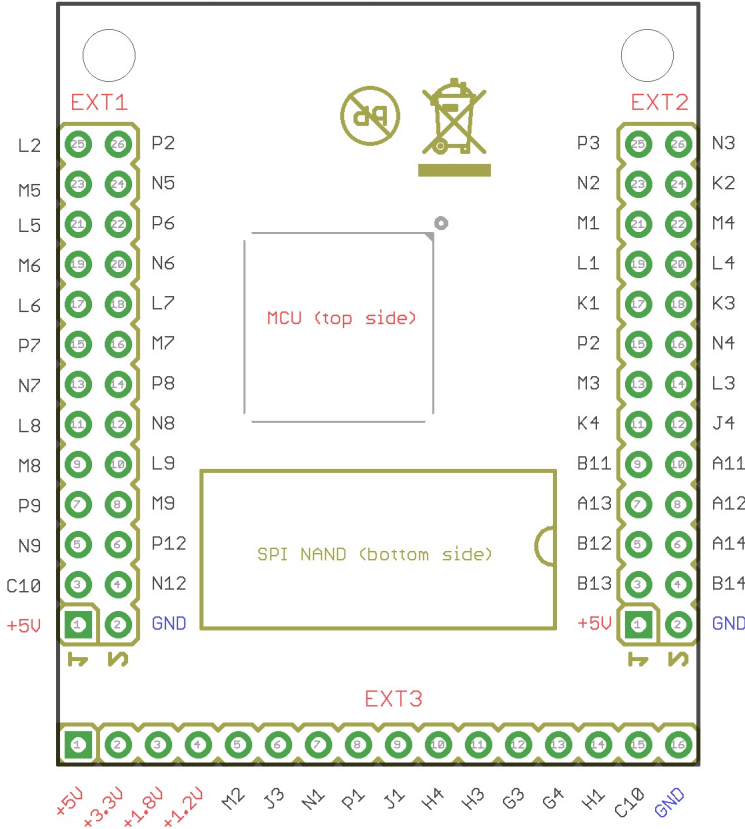


RT5350F-0Linuxino

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This is top view.



You can find the board's schematics at our GitHub pages.

EXT1

1. +5V
2. GND
3. PORSTN - C10
4. UPHY0_PADM - N12
5. RXIM4 - N9
6. UPHY0_PADP - P12
7. RXIP4 - P9
8. TXOM4 - M9
9. RXIM3 - M8
10. TXOP4 - L9
11. RXIP3 - L8
12. TXOM3 - N8
12. RXIM2 - N7
14. TXOP3 - P8
15. RXIP2 - P7
16. TXOM2 - M7
17. RXIM1 - L6
18. TXOP2 - L7
19. RXIP1 - M6
20. TXIM0 - N6
21. RXIM0 - L5
22. TXOP1 - P6
23. RXIP0 - M5
24. TXOM0 - N5
25. SPI_CS1 - L2
26. TXOP0 - P2

EXT2

1. +5V
2. GND
3. I2C_SD - B13
4. I2C_SCLK - B14
5. GPI00 - B12
6. EJTAG_TDI - A14
7. EJTAG_TMS - A13
8. EJTAG_TCK - A12
9. EJTAG_TDO - B11
10. EJTAG_TRSTN - A11
11. CTS_N - K4
12. DCD_N - J4
13. RIN - M3
14. DSR_N - L3
15. RTS_N - P2
16. DTR_N - N4
17. WLAN_LED - K1
18. LINK0 - K3
19. LINK1 - L1
20. LINK2 - L4
21. LINK3 - M1
22. LINK4 - M4
23. UART_TXD2 - N2
24. TXD - K2
25. UART_RXD2 - P3
26. RXD - N3

EXT3

1. +5V
2. UCC (+3.3V)
3. UPP (+1.8V)
4. VEE (+1.2V)
5. SPI_MISO - M2
6. SPI_MOSI - J3
7. SPI_CLK - N1
8. SPI_CS0 - P1
9. LGATE - J1
10. FB - H4
11. EXT_LDOO_1P2 - H3
12. LDOSEL - G3
13. COMP - G4
14. UGATE - H1
15. PORSTN - C10
16. GND