

3 Appendix: Circuit Diagrams for Evaluation Module Board

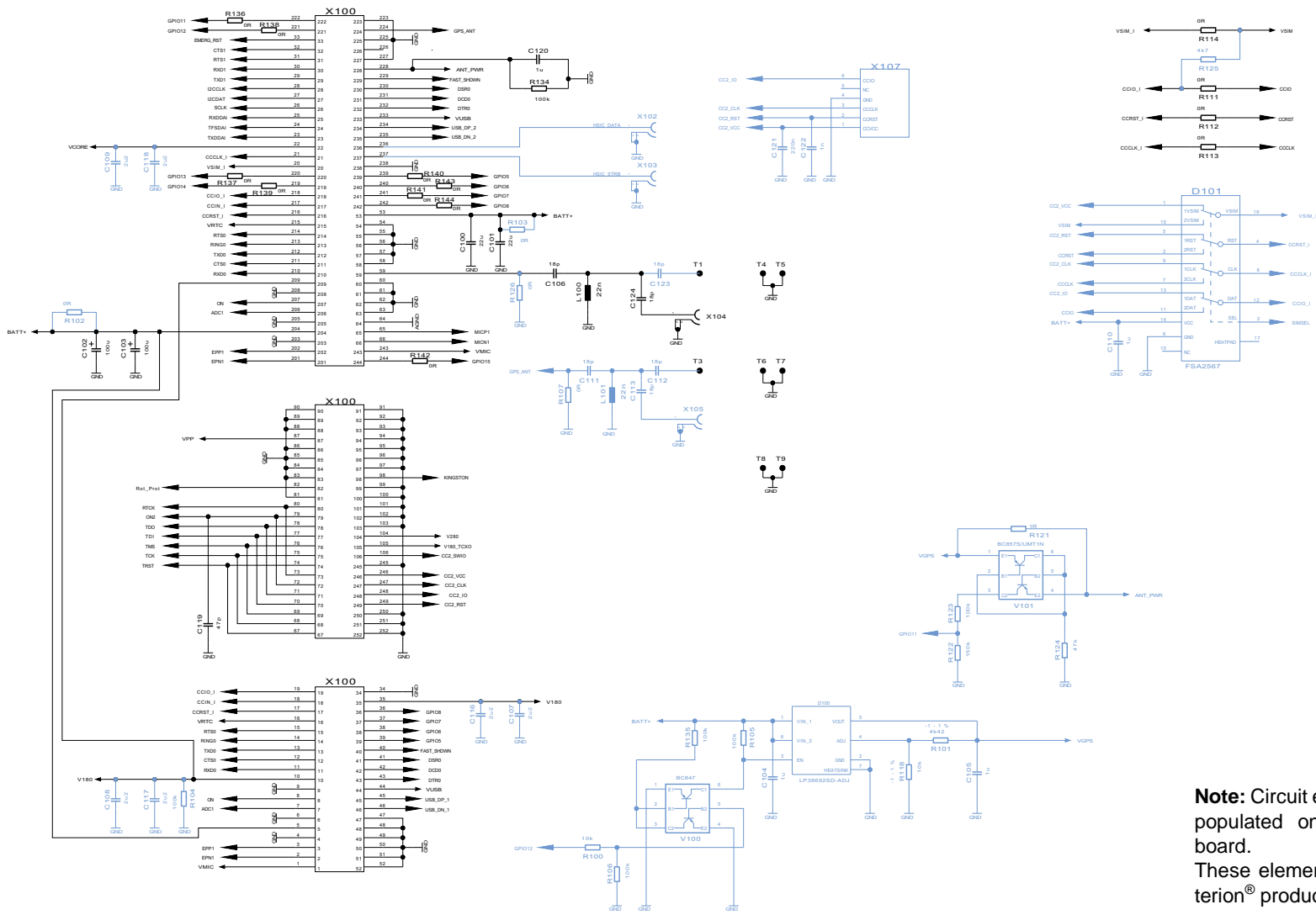
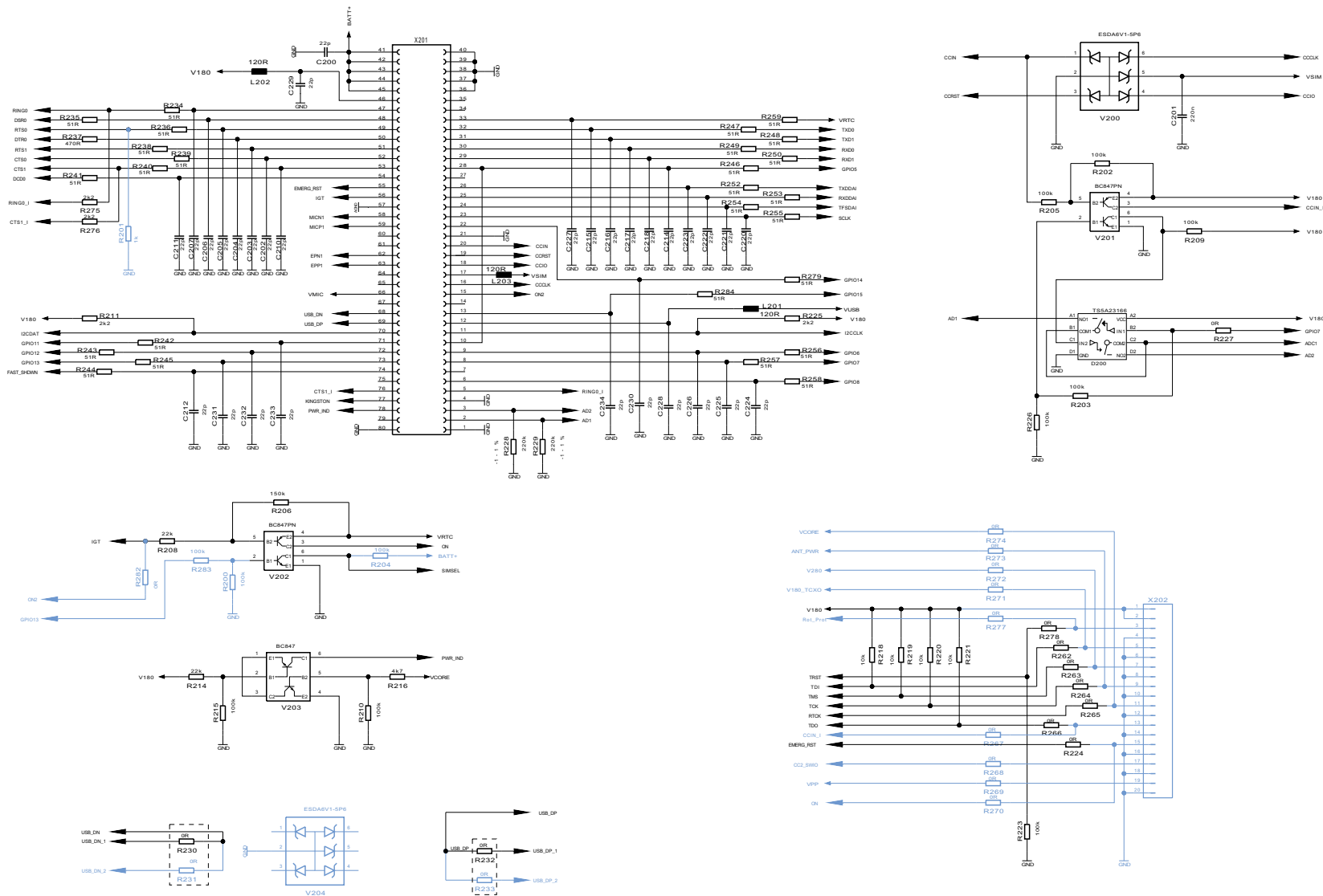


Figure 6: Schematic sheet 1

Note: Circuit elements marked blue are not (yet) populated on the BGS5 evaluation module board. These elements are applicable for further Cinterion® products only.



Note: Circuit elements marked blue are not (yet) populated on the BGS5 evaluation module board. These elements are applicable for further Cinterion® products only.

Figure 7: Schematic sheet 2

X100	PIN_NO	NETNAME
	1	VMIC
	2	EPN1
	3	EPP1
	4	GND
	5	BATT+
	6	GND
	7	ADC1
	8	ON
	9	GND
	10	V180
	11	RXD0
	12	CTS0
	13	TXD0
	14	RING0
	15	RTS0
	16	VRTC
	17	CCBST_I
	18	CCIN_I
	19	CCIO_I
	20	VSIM_I
	21	CCCLK_I
	22	VOOBE
	23	TXDDAL
	24	TESDAL
	25	RXDDAL
	26	SCLK
	27	I2CDAT
	28	I2CCLK
	29	TXD1
	30	RXD1
	31	RTS1
	32	CTS1
	33	EMERG_RST
	34	GND
	35	V180
	36	GPIO8
	37	GPIO7
	38	GPIO6
	39	GPIO5
	40	FAST_SHDWN
	41	DSR0
	42	DCD0
	43	DTR0
	44	USB
	45	USB_DP_1
	46	USB_DN_1
	47	GND
	48	GND
	49	GND
	50	GND

X100	PIN_NO	NETNAME
	51	GND
	52	GND
	53	BATT+
	54	GND
	55	GND
	56	GND
	57	GND
	58	GND
	59	RF_OUT_3
	60	GND
	61	GND
	62	GND
	63	GND
	64	AGND
	65	MICP1
	66	MICN1
	67	TRST
	68	TCK
	69	TMS
	70	TDI
	71	TDO
	72	ON2
	73	RTCK
	74	TRST
	75	TCK
	76	TMS
	77	TDI
	78	TDO
	79	ON2
	80	RTCK
	81	GND
	82	Ro1_Prot
	83	GND
	84	GND
	85	GND
	86	GND
	87	VPP
	88	GND
	89	GND
	90	GND
	91	GND
	92	GND
	93	GND
	94	GND
	95	GND
	96	GND
	97	GND
	98	KINGSTON
	99	GND
	100	GND
	101	GND
	102	GND
	103	GND
	104	V280
	105	V180_TCXO

X100	PIN_NO	NETNAME
	106	CC2_SIMO
	201	EPN1
	202	EPP1
	203	GND
	204	BATT+
	205	GND
	206	ADC1
	207	ON
	208	GND
	209	V180
	210	RXD0
	211	CTS0
	212	TXD0
	213	RING0
	214	RTS0
	215	VRTC
	216	CCBST_I
	217	CCIN_I
	218	CCIO_I
	219	GPIO14
	220	GPIO13
	221	GPIO12
	222	GPIO11
	223	GND
	224	GPS_ANT
	225	GND
	226	GND
	227	GND
	228	ANT_PWR
	229	FAST_SHDWN
	230	DSR0
	231	DCD0
	232	DTR0
	233	USB
	234	USB_DP_2
	235	USB_DN_2
	236	HSIC_DATA
	237	HSIC_STRB
	238	GND
	239	GPIO5
	240	GPIO6
	241	GPIO7
	242	GPIO8
	243	VMIC
	244	GND
	245	GND
	246	CC2_VCC
	247	CC2_CLK
	248	CC2_IO
	249	CC2_RST
	250	GND
	251	GND
	252	GND

X102	PIN_NO	NETNAME
	1	HSIC_DATA
	2	GND
	3	GND

X103	PIN_NO	NETNAME
	1	HSIC_STRB
	2	GND
	3	GND

X104	PIN_NO	NETNAME
	1	SIGN633
	2	GND
	3	GND

X105	PIN_NO	NETNAME
	1	SIGN635
	2	GND
	3	GND

X106	PIN_NO	NETNAME
	1	SIGN638
	2	GND
	3	GND

X107	PIN_NO	NETNAME
	1	CC2_VCC
	2	CC2_RST
	3	CC2_CLK
	4	GND
	5	GND
	6	CC2_IO

X108	PIN_NO	NETNAME
	1	

X201	PIN_NO	NETNAME
	1	GND
	2	AD1
	3	AD2
	4	GND
	5	RING0_I
	6	SIGN609
	7	
	8	SIGN608
	9	SIGN607
	10	GPIO5_X
	11	I2CCLK
	12	SIGN661
	13	
	14	
	15	ON2
	16	CCCLK
	17	SIGN648
	18	CCIO
	19	CCBST
	20	CCIN
	21	GND
	22	SIGN662
	23	SIGN606
	24	SIGN605
	25	SIGN604
	26	SIGN603
	27	
	28	GPIO5_X
	29	SIGN602
	30	SIGN601
	31	SIGN600
	32	SIGN599
	33	SIGN611
	34	
	35	
	36	GND
	37	GND
	38	GND
	39	GND
	40	GND
	41	BATT+
	42	BATT+
	43	BATT+
	44	BATT+
	45	BATT+
	46	SIGN613
	47	SIGN587
	48	SIGN588
	49	SIGN589
	50	SIGN590

X201	PIN_NO	NETNAME
	51	SIGN591
	52	SIGN592
	53	SIGN593
	54	SIGN594
	55	EMERG_RST
	56	IQT
	57	AGND
	58	MICN1
	59	MICP1
	60	
	61	
	62	EPN1
	63	EPP1
	64	
	65	
	66	VMIC
	67	
	68	USB_DN
	69	USB_DP
	70	I2CDAT
	71	SIGN668
	72	SIGN669
	73	SIGN670
	74	SIGN598
	75	
	76	CTS1_I
	77	KINGSTON
	78	PWR_IND
	79	
	80	GND

X202	PIN_NO	NETNAME
	1	V180
	2	V180
	3	SIGN660
	4	GND
	5	SIGN658
	6	GND
	7	SIGN657
	8	GND
	9	SIGN656
	10	GND
	11	SIGN655
	12	GND
	13	SIGN659
	14	GND
	15	SIGN574
	16	GND
	17	SIGN654
	18	GND
	19	SIGN653
	20	GND

Note: Not all PIN NOs are usable on an BGS5 evaluation module board. These PIN NOs are applicable for further Cinterion® products only. For assigned pads please refer to [2] (the ON2 pad is also called AUTO_ON).

Figure 8: Schematic sheet 3

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