TFT DISPLAY SPECIFICATION

WINSTAR Display Co.,Ltd.

華凌光電股份有限公司

Winstar Display Co., LTD
華凌光電股份有限公司
WEB: http://www.winstar.com.tw *E-mail: sales@winstar.com.tw*

SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF57MTIBCDRG0#

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2017/02/16			

TFT Display Inspection Specification: http://www.winstar.com.tw/technology/download.html

MODLE NO：

DOC. FIRST ISSUE

RECORDS OF REVISION

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2016/03/22		First issue
A	2016/06/23		Modify SPI Timing Characteristics.
B	2016/08/10		Modify Vibration test.
C	2016/08/25		Modify Interface (CON3).
D	2016/10/05		Modify Summary Add Aspect Ratio
E	2016/11/01		Modify Touch Panel Information.
F	2016/12/12		Modify Brightness
G	2017/02/16		Modify CTP interface.

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12.Touch Panel Information

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14.Other

1.Module Classification Information

W	F	57	M	T	I	B	C	D	R	G	0	#
①	②	③	④	⑤	⑥	⑦	⑧	⑨	⑩	⑪	⑫	⑬

①	Brand：WINSTAR DISPLAY CORPORATION	
②	Display Type：F→TFT Type, J→Custom TFT	
③	Display Size：5.7” TFT	
④	Model serials no.	
⑤	Backlight Type：	T→LED, White S→LED, High Light White
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	I→Transmissive, W. T, 6:00 L→Transmissive, W.T,12:00 Z→Transmissive, W.T, O-TFT
⑦	B：TFT+FR+CONTROL BOARD	
⑧	Resolution:	

C:320240	E:480272	G: 800480		

⑨	TFT type D: Digital panel
⑩	Interface： R: Uart S: SPI-4 (preservation)
⑪	TS： N：Without TS T：RTP (preservation)
⑫	Version
⑬	Special Code #:Fit in with ROHS directive regulations

2.Summary

TFT 5.7” is a TN transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT_LCD module, It is usually designed for industrial application and this module follows RoHs.

3.General Specifications

Item	Dimension	Unit
Size	5.7	inch
Dot Matrix	320 x RGBx240(TFT)	dots
Module dimension	141.12(W) x 101.55(H) x 15.22 (D)	mm
Active area	115.2 x 86.4	mm
Dot pitch	0.12 x 0.36	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Aspect Ratio	4:3	
Backlight Type	LED,Normally White	
Interface	Uart 19200 Baud rate/SPI	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage

4.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

5.Electrical Characteristics

5.1. Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Supply Voltage For LCM	VBUS	—	4.5	5	5.5	V	—
Supply Current For LCM	IBUS	—	—	521	—	mA	Note1
Power Consumption	—	VBUS=5V	—	2605		mW	VBUS=5V Note 2

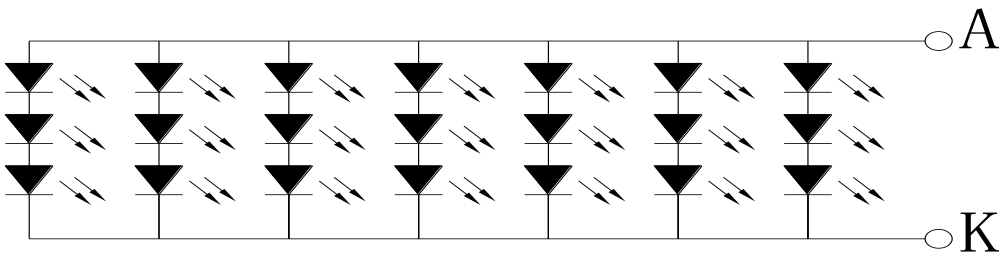
Note 1 : This value is test for VBUS=5V , Ta=25 ℃ only

Note 2 : Power consumption is include Backlight driver system

5.2. LED driving conditions (LED Driver system build in)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current		-	140	-	mA	
Power Consumption		1260	-	1470	mW	
LED voltage	VBL+	9.0	-	10.5	V	Note 1
LED Life Time		-	50,000	-	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



Note 2 : Ta = 25 ℃

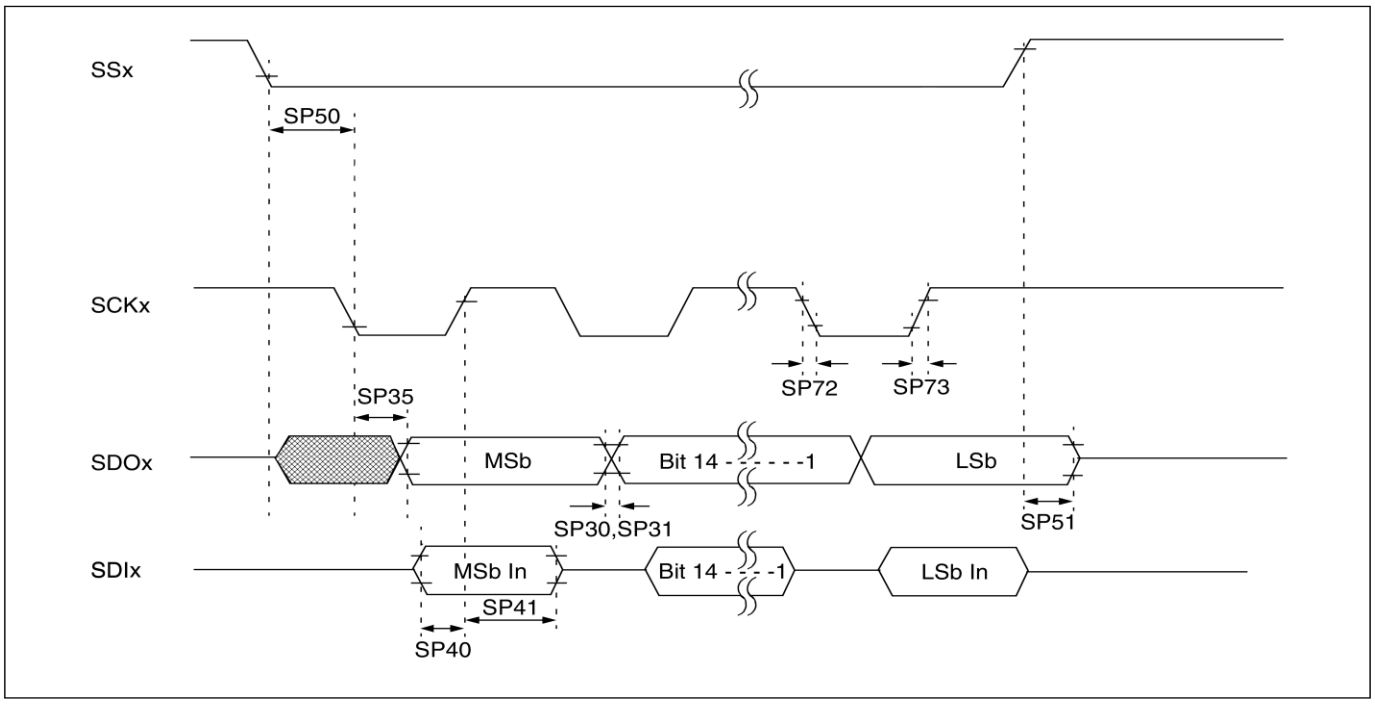
Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

6.DC CHARATERISTICS

Parameter	Symbol	Rating			Unit	Condition
		Min	Typ	Max		
Low level input voltage	V_{IL}	0	-	0.3VBUS	V	
High level input voltage	V_{IH}	0.7VBUS	-	VBUS	V	

7.SPI Timing Characteristics



AC CHARACTERISTICS							
Param No.	Symbol	Characteristic	Min	Typ(1)	Max	Units	Conditions
SP70	TscL	SCKx Input Low Time	250	—	—	ns	
SP71	TscH	SCKx Input High Time	250	—	—	ns	
SP72	TscF	SCKx Input Fall Time(2)	—	10	25	ns	
SP73	TscR	SCKx Input Rise Time(2)	—	10	25	ns	
SP30	TdoF	SDOx Data Output Fall Time(2)	—	10	25	ns	
SP31	TdoR	SDOx Data Output Rise Time(2)	—	10	25	ns	
SP35	TscH2doV, TscL2doV	SDOx Data Output Valid after SCKx Edge	—	—	30	ns	
SP40	TdiV2scH, TdiV2scL	Setup Time of SDIx Data Input to SCKx Edge	20	—	—	ns	
SP41	TscH2diL, TscL2diL	Hold Time of SDIx Data Input to SCKx Edge	20	—	—	ns	
SP50	TssL2scH, TssL2scL	$\overline{SSx}$ to SCKx ↑ or SCKx Input	120	—	—	ns	
SP51	TssH2doZ	$\overline{SSx}$ ↑ to SDOx Output High-Impedance(3)	10	—	50	ns	
SP52	TscH2ssH TscL2ssH	$\overline{SSx}$ after SCKx Edge	50	—	—	ns	

8.Instructions Table

8.1. UART Mode

Text Mode

Instruction of text mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
SB1	PL	SB2	SB3	MD	WR	TR	XH	XL	YH	YL	SR	SG	SB	BR	BG	BB	SDATA	EB1	EB2	EB3

Graphic Mode

Instruction of Graphic mode:

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
SB1	PL	SB2	SB3	MD	RR	XH	XL	YH	YL	PH	PL	EB1	EB2	EB3	SB1

Pixel Mode

Instruction of Pixel mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
SB1	PL	SB2	SB3	MD	RR	XH	XL	YH	YL	PR	PG	PB	EB1	EB2	EB3

Geometry Mode

Instruction of geometry mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
SB1	PL	SB2	SB3	MD	RR	XSH	XSL	YSH	YSL	XEH	XEL	YEH	YEL	LS	LR	LG	LB	EB1	EB2	EB3

Clean Mode

Instruction of Clean Mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
SB1	PL	SB2	SB3	MD	XSH	XSL	YSH	YSL	XEH	XEL	YEH	YEL	EB1	EB2	EB3

PWM Mode

Instruction of Pixel mode

1	2	3	4	5	6	7	8	9	10	11	12	13
SB1	PL	SB2	SB3	MD	PS	PFH	PFL	PDH	PDL	EB1	EB2	EB3

Backlight Mode

Instruction of Clean Mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
SB1	PL	SB2	SB3	MD	RR	XH	XL	YH	YL	PR	PG	PB	EB1	EB2	EB3

8.2. SPI Mode

Text Mode

Instruction of text mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
0x31	SB3	MD	WR	TR	XH	XL	YH	YL	SR	SG	SB	BR	BG	BB	SDATA

Graphic Mode

Instruction of Graphic mode:

1	2	3	4	5	6	7	8	9	10
0x31	SB3	MD	RR	XH	XL	YH	YL	PH	PL

Pixel Mode

Instruction of Pixel mode

1	2	3	4	5	6	7	8	9	10	11
0x31	SB3	MD	RR	XH	XL	YH	YL	PR	PG	PB

Geometry Mode

Instruction of geometry mode

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
0x31	SB3	MD	RR	XSH	XSL	YSH	YSL	XEH	XEL	YEH	YEL	LS	LR	LG	LB

Clean Mode

Instruction of Clean Mode

1	2	3	4	5	6	7	8	9	10	11
0x31	SB3	MD	XSH	XSL	YSH	YSL	XEH	XEL	YEH	YEL

PWM Mode

Instruction of Pixel mode

1	2	3	4	5	6	7	8
0x31	SB3	MD	PS	PFH	PFL	PDH	PDL

Backlight Mode

Instruction of Clean Mode

1	2	3	4	5	6	7	8	9	10	11
0x31	SB3	MD	RR	XH	XL	YH	YL	PR	PG	PB

9.Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr	θ =0°、Φ=0°	-	15	30	.ms	Note 3,5
		Tf		-	35	50	.ms	
Contrast ratio		CR	At optimized viewing angle	150	250	-	-	Note 4,5
Color Chromaticity	White	Wx	θ =0°、Φ=0	0.27	0.32	0.37		Note 2,6,7
		Wy		0.32	0.37	0.42		
Viewing angle (Gray Scale Inversion Direction)	Hor.	Θ R	CR≧10	60	70		Deg.	Note 1
		Θ L		60	70			
	Ver.	Φ T		40	50			
		Φ B		60	70			
Brightness		-	-	600	700		cd/m^2	Center of display

Ta=25±2℃, IL=140mA

Note 1: Definition of viewing angle range

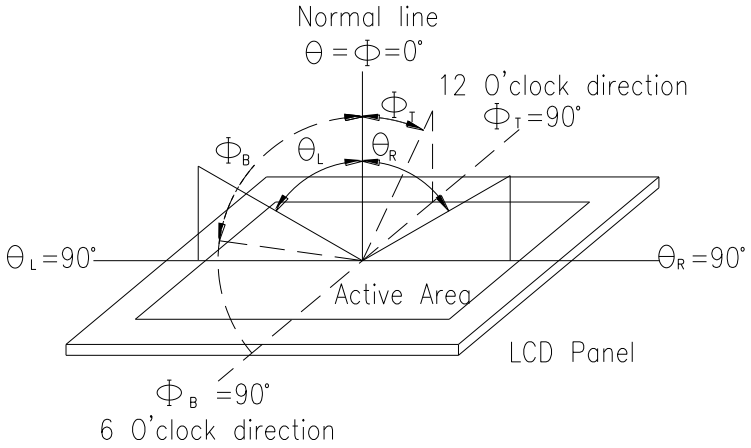


Fig. 9.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

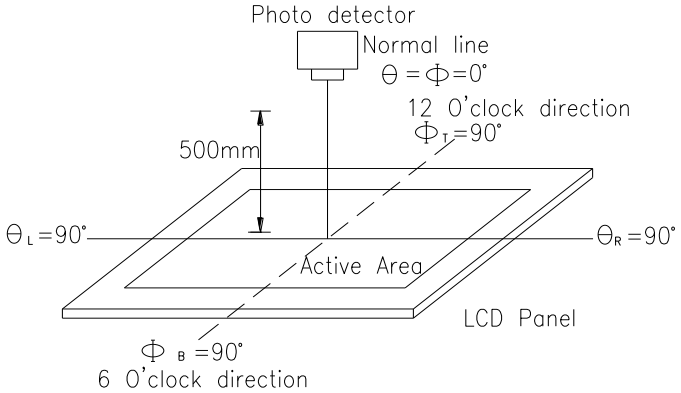
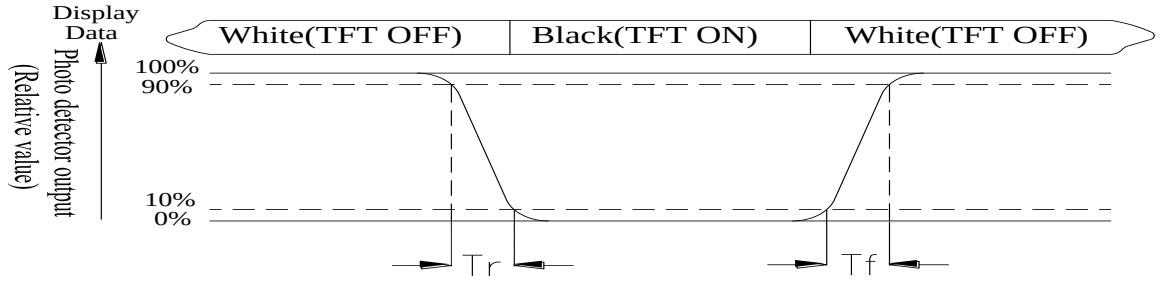


Fig. 9.2. Optical measurement system setup

Note 3: Definition of Response time:
The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, Tr, is the time between photo detector output intensity changed from 90%to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10%to 90%



Note 4: Definition of contrast ratio:
The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White Vi = Vi50 ± 1.5V
Black Vi = Vi50 ± 2.0V
“±” means that the analog input signal swings in phase with VCOM signal.
“±” means that the analog input signal swings out of phase with VCOM signal.
The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)
Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

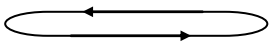
10.Interface

CON 2			
Pin	**Symbol**	**I/O**	**Function**
1	GND	Power Supply	Power Ground
2	TX	O	Uart Transmit pin
3	RX	I	Uart Receive pin
4	VBUS	Power Supply	Power supply : 5V
5	D+	I/O	USB Data +
6	D-	I/O	USB Data -
7	GND	Power Supply	Power Ground
8	/REST	I	Reset (active Low)
9	GND	Power Supply	Power Ground
10	PWM	O	Pulse width modulation
11	GND	Power Supply	Power Ground
12	VBUS	Power Supply	Power supply : 5V

CON 3			
Pin	**Symbol**	**I/O**	**Function**
1	GND	Power Supply	Power Ground
2	SW1	I	Switch (active low)
3	SW2	I	Switch (active low)
4	SW3	I	Switch (active low)
5	SW4	I	Switch (active low)
6	GND	Power Supply	Power Ground
7	SDI	O	Master Input Slave Output (MISO)
8	SDO	I	Master Output Slave Input (MOSI)
9	SCK	I	Serial Clock
10	CS	I	Serial Chip selection
11	SPI_INT	O	Serial Interrupt
12	VBUS	Power Supply	Power supply : 5V

11.Reliability

Content of Reliability Test (Wide temperature, -20℃~70℃)

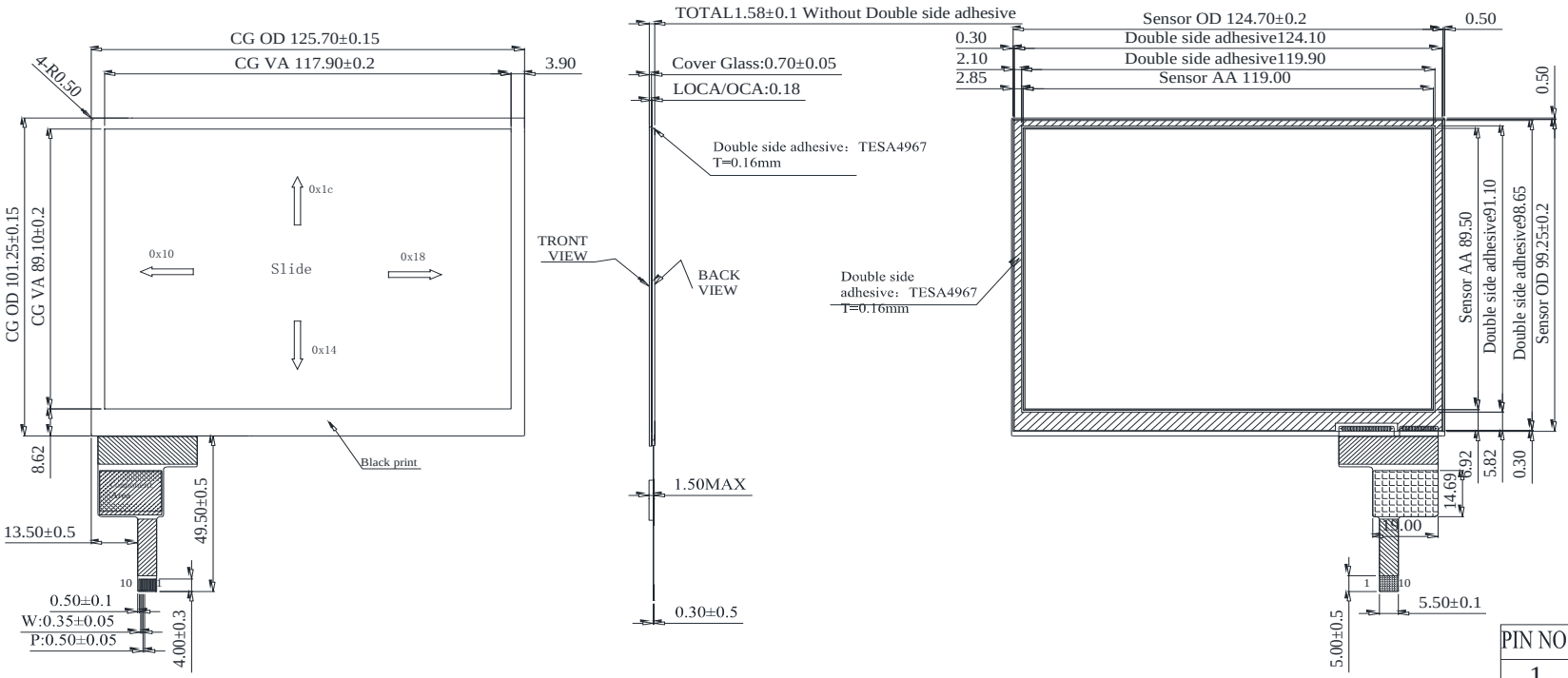
Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	——
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60℃,90%RH max	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20℃ 25℃ 70℃  30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	——
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	——

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

12.Touch Panel Information



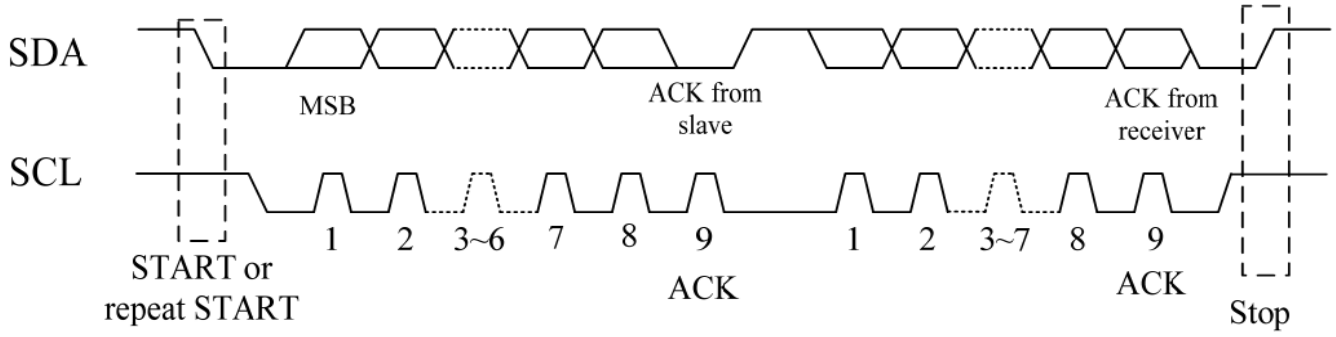
FRONT VIEW

SIDE VIEW

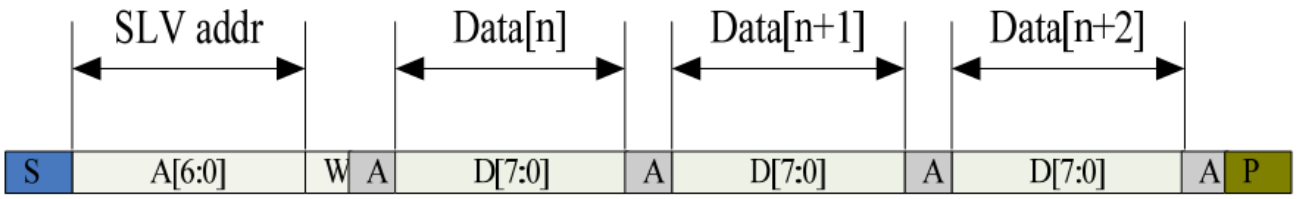
BACK VIEW

PIN NO.	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

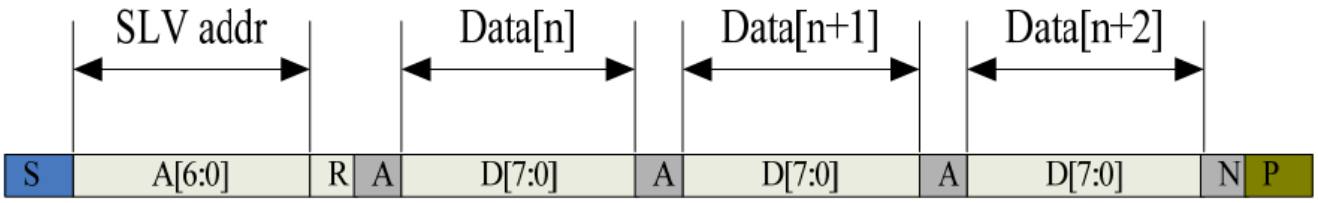
12.1. CTP I2C Timing:



I2C Serial Data Transfer Format



I2C master write, slave read



I2C master read, slave write

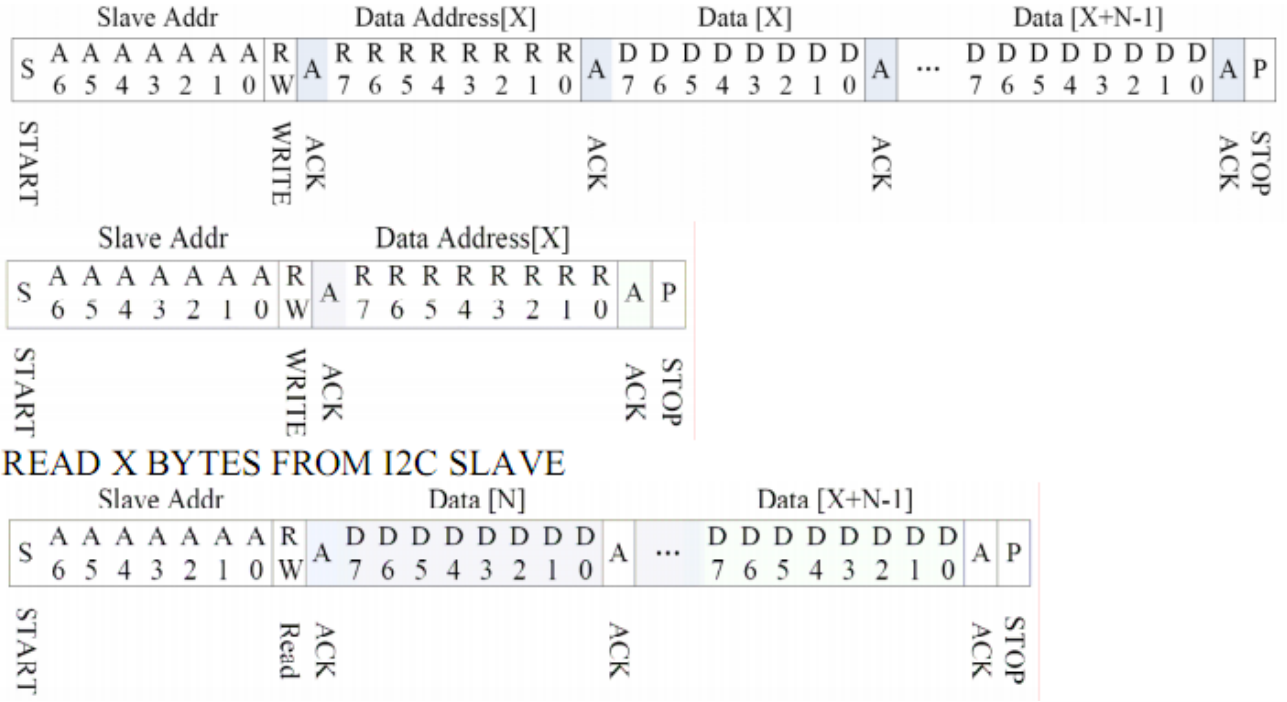
Table mnemonics description

Mnemonics	Description
S	12C Start or 12C Restart
A[6:0]	Slave address
R/W	READ/WRITE bit, ‘1' for read, ‘0' for write
A(N)	ACK(NACK) bit
P	STOP :the indication of the end of a packet(if this bit is missing, S will indicate the end of the current packet and beginning of the next packet)

Table I2C timing Characteristics

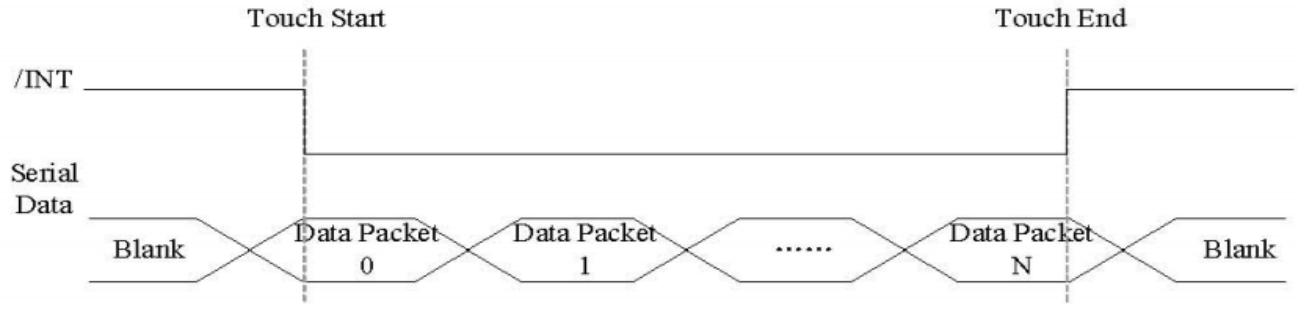
Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) ST ART condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup time for STOP condition	us	4.0	\

12.2. WRITE BYTES TO I2C SLAVE



READ X BYTES FROM I2C SLAVE

AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA, HERE IS THE TIMING TO GET TOUCH DATA.



Address: 0x38

TOUCH DATA READ PROTOCOL

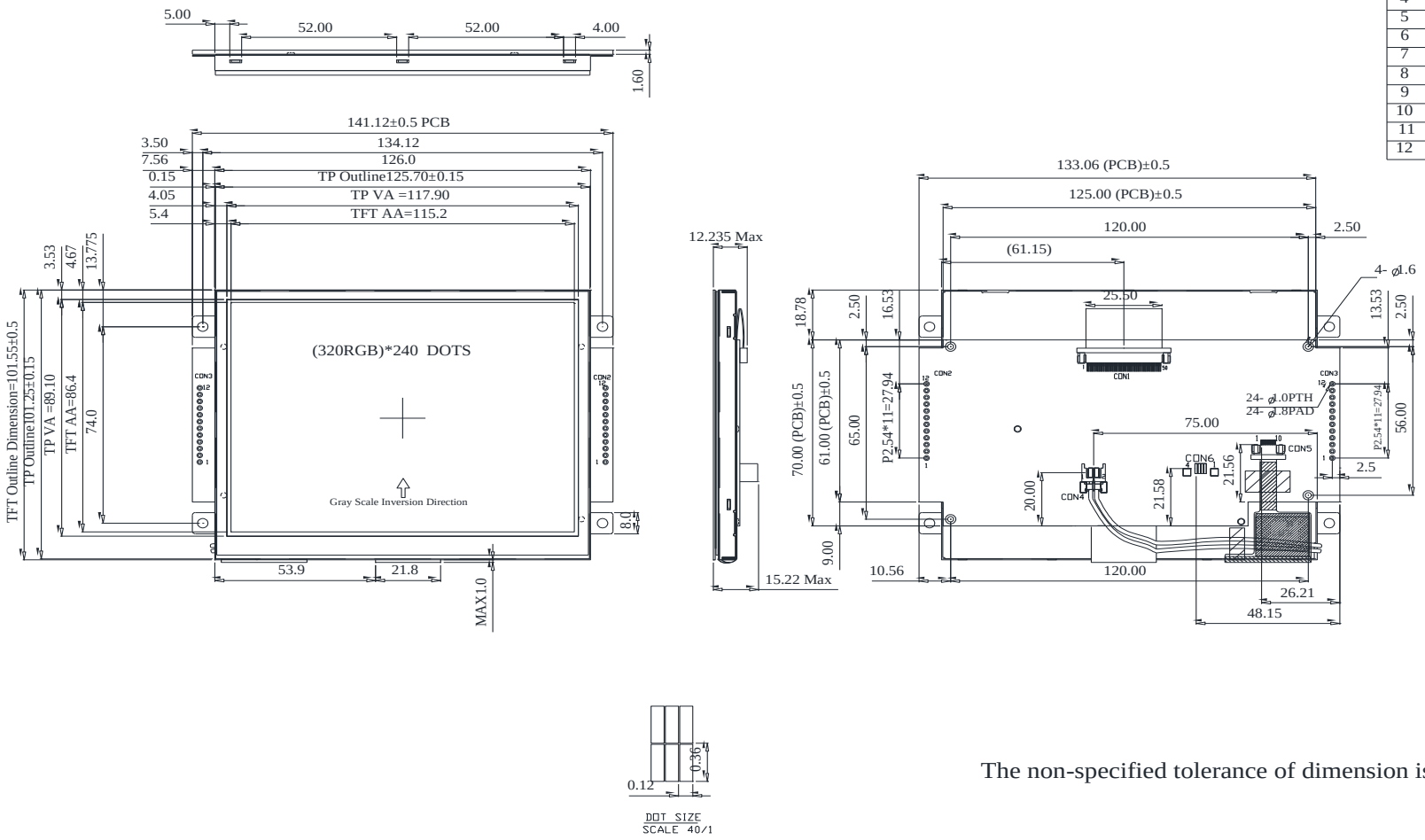
NAME	VALUE	DESCRIPTION
START CH	0X00	START COMMAND FOR CTPM TOUCH DATA PACKET,HOST MUST SEND CTPM A START CH COMMAND BEFORE READ TOUCH DATA
lst READ BYTE～ LAST READ BYTE		TOUCH DATA PACKET SENT BY CTPM,EACH BYTE HAS 8-BIT DATA ,A TOUCH DATA PACKET CONSISTS OF N BYTE

Address	**Name**	**Bit7**	**Bit6**	**Bit5**	**Bit4**	**Bit3**	**Bit2**	**Bit1**	**Bit0**	**Host Access**
00h	Devide__Mode		Device Model[2:0]							RW
01h	Gest__ID	Gesture ID[7:0]								R
02h	TD__Status					Number of touch points[3:0]				R
03h	Touch1__XH	1st Event Flag				1st Touch X Position[11:8]				R
04h	Touch1__XL	1st Touch X Position[7:0]								R
05h	Touch1__YH	1st Touch ID[3:0]				1st Touch Y Position[11:8]				R
06h	Touch1__YL	1st Touch Y Position[7:0]								R
09h	Touch2__XH	2nd Event Flag				2nd Touch X Position[11:8]				R
0Ah	Touch2__XL	2nd Touch X Position[7:0]								R
0Bh	Touch2__YH	2nd Touch ID[3:0]				2ndTouch Y Position[11:8]				R
0Ch	Touch2__YL	2nd Touch Y Position[7:0]								R
0Fh	Touch3__XH	3rdEvent Flag				3rdTouch X Position[11:8]				R
10h	Touch3__XL	3rd Touch X Position[7:0]								R
11h	Touch3__YH	3rdTouch ID[3:0]				3rdTouch Y Position[11:8]				R
12h	Touch3__YL	3rd Touch Y Position[7:0]								R
15h	Touch4__XH	4thEvent Flag				4thTouch X Position[11:8]				R
16h	Touch4__XL	4th Touch X Position[7:0]								R

17h	Touch4__YH	4thTouch ID[3:0]	4thTouch Y Position[11:8]	R
18h	Touch4__YL	4th Touch Y Position[7:0]		R
1Bh	Touch5__XH	5thEvent Flag	5thTouch X Position[11:8]	R
1Ch	Touch5__XL	5th Touch X Position[7:0]		R
1Dh	Touch5__YH	5thTouch ID[3:0]	5thTouch Y Position[11:8]	R
1Eh	Touch5__YL	5th Touch Y Position[7:0]		R

13.Contour Drawing

CON2		CON3	
PIN	SYMBOL	PIN	SYMBOL
1	GND	1	GND
2	TX	2	SW1
3	RX	3	SW2
4	VBUS	4	SW3
5	D+	5	SW4
6	D-	6	GND
7	GND	7	SDI
8	RST	8	SDO
9	GND	9	SCK
10	PWM	10	SCS
11	GND	11	SPI_INT
12	VBUS	12	VBUS



The non-specified tolerance of dimension is ±0.3mm.

winstar

LCM Sample Estimate Feedback Sheet

Module Number： _____

1、Panel Specification：

1. Panel Type： □ Pass □ NG ,_____
2. View Direction： □ Pass □ NG ,_____
3. Numbers of Dots： □ Pass □ NG ,_____
4. View Area： □ Pass □ NG ,_____
5. Active Area： □ Pass □ NG ,_____
6. Operating Temperature： □ Pass □ NG ,_____
7. Storage Temperature： □ Pass □ NG ,_____
8. Others：_____

2、Mechanical Specification：

1. PCB Size： □ Pass □ NG ,_____
2. Frame Size： □ Pass □ NG ,_____
3. Material of Frame： □ Pass □ NG ,_____
4. Connector Position： □ Pass □ NG ,_____
5. Fix Hole Position： □ Pass □ NG ,_____
6. Backlight Position： □ Pass □ NG ,_____
7. Thickness of PCB： □ Pass □ NG ,_____
8. Height of Frame to PCB： □ Pass □ NG ,_____
9. Height of Module： □ Pass □ NG ,_____
10. Others： □ Pass □ NG ,_____

3、Relative Hole Size：

1. Pitch of Connector： □ Pass □ NG ,_____
2. Hole size of Connector： □ Pass □ NG ,_____
3. Mounting Hole size： □ Pass □ NG ,_____
4. Mounting Hole Type： □ Pass □ NG ,_____
5. Others： □ Pass □ NG ,_____

4、Backlight Specification：

1. B/L Type： □ Pass □ NG ,_____
2. B/L Color： □ Pass □ NG ,_____
3. B/L Driving Voltage (Reference for LED Type)： □ Pass □ NG ,_____
4. B/L Driving Current： □ Pass □ NG ,_____
5. Brightness of B/L： □ Pass □ NG ,_____
6. B/L Solder Method： □ Pass □ NG ,_____
7. Others： □ Pass □ NG ,_____

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5、Electronic Characteristics of Module：

1.	Input Voltage：	□ Pass	□ NG ,_____
2.	Supply Current：	□ Pass	□ NG ,_____
3.	Driving Voltage for LCD：	□ Pass	□ NG ,_____
4.	Contrast for LCD：	□ Pass	□ NG ,_____
5.	B/L Driving Method：	□ Pass	□ NG ,_____
6.	Negative Voltage Output：	□ Pass	□ NG ,_____
7.	Interface Function：	□ Pass	□ NG ,_____
8.	LCD Uniformity：	□ Pass	□ NG ,_____
9.	ESD test：	□ Pass	□ NG ,_____
10.	Others：	□ Pass	□ NG ,_____

6、Summary：

Sales signature：_____

Customer Signature：_____ **Date**：____/____/____