



Features

- RoHS compliant*
- Protects one line or one I/O port
- Bidirectional configuration
- ESD protection 30 kV max.
- Low capacitance: ~3 pF typical
- Replaces 0805 MLV devices

Applications

- Cellular phones
- PDAs and notebooks
- Digital cameras
- MP3 players and GPS
- USB interface

CDSOD323-TxxC - TVS Diode Array Series

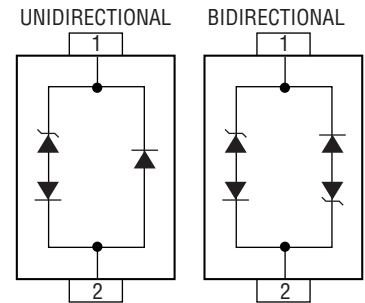
General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Transient Voltage Suppressor Array diodes for surge and ESD protection applications, in SOD323 package size format. The Transient Voltage Suppressor Array series offers a choice of voltage types ranging from 3 V to 24 V in a unidirectional or bidirectional configuration. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

The Bourns® device will meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements.

Note: For 12 V and 24 V VDSL applications, the CDSOD323-TxxC-DSL family of devices is recommended.



Electrical & Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20 μs)	P _{PP}	350	W
Operating Temperature	T _J	-55 to +150	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Parameter	Symbol	CDSOD323-								Unit
		Uni-T03	Bi-T03C	Uni-T05	Bi-T05C	Uni-T08	Bi-T08C	Uni-T12	Bi-T12C	
Minimum Breakdown Voltage @ 1 mA	V _{BR}	4.0		6.0		8.5		13.3		V
Working Peak Voltage	V _{WM}	3.3		5.0		8.0		12.0		V
Maximum Clamping Voltage @ I _P = 1 A	V _C	7.0		9.8		13.4		19.0		V
Typical Clamping Voltage @ 8/20 μs @ I _{PP}	V _C	19.0 V @ 20 A		18.3 V @ 17 A		18.5 V @ 17 A		28.3 V @ 11 A		V
Maximum Leakage Current @ V _{WM}	I _D	5		5		2		1		μA
Typical Capacitance @ 0 V, 1 MHz	C _J	3								pF

Parameter	Symbol	CDSOD323-						Unit
		Uni-T15	Bi-T15C	Uni-T18	Bi-T18C	Uni-T24	Bi-T24C	
Minimum Breakdown Voltage @ 1 mA	V _{BR}	16.7		20.0		26.7		V
Working Peak Voltage	V _{WM}	15.0		18.0		24.0		V
Maximum Clamping Voltage @ I _P = 1 A	V _C	24.0		29.0		43.0		V
Typical Clamping Voltage @ 8/20 μs @ I _{PP}	V _C	31.8 V @ 10 A		45.0 V @ 8 A		56.0 V @ 6 A		V
Maximum Leakage Current @ V _{WM}	I _D	1						μA
Typical Capacitance @ 0 V, 1 MHz	C _J	3						pF

Notes:

1. Part numbers with suffix "C" indicate bidirectional device, i.e. CDSOD323-T05C.
2. For bidirectional devices only, the electrical specifications apply in both directions.

*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice.

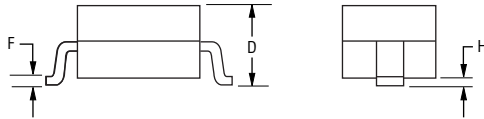
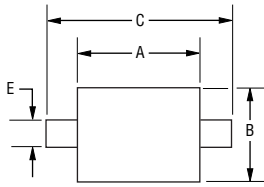
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

CDSOD323-TxxC - TVS Diode Array Series



Product Dimensions

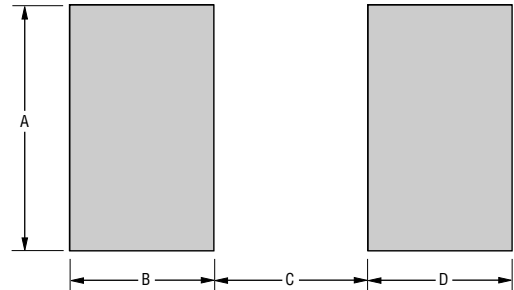
This is an RoHS compliant molded JEDEC SOD-323 package with 100 % Sn plating on the terminations. It weighs approximately 30 mg and has a flammability rating of UL 94V-0.



Dimensions	
A	$\frac{1.60 - 1.90}{(0.063 - 0.075)}$
B	$\frac{1.15 - 1.45}{(0.045 - 0.057)}$
C	$\frac{2.39 - 2.70}{(0.094 - 0.106)}$
D	$\frac{0.92 - 1.10}{(0.036 - 0.043)}$
E	$\frac{0.25 - 0.40}{(0.010 - 0.016)}$
F	$\frac{0.10 - 0.20}{(0.004 - 0.008)}$
H	$\frac{0.10}{(0.004)}$ MAX

DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

Recommended Footprint



Dimensions	
A	$\frac{0.80}{(0.031)}$
B	$\frac{0.80}{(0.031)}$
C	$\frac{1.40}{(0.055)}$
D	$\frac{0.80}{(0.031)}$

DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

Typical Part Marking

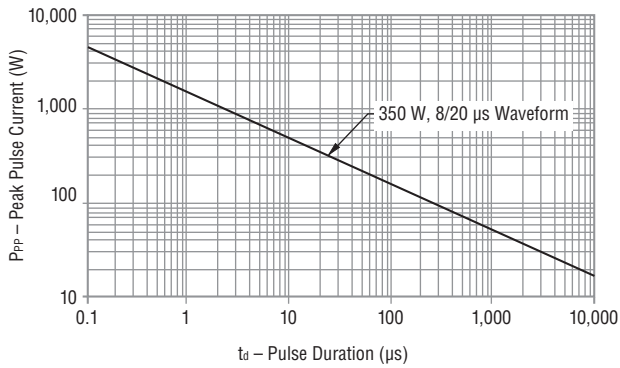
Each device has device marking outlined below and the unidirectional devices have an additional Polarity Band indicating the cathode.

CDSOD323-T033
CDSOD323-T03C3C
CDSOD323-T055
CDSOD323-T05C5C
CDSOD323-T088
CDSOD323-T08C8C
CDSOD323-T122
CDSOD323-T12C2C
CDSOD323-T156
CDSOD323-T15C6C
CDSOD323-T181
CDSOD323-T18C1C
CDSOD323-T244
CDSOD323-T24C4C

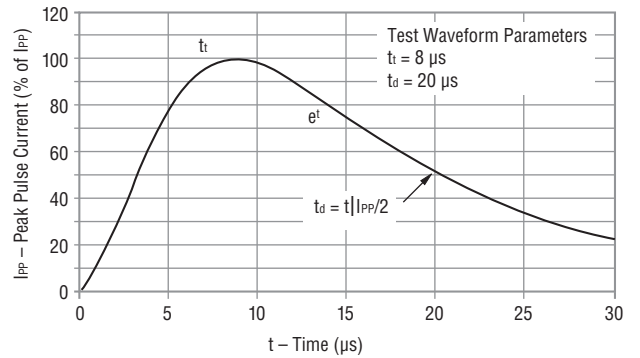
CDSOD323-TxxC - TVS Diode Array Series

Performance Graphs

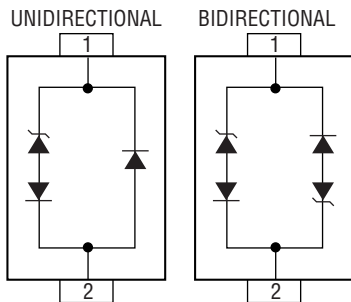
Peak Pulse Power vs Pulse Time



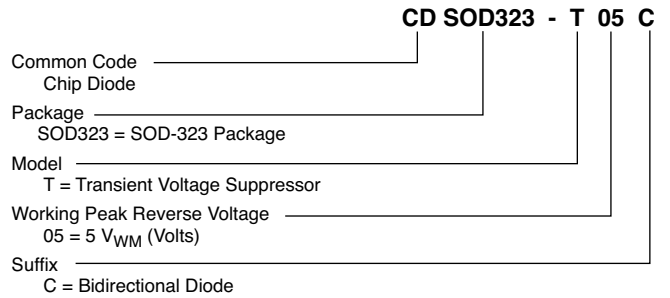
Pulse Waveform



Block Diagram



How to Order



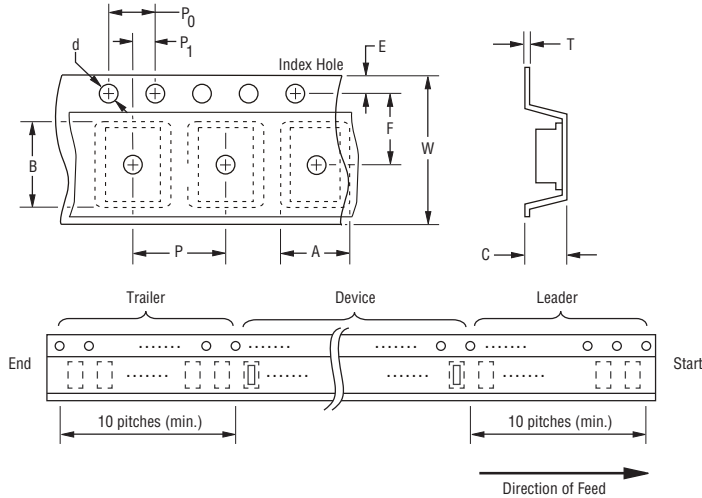
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Packaging Information

The product is packaged in tape and reel format per EIA-481 standard.



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Item	Symbol	SOD-323
Carrier Width	A	$\frac{1.55 \pm 0.10}{(0.061 \pm 0.004)}$
Carrier Length	B	$\frac{2.90 \pm 0.10}{(0.114 \pm 0.004)}$
Carrier Depth	C	$\frac{1.35 \pm 0.10}{(0.053 \pm 0.004)}$
Sprocket Hole	d	$\frac{1.55 \pm 0.05}{(0.061 \pm 0.002)}$
Reel Outside Diameter	D	$\frac{178}{(7.008)}$
Reel Inner Diameter	D ₁	$\frac{80.0}{(3.150)}$ MIN.
Feed Hole Diameter	D ₂	$\frac{13.0 \pm 0.20}{(0.512 \pm 0.008)}$
Sprocket Hole Position	E	$\frac{1.75 \pm 0.10}{(0.069 \pm 0.004)}$
Punch Hole Position	F	$\frac{3.50 \pm 0.05}{(0.138 \pm 0.002)}$
Punch Hole Pitch	P	$\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$
Sprocket Hole Pitch	P ₀	$\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$
Embossment Center	P ₁	$\frac{2.00 \pm 0.05}{(0.079 \pm 0.002)}$
Overall Tape Thickness	T	$\frac{0.20 \pm 0.10}{(0.008 \pm 0.004)}$
Tape Width	W	$\frac{8.00 \pm 0.20}{(0.315 \pm 0.008)}$
Reel Width	W ₁	$\frac{13.5}{(0.531)}$ MAX.
Quantity per Reel	--	3000

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