SRPASSIVES

CY1Y-2N2

DATA SHEET

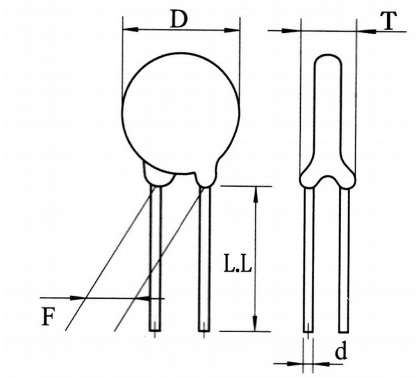
Date：17-May-2017

customer name		Description	**CY1Y-2N2**	Quantity	**PCS**
customer lot no.		Material lot no.		Temperature	**25**℃
Project Lot no.		Sampling Standard	**MIL-STD-105E，normal inspection，inspection levelⅡ**		
Allow acceptable quality level	**Critical Defect：AC=0，RE=1；Major Defect：AQL=0.4；Minor Defect：AQL=0.65**				

Sampling Qty	**PCS**	Acceptable Qty	Minor Defect	Major Defect	Critical Defect	Disqualified Qty	Minor Defect	Major Defect	Critical Defect
			0 PCS	**0 PCS**	**0 PCS**		**1 PCS**	**1 PCS**	**1 PCS**

Project		Capacitance (**C**)	Dissipation Factor (**D.F**)	Insulation Resistance (**I.R**)	Voltage Proof (**T.V**)	Pitch (**P**)	diameter of leader wire (d)	diameter for the chip (D)	Thickness (**T**)	length of the lead wire (**L.L**)	Sol	Appearance
Standard Value **NO.**	**PF**	1760	1.00	10GΩ	4000V	9.5mm	0.58mm	8.8mm	5.0	20.0		
		2640	Maximum	Minimum	AC	±1.5mm	±10%	Maximum	Maximum	Minimum		

Product Shape:



logo printing：

Remark	1.ref. to the Capacitance（c）,Dissipation Factor（D.F）,Instrumentation:TH2617A,1KHZ(Rate),1.0V(Voltage Testing） 2.Instrumentation for Insulation Rediditance:TH2683 3.Instrumentation for Voltage Proof：CS2671A 4.Dimensional inspection measuring tool for Appearance：Vernier caliper（0.02mm）

Adjudged	***Qualified**	**Disqualified**	Inspector	Li Shuipeng	Supervisor	Zheng Xiuwen