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## SAM E51 Integrated Graphics and Touch Curiosity User Guide

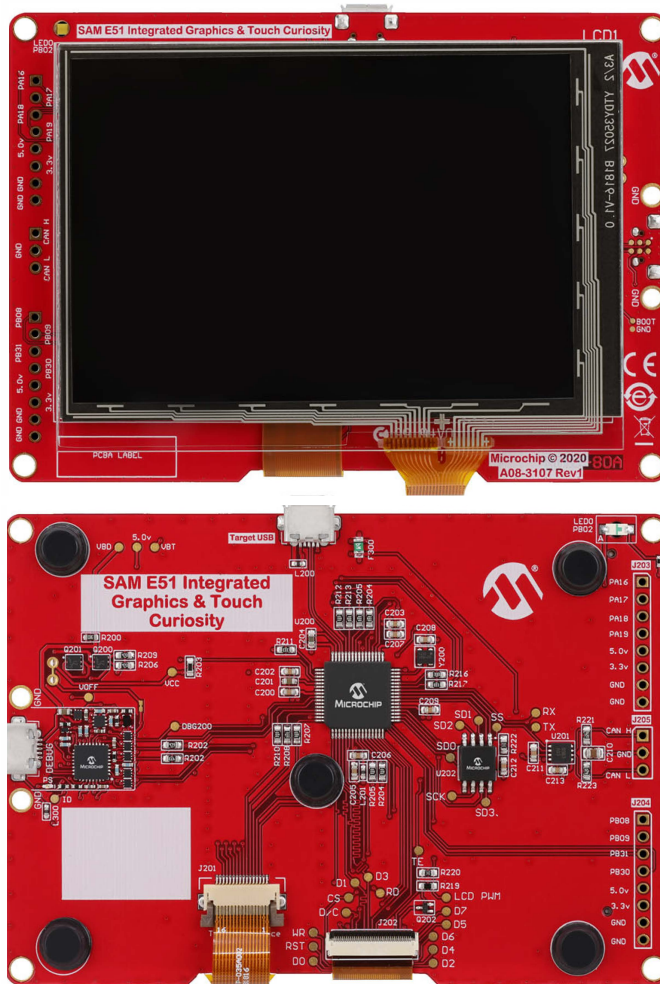
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### Preface

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The SAM E51 Integrated Graphics and Touch Curiosity Evaluation Kit (EV14C17A) is a hardware platform for evaluating the SAME51J20A microcontroller. The evaluation kit integrates a complete touch screen, TFT graphics and touch surface into a single-chip solution supported by the MPLAB<sup>®</sup> X Integrated Development Environment (IDE). The evaluation kit provides easy access to the features of the SAME51J20A to integrate the device into a custom design. The Integrated Graphics and Touch Curiosity Evaluation kit includes an On-Board Debugger, therefore no external tools are necessary to program the SAME51J20A device.



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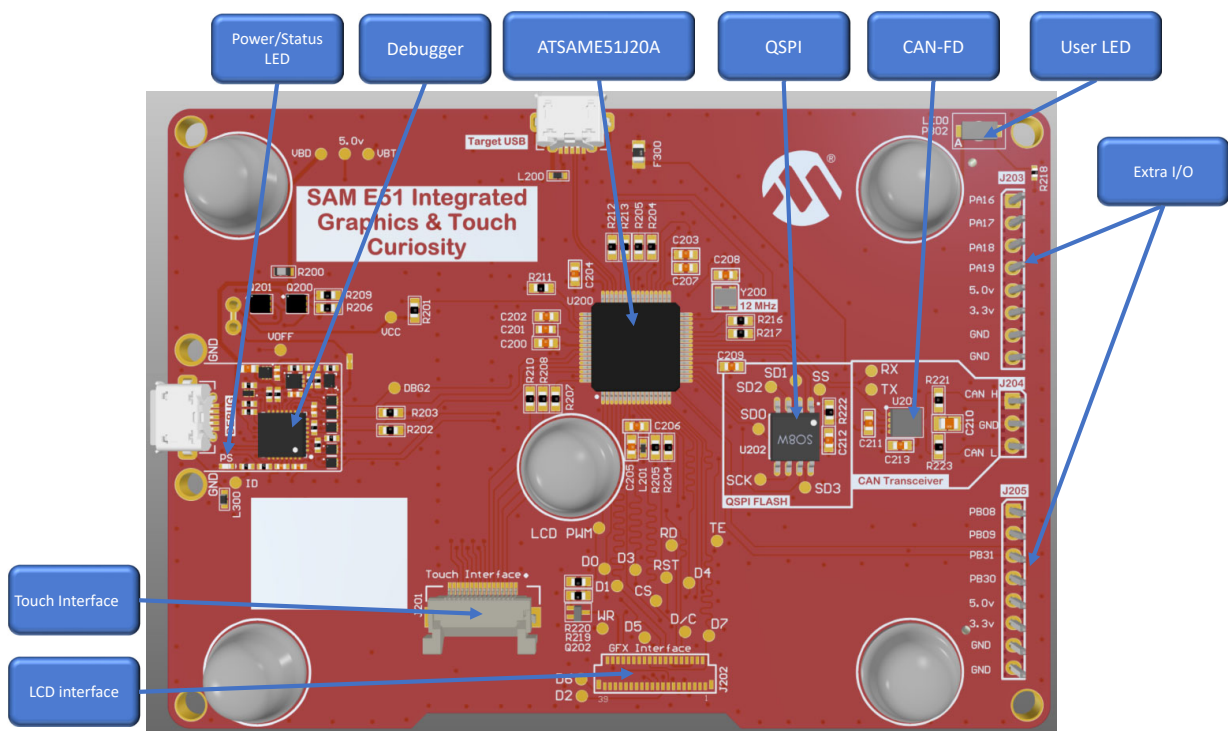
# 1. Introduction

## 1.1 Features

The following are key features of the evaluation kit.

- ATSAME51J20A microcontroller
- One user LED
- On-board debugger
  - Board identification in MPLAB X IDE
  - One green power LED and status LED
  - Programming and debugging
  - Virtual COM port (CDC)
  - One Logic Analyzer (DGI GPIO)
- 8 MB QSPI Flash
- On-board CAN-FD transceiver
- USB powered
- Adjustable target voltage:
  - MIC5353 LDO regulator controlled by the on-board debugger
  - 1.7-3.6V output voltage
- 500 mA maximum output current (limited by ambient temperature and output voltage)
- 480 x 320 pixel TFT display with 16-bit color

## 1.2 Kit Overview



## **2. Getting Started**

### **2.1 Quick Start**

Follow these steps to exploring the platform:

1. Download MPLAB X IDE.
2. Launch MPLAB X IDE.
3. Connect a USB cable (Standard-A to Micro-B or Micro-AB) between the PC and the debug USB port on the kit.

When the Curiosity kit is connected to the computer for the first time, the operating system will perform a driver software installation. The driver file supports both 32-bit and 64-bit versions of Microsoft® Windows® XP, Windows Vista®, Windows 7, Windows 8, and Windows 10. The drivers for the evaluation kit are included with MPLAB X IDE.

After the Curiosity board is powered, the green status LED will be lit and MPLAB X IDE will auto-detect which Curiosity board is connected. MPLAB X IDE will present relevant information, such as data sheets and kit documentation. The SAME51J20A device is programmed and debugged by the on-board debugger, therefore no external programmer or debugger tool is required.

### 3. On-Board Debugger

The SAM E51 Integrated Graphics and Touch Curiosity Board contains an on-board debugger for programming and debugging. The on-board debugger is a composite USB device of several interfaces: a debugger, a data gateway, and a Virtual COM port. Together with MPLAB X IDE, the on-board debugger interface can program and debug the SAME51J20A. A Data Gateway Interface (DGI) is available for use with the logic analyzer channels for code instrumentation, to visualize program flow. DGI GPIOs can be graphed using the Data Visualizer.

The Virtual COM port is connected to a UART on the SAME51J20A providing an easy way to communicate with the target application through terminal software. The on-board debugger controls one Power and Status LED (marked PS) on the SAM E51 Integrated Graphics and Touch Curiosity board. The following table shows how the LED is controlled in different operation modes.

**Table 3-1. On-Board Debugger LED Control**

Operation Mode	Status LED
Boot Loader mode	LED blink at 1 Hz during power up.
Power-up	LED is lit - constant.
Normal operation	LED is lit - constant.
Programming	Activity indicator; the LED flashes slowly during programming or debugging.
Fault	The LED flashes fast if a power fault is detected.
Sleep/Off	LED is off. The on-board debugger is either in Sleep mode or powered down. This can occur if the kit is externally powered.

#### 3.1 Virtual COM Port

A general-purpose USB serial bridge between a Host PC and a target device.

##### 3.1.1 Overview

The debugger implements a composite USB device that includes a standard Communications Device Class (CDC) interface, which appears on the Host as a Virtual COM Port. The CDC can be used to stream arbitrary data in both directions between the Host and the target: characters sent from the Host will appear in UART form on the CDC TX pin, and UART characters sent into the CDC RX pin will be sent back to the Host. On Windows machines, the CDC will enumerate as Curiosity Virtual COM Port and appear in the ports section of the device manager.



**Important:** On older Windows systems, a USB driver is required for the CDC. This driver is included in Atmel® Studio and MPLAB X IDE installations. On Linux machines, the CDC will enumerate and appear as `/dev/ttyACM#`. On MAC machines, the CDC will enumerate and appear as `/dev/tty.usbmodem#`. Depending on which terminal program is used, it will appear in the available list of modems as `usbmodem#`.

##### 3.1.2 Limitations

Not all UART features are implemented in the debugger CDC; the constraints are as follows:

- Baud rate – Must be in the range of 1200 bps to 500 kbps, and values outside this range will be capped to these values, without warning. Baud rate can be changed on-the-fly.
- Character format – Only 8-bit characters are supported.
- Parity – Can be odd, even, or none.
- Hardware flow control – Not supported.
- Stop bits – One or two bits are supported.

### 3.1.3 Signaling

During USB enumeration, the Host OS will start both communication and data pipes of the CDC interface. At this point, it is possible to set and read back baud rate and other UART parameters of the CDC, but data sending and receiving will not be enabled. When a terminal connects on the Host, it must assert the DTR signal. This is a virtual control signal that is implemented on the USB interface but not in the hardware of the debugger.

Asserting DTR from the Host will indicate to the debugger that a CDC session is active, and it will enable its level shifters (if available), and start the CDC data send and receive mechanisms. De-asserting the DTR signal will not disable the level shifters, but it will disable the receiver, hence no further data will be streamed to the Host. Data packets that are already queued up for sending to the target will continue to be sent out, but no further data will be accepted.

### 3.1.4 Advanced Use

In CDC Override mode for normal operation, the on-board debugger is a true UART bridge between the Host and the device. However, under certain use cases, the debugger can override the Basic Operating mode and use the CDC pins for other purposes. Dropping a text file (with extension .txt) into the debugger's mass storage drive can be used to send characters out of the CDC TX pin. The text file must start with the characters, `CMD:SEND_UART=`. The maximum message length is 50 characters, and all remaining data in the frame is ignored. The default baud rate used in this mode is 9600 bps, but if the CDC is already active or configured, the last used baud rate still applies.

#### USB-Level Framing Considerations:

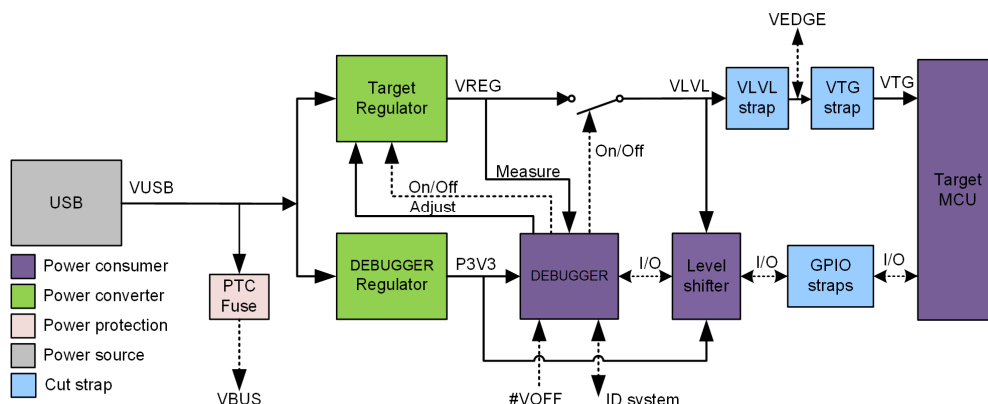
Sending data from the Host to the CDC can be done byte-wise or in blocks, which will be chunked into 64-byte USB frames. Each frame will be queued up for sending to the CDC TX pin. Sending a small amount of data per frame can be inefficient, particularly at low baud rates, because the debugger buffers frames, not bytes. A maximum of 4 x 64-byte frames can be active at any time, the debugger will throttle the incoming frames accordingly. Sending full 64-byte frames containing data is the most efficient.

When receiving data from the target, the debugger will queue incoming bytes into 64-byte frames, which are sent to the USB queue for transmission to the Host when they are full. Incomplete frames are also pushed to the USB queue at approximately 100 ms intervals, triggered by USB start-of-frame tokens. Up to 8 x 64-byte frames can be active at any time. If the Host or software fails to receive data fast enough, an overrun will occur. When this happens, the last-filled buffer frame will be recycled instead of being sent to the USB queue, and a full frame of data will be lost. To prevent this occurrence, the user must ensure that the CDC data pipe is being read continuously, or the incoming data rate must be reduced.

## 3.2 Power Supply

The kit is powered through the USB port and contains two regulators for generating 3.3V for the debugger and an adjustable regulator for the target. The voltage from the USB connector can vary between 4.4V to 5.25V according to the USB specification, and will limit the maximum voltage to the target. The following figure shows the entire power supply system on the SAM E51 Integrated Graphics and Touch Curiosity Board.

**Figure 3-1. Power Supply Block Diagram**

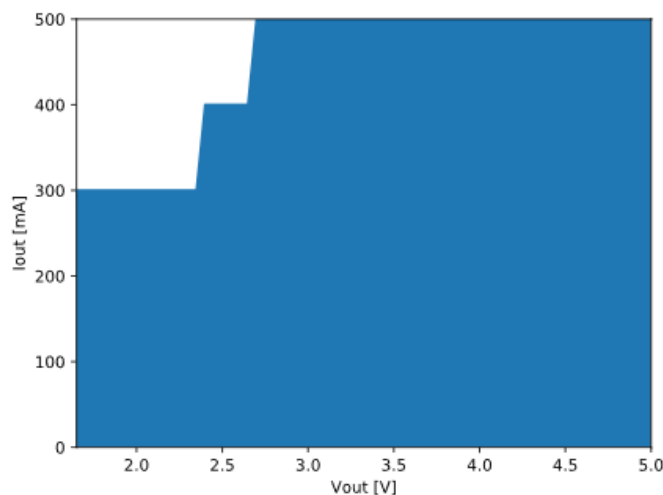


### 3.2.1 Target Regulator

The target voltage regulator is a MIC5353 variable output LDO. The on-board debugger can adjust the voltage output that is supplied to the kit target section by manipulating the feedback voltage of the MIC5353. The hardware implementation is limited to an approximate voltage range from 1.7V to 5.1V. Additional output voltage limits are configured in the debugger firmware to ensure that the output voltage never exceeds the hardware limits of the SAME51J20A microcontroller. The voltage limits configured in the on-board debugger on the SAM E51 Integrated Graphics and Touch Curiosity are 1.7V to 3.6V. The target voltage is set to 3.3V in production and can be changed through MPLAB X IDE. Any change to the target voltage done in MPLAB X IDE is persistent, even through a power toggle.

The MIC5353 supports a maximum current load of 500 mA. It is an LDO regulator in a small package, placed on a small PCB, and the thermal shutdown condition can be reached at lower loads than 500 mA. The maximum current load depends on the input voltage, set output voltage, and the ambient temperature. The following figure shows the safe operation area for the regulator, with an input voltage of 5.1V and an ambient temperature of 23°C.

**Figure 3-2. Target Regulator Safe Operation Area**



### 3.2.2 External Supply

The SAME51J20A Curiosity can be powered by an external voltage instead of the on-board target regulator. When the Voltage Off (VOFF) pin is shorted to ground (GND), the on-board debugger firmware disables the target regulator and it is safe to apply an external voltage to the VTG pin.



Applying an external voltage to the VTG pin without shorting VOFF to GND may cause permanent damage to the kit.



Absolute maximum external voltage is 5.5V for the level shifters on board. Applying a higher voltage may cause permanent damage to the kit.

Programming, debugging, and data streaming is still possible while using external power, as the debugger and signal level shifters will be powered from the USB cable. Both regulators, the debugger, and the level shifters are powered down when the USB cable is removed.

## 4. Hardware

The system block diagram is shown below.

Figure 4-1. System Block Diagram

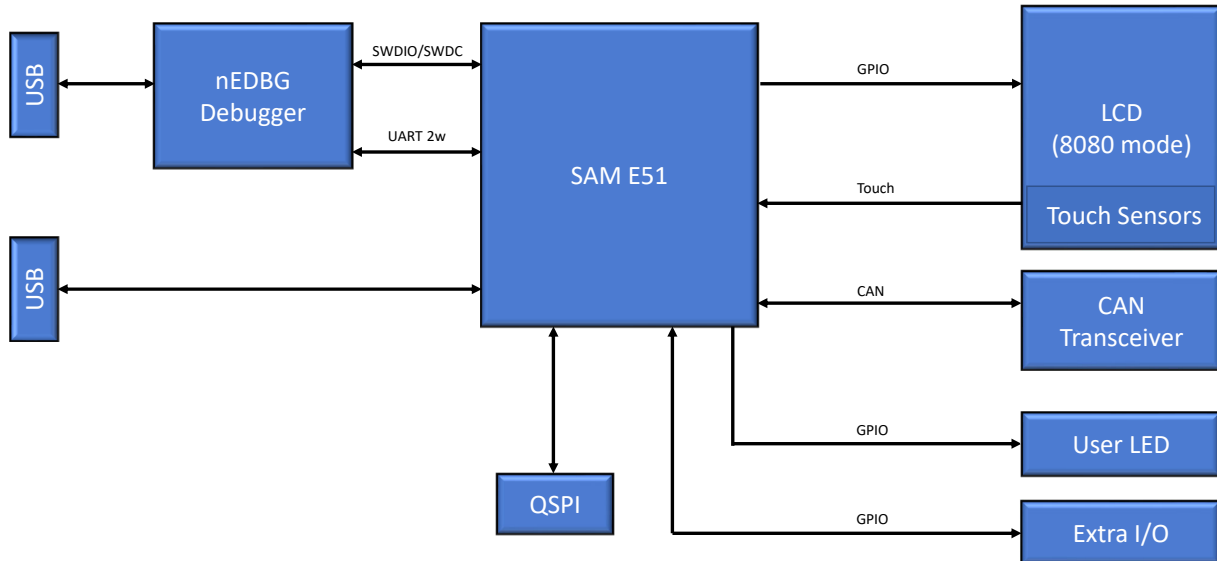


Table 4-1. User Programmable LED

Pin	Port	Function	Other
63	PB02	User LED	Active Low

Table 4-2. Virtual CDC COM Port

Pin	Port	Function	Description
29	PA12	SERCOM2/PAD0	TX from MCU to Debugger
30	PA13	SERCOM2/PAD1	RX to MCU From Debugger

Table 4-3. CAN-FD Interface

Pin	Port	Function
25	PB12	CAN1 TX
26	PB13	CAN1 RX

**Note:** This board has a CAN-FD interface and an on-board transceiver. The transceiver output is available on J204.

Table 4-4. Extra Ports

Pin	Port	Function				
35	PA16	SERCOM1/PAD0	SERCOM3/PAD1	TC2/WO[0]	TCC1/WO[0]	PTC[X10/Y10]
36	PA17	SERCOM1/PAD1	SERCOM3/PAD0	TC2/WO[1]	TCC1/WO[1]	PTC[X11/Y11]
37	PA18	SERCOM1/PAD2	SERCOM3/PAD2	TC3/WO[0]	TCC1/WO[2]	PTC[X12/Y12]
38	PA19	SERCOM1/PAD3	SERCOM3/PAD3	TC3/WO[1]	TCC1/WO[3]	PTC[X13/Y13]
11	PB08	SERCOM4/PAD0	ADC0/AIN[2]	ADC1/AIN[0]	–	PTC[X1/Y1]



.....continued

Pin	Port	Function				
12	PB09	SERCOM4/PAD1	ADC0/AIN[1]	ADC1/AIN[1]	TC4/WO[1]	PTC[X2/Y2]
60	PB31	SERCOM5/PAD0	SERCOM7/PAD1	TC0/WO[1]	TCC4/WO[1]	
59	PB30	SERCOM5/PAD1	SERCOM7/PAD0	TC0/WO[0]	TCC4/WO[0]	

**Notes:**

1. Extra I/O is available on a header that can be used for user defined functions.
2. For additional information on peripherals and pins, refer to the data sheet.

## 5. Hardware Revision History

This user guide provides the latest available revision of the kit. This chapter contains information about known issues, a revision history, and how older revisions differ from the latest revision.

### 5.1 Identifying Product ID and Revision

The revision and product identifier of the SAM E51 Integrated Graphics and Touch Curiosity Boards can be found in two ways: either through MPLAB X IDE or by looking at the sticker on the bottom of the PCB. By connecting a Curiosity board to a computer with MPLAB X IDE running, an information window will pop up. The first six digits of the serial number, which is listed under kit details, contains the product identifier and revision. The same information can be found on the sticker on the bottom of the PCB. Most kits will print the identifier and revision in plain text as A09-nnnn\rr, where nnnn is the identifier and rr is the revision. Boards with limited space have a sticker with only a QR-code, which contains a serial number string. The serial number string has the following format:

"nnnnrrssssssssss"

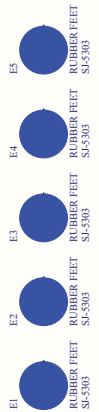
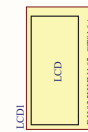
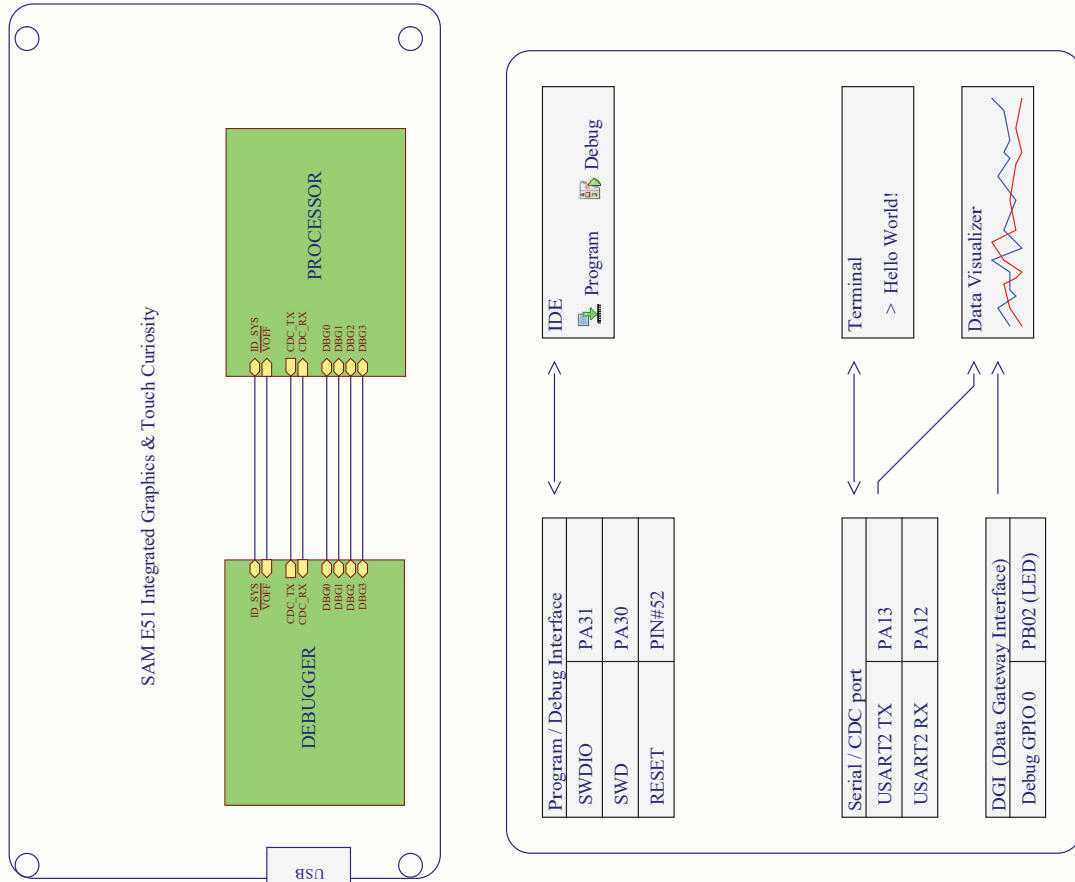
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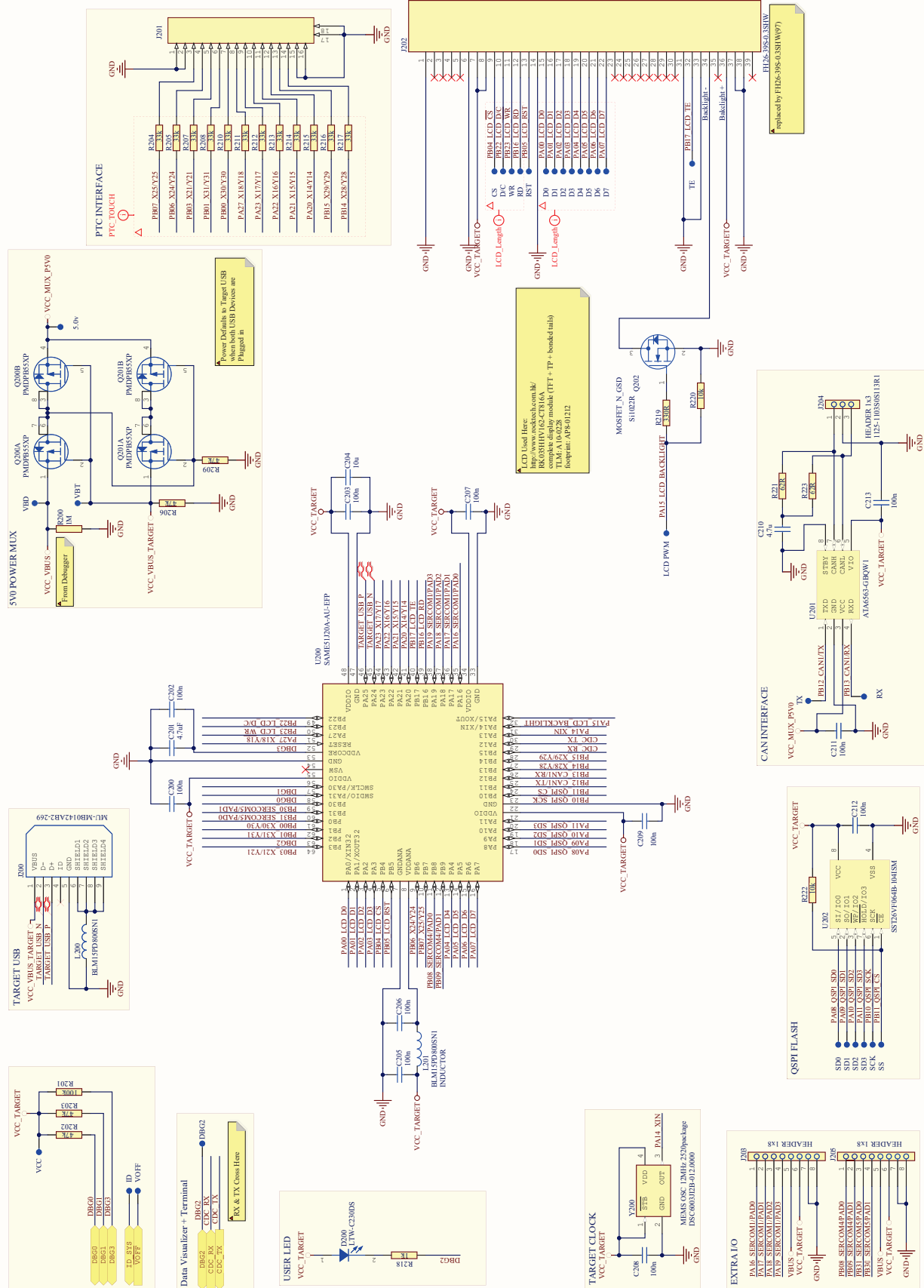
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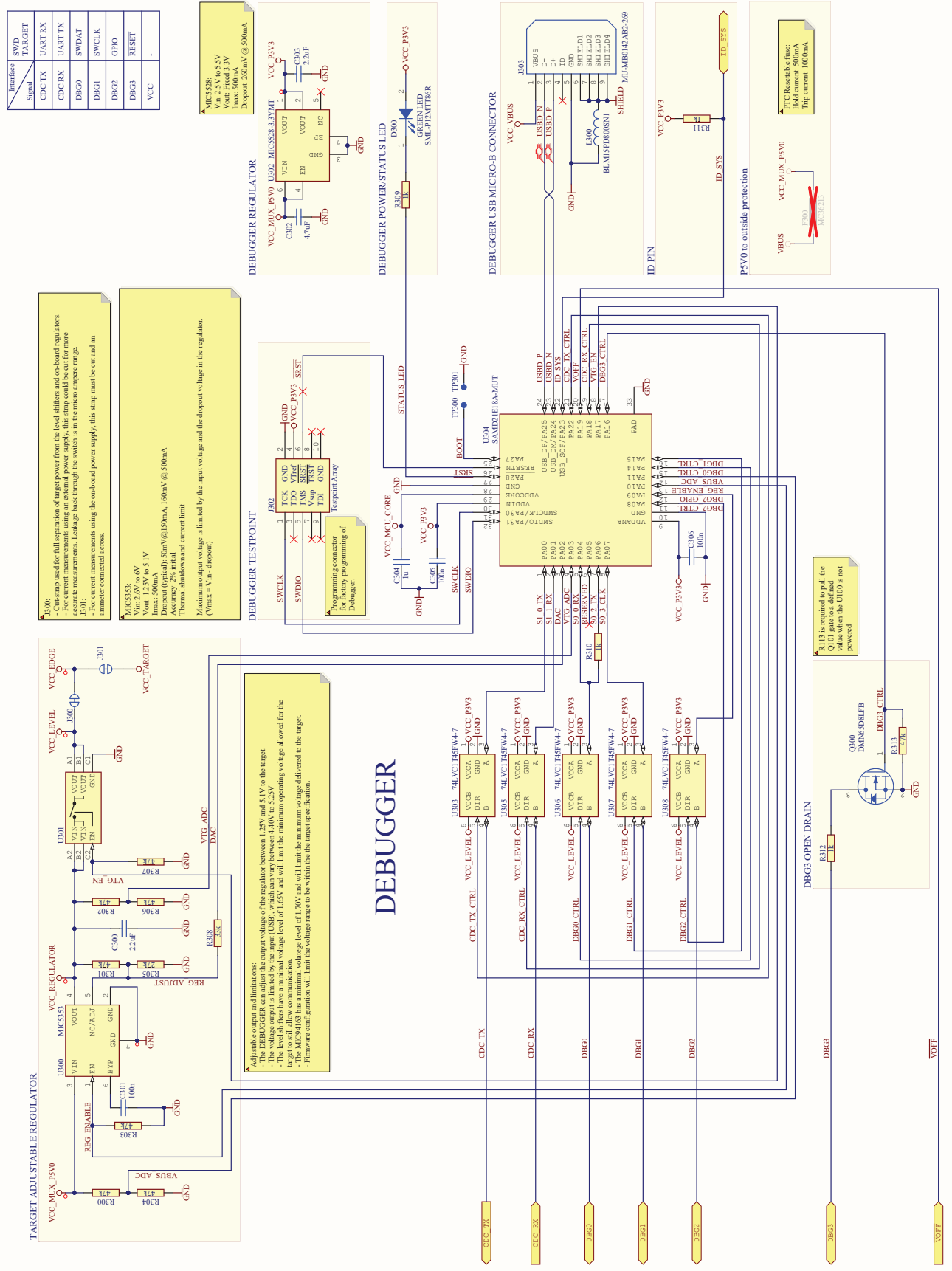
s = serial number

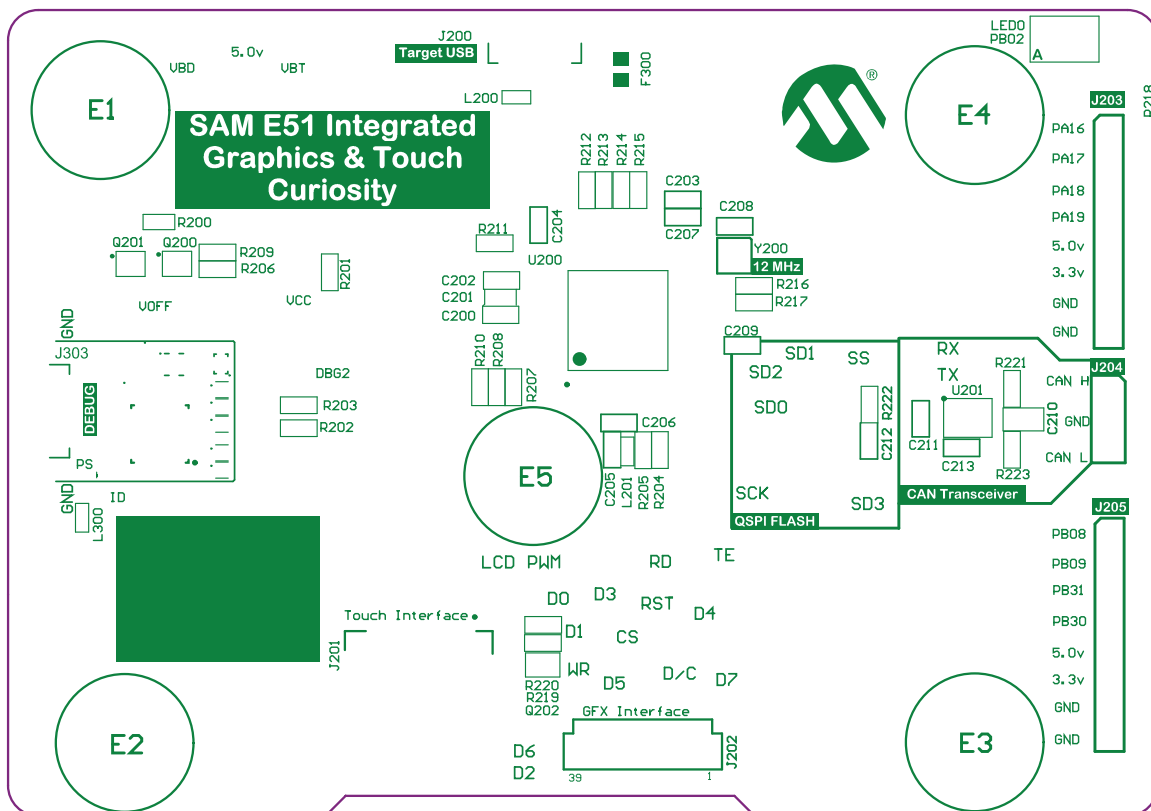
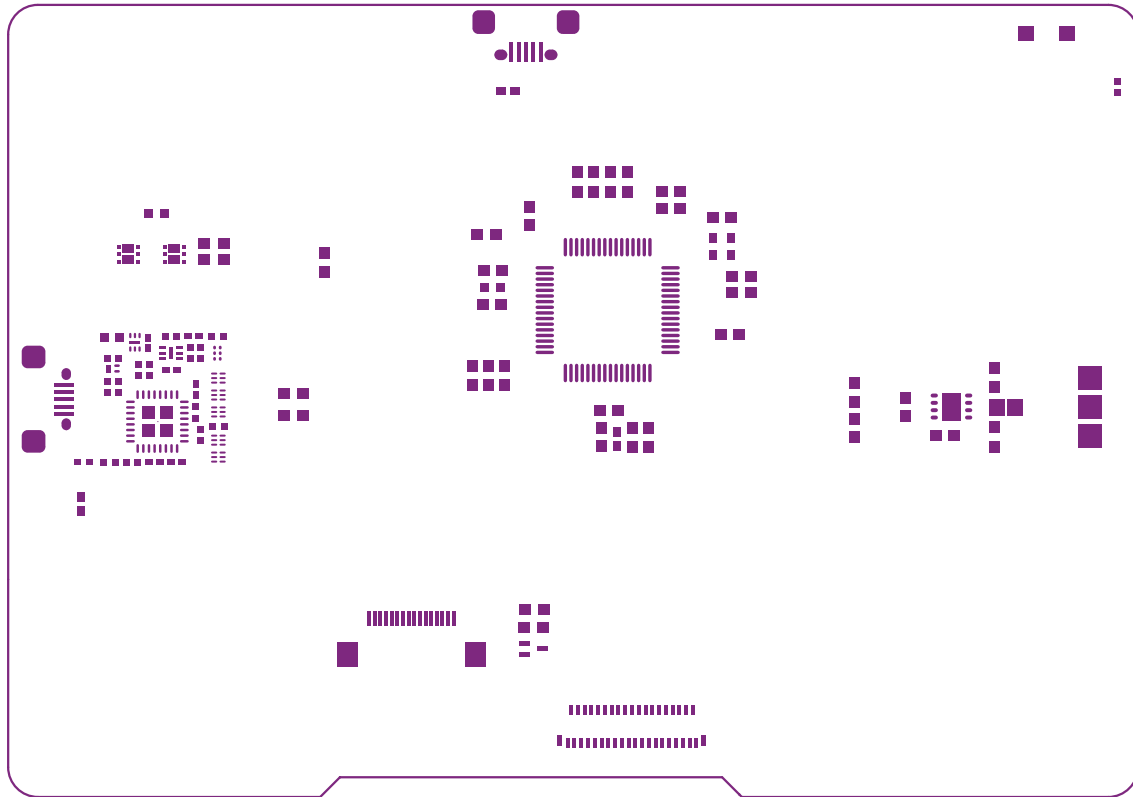
6. Schematics

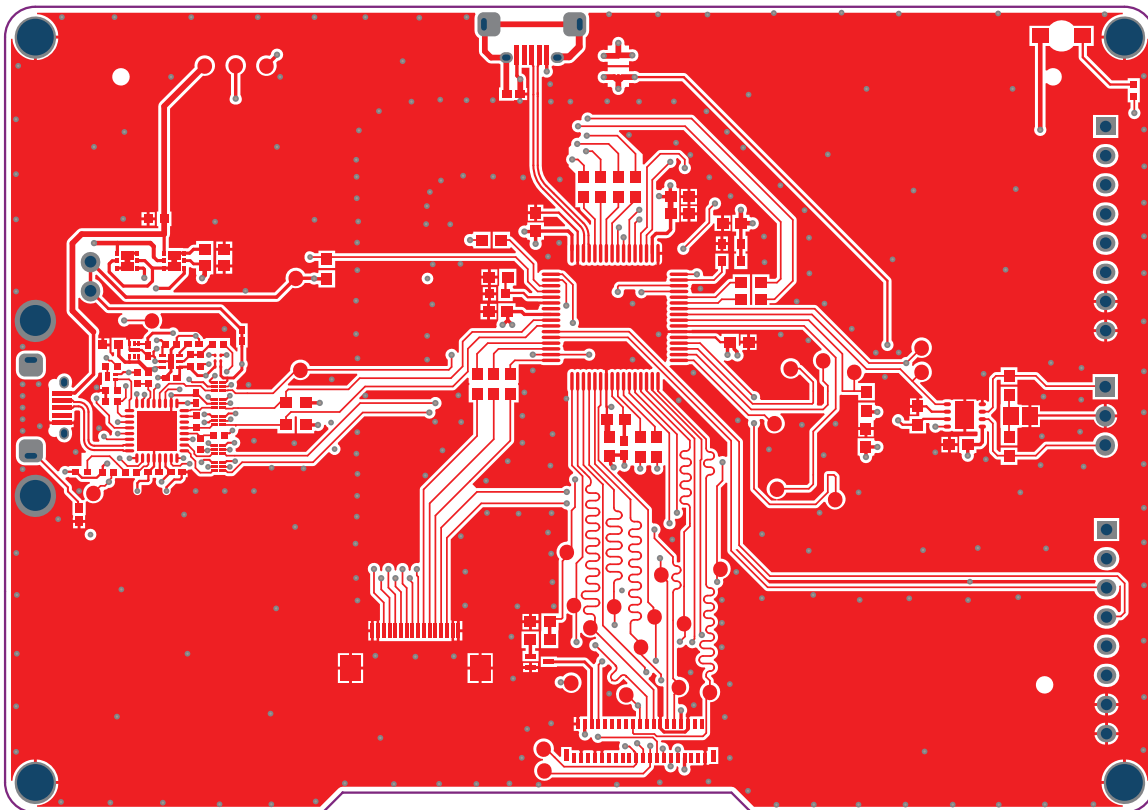
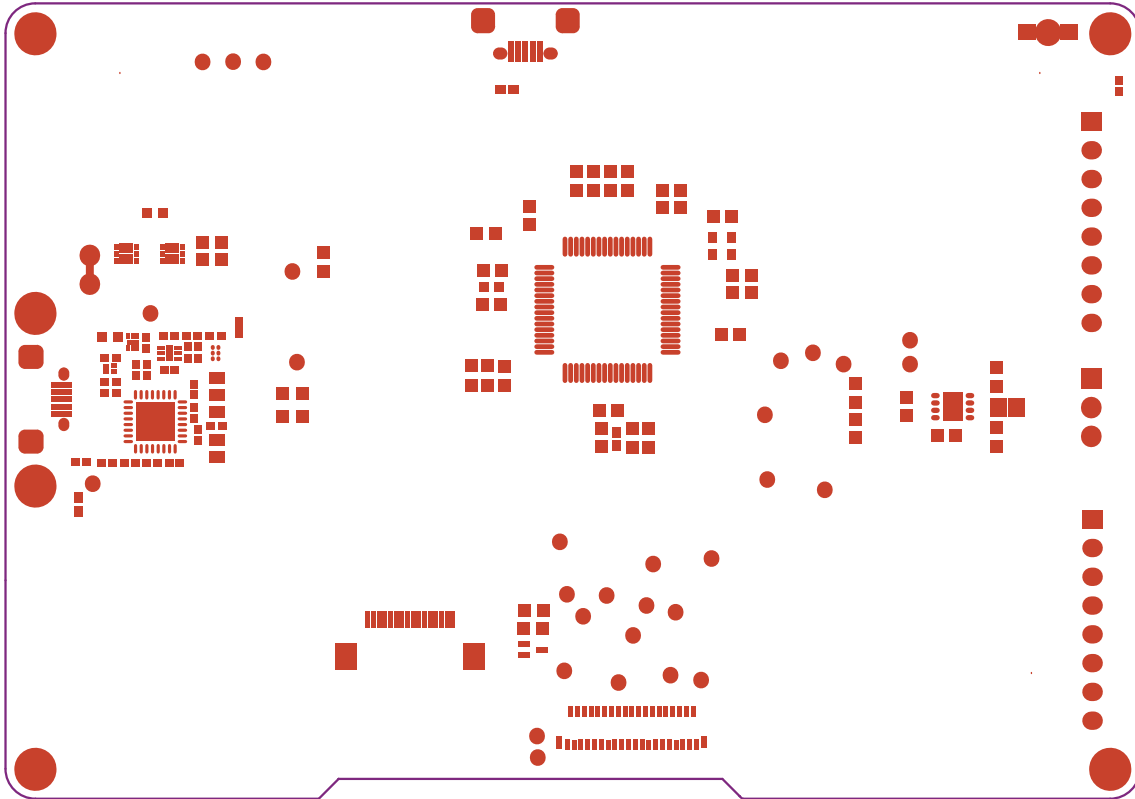
SAM E51 Integrated Graphics & Touch Curiosity

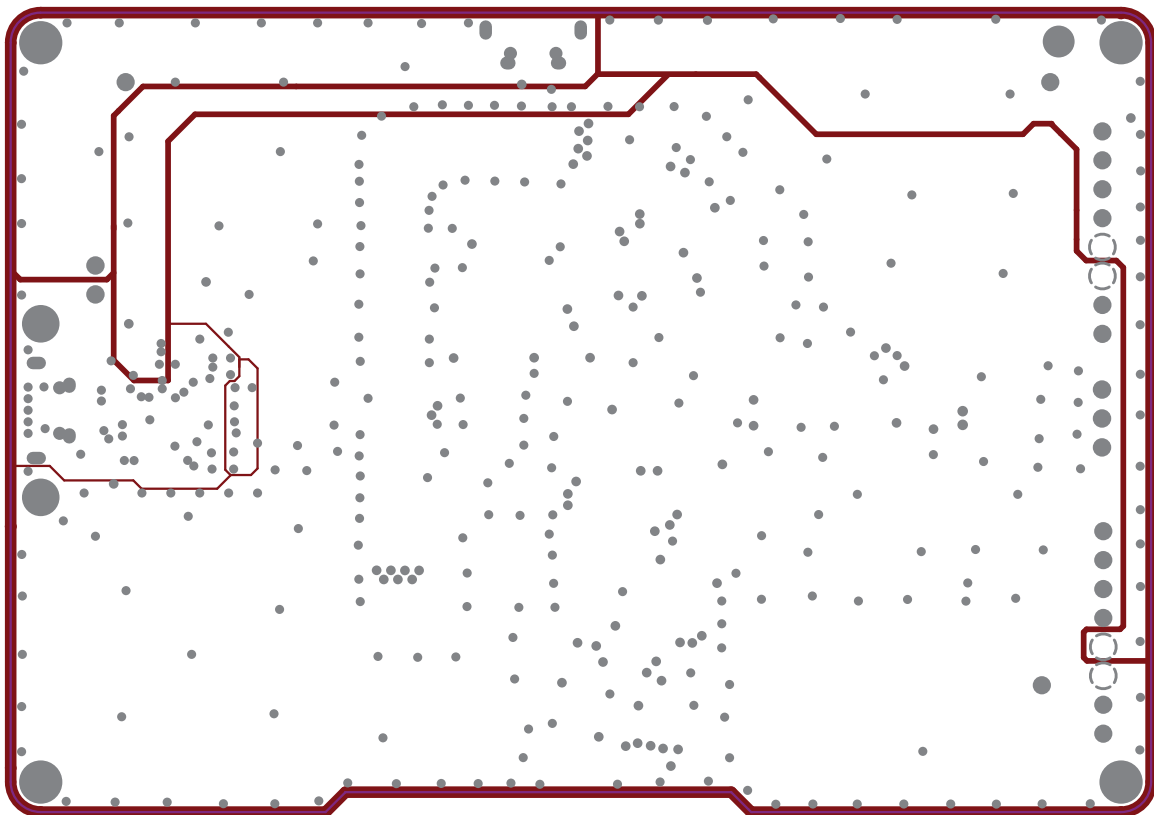




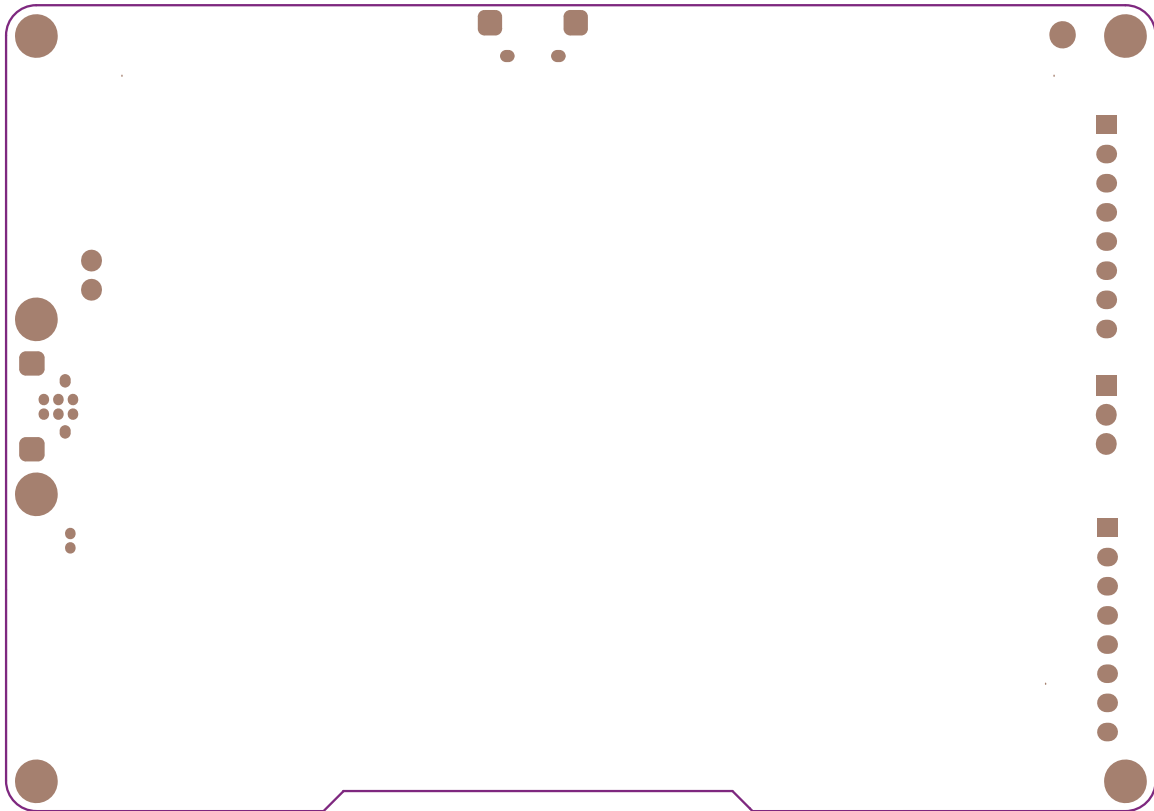
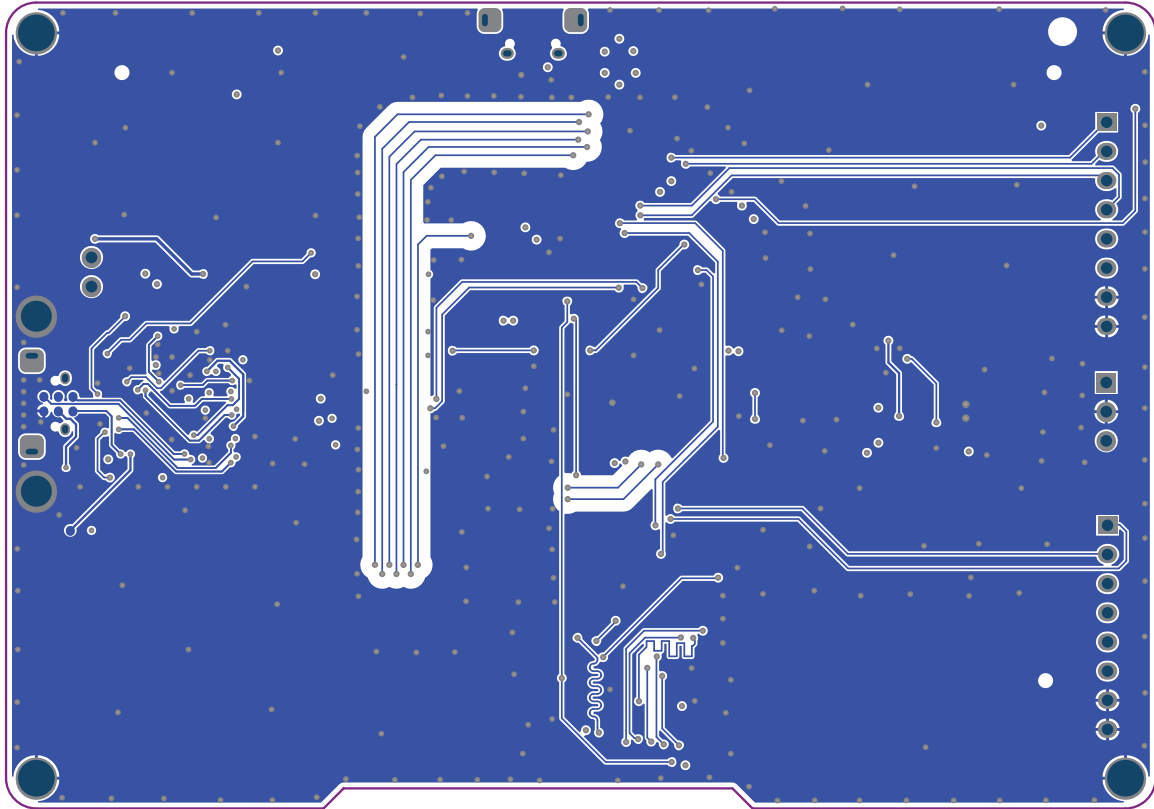


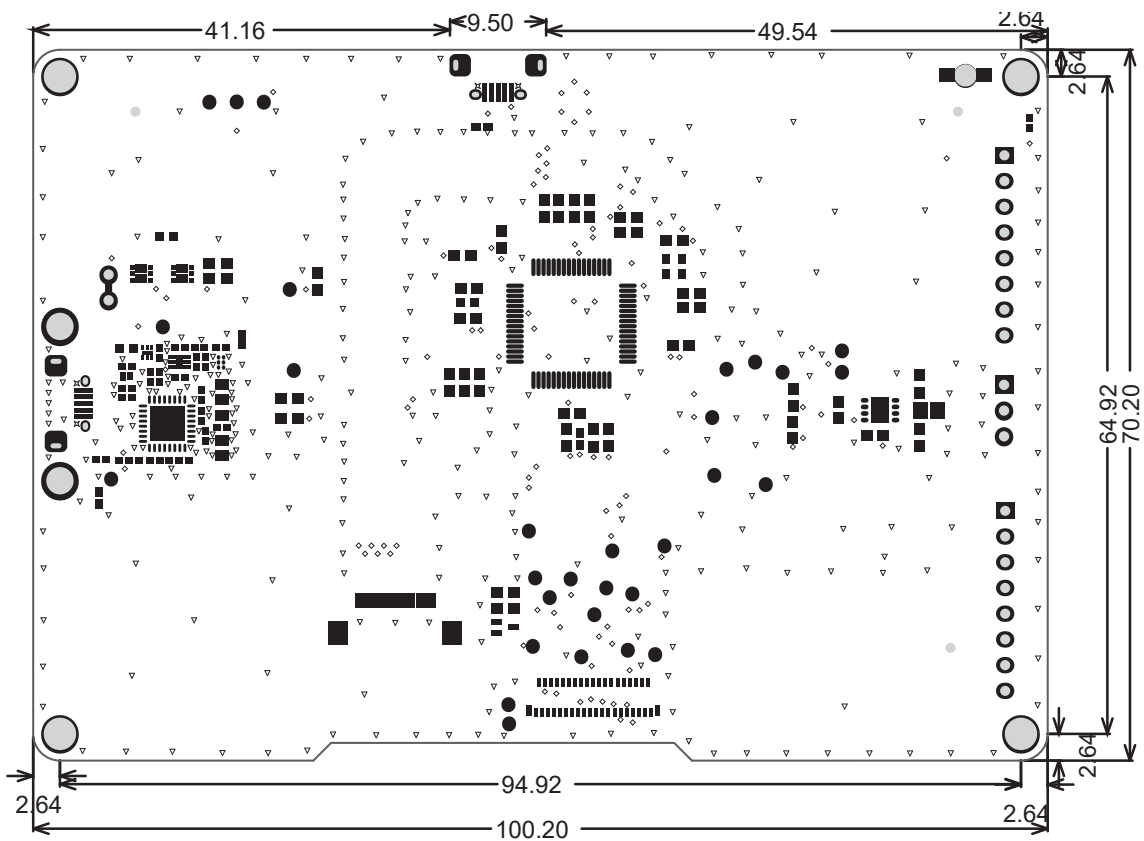








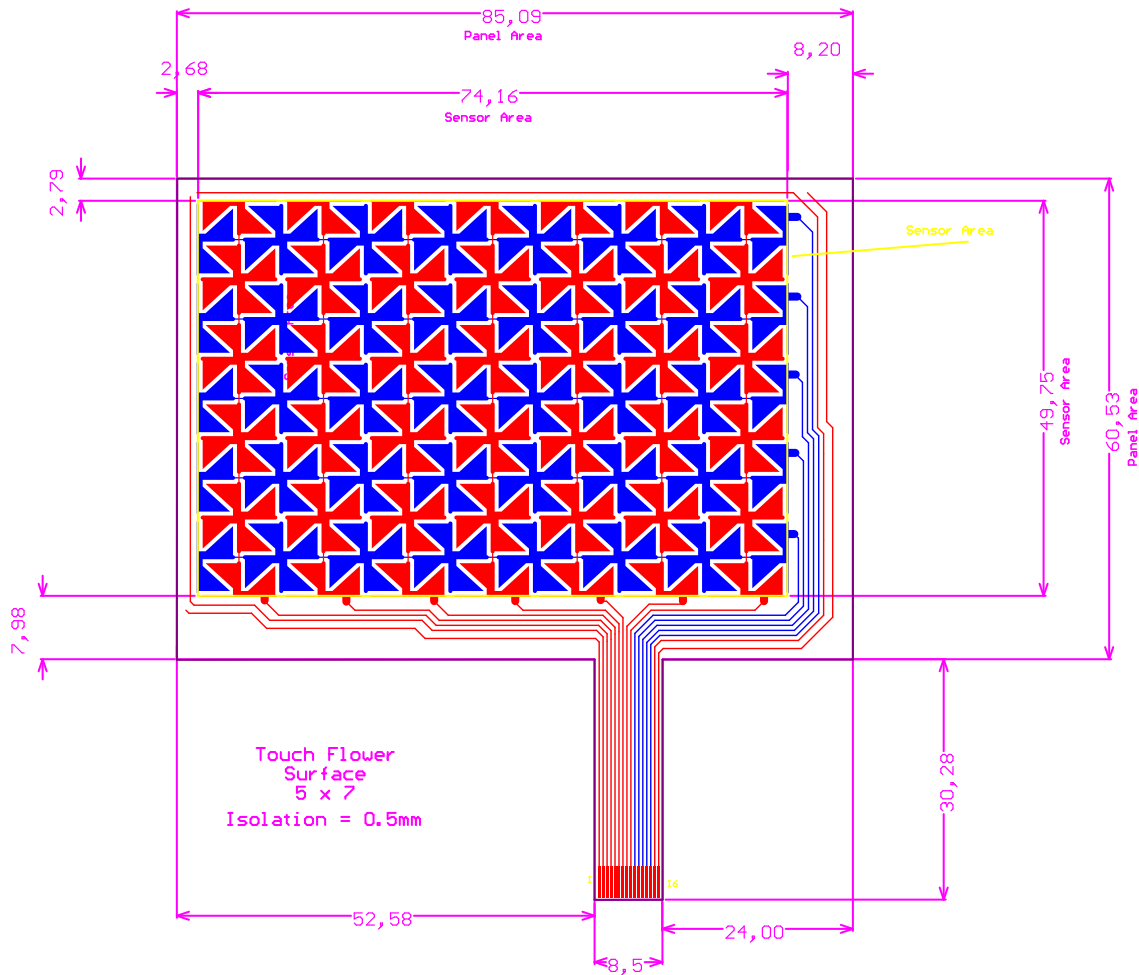




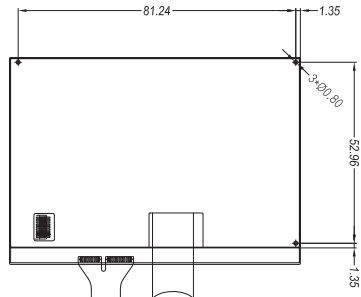
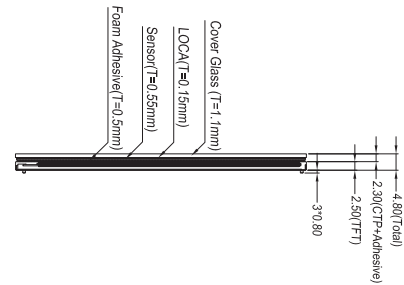
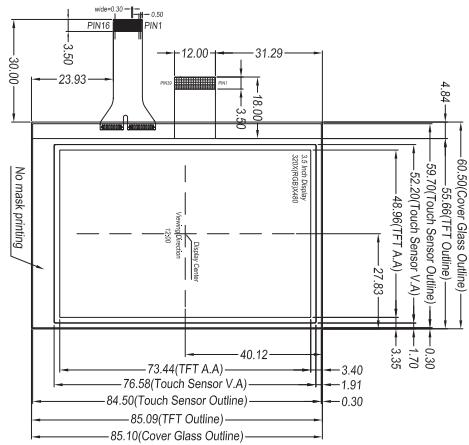
## 7. LCD and Touch Screen Overlay

The demonstration kit comes with a 3.5 inch TFT + CTP display attached. Displays, RK035HHV162-CT675A, can be directly purchased from ROCKTECH®.

The following images are used with permission from ROCKTECH® Displays Limited for describing the LCD and Touchscreen Overlay used on the SAM E51 Integrated Graphics and Touch Curiosity Board. For further information, contact a local Microchip or ROCKTECH® Sales representative.



# EV14C17A LCD and Touch Screen Overlay



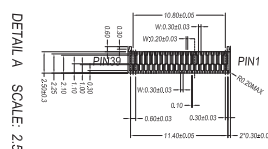
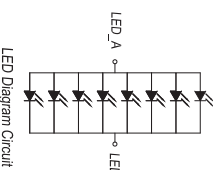
FTF ASSIGNMENT

PIN SYMBOL	END
1	NC
2	NC
3	NC
4	NC
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC
21	NC
22	NC
23	NC
24	NC
25	NC
26	NC
27	NC
28	NC
29	NC
30	NC

CTP ASSIGNMENT

PIN SYMBOL	END
1	NC
2	NC
3	NC
4	NC
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC
21	NC
22	NC
23	NC
24	NC
25	NC
26	NC
27	NC
28	NC
29	NC
30	NC

Display Type	Transmissive, Normally White
Display Resolution	320(RGB)X480
Viewing Angle	12:00
LCD Controller/Driver	ILI9488(FTT)
LCD Driving Voltage	3.3V
Operation Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Backlight Speciality	8PCS LEDs V <sub>f</sub> =3.2V/I <sub>f</sub> =160mA
Remark	Surface hardness: ≥6H, G+G All materials in the drawing comply with the ROHS



TITLE:	LCM Outline		
PROJECT NO.:	RK035HHV162-CT675A		
DESCRIPTION:	3.5 Inch TFT+CTP		
GENERAL TOLERANCE:	±0.2		
<b>Rocktech Displays Limited</b>			
THIRD ANGLE PROJECTION			
DRAWN	NAME	SIGN	DATE
CHECKED	Marvin Zhou		2019.03.04
REV.: 1.0	UNIT: MM	SCALE: 1/1	SHEET: 1 OF 1

ISSUE	MODIFY DESCRIPTION	DATE
1.0	First Issue	2019.03.04

## **8. Revision History**

### **Revision A - May 2021**

This is the initial released version of this document.

Terminology used in this document may not match with the contents of the current revision of the device data sheet or other Microchip documentation and collateral. If there are any questions or concerns regarding terminology, contact a Microchip support or sales representative.

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- Embedded Solutions Engineer (ESE)
- Technical Support

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## Microchip Devices Code Protection Feature

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- Microchip is willing to work with any customer who is concerned about the integrity of its code.
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ISBN: 978-1-5224-8188-1

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