晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AC-404AYILY-88H-B**
APPROVED BY	
DATE	

☐ **Preliminary Specification**
■ **Formal Specification**

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APPROVED BY	CHECKED BY	ORGANIZED BY
Kokai	Mark	Lawlite

This Specification is subject to change without notice.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2024/01/02	-	New Release	Lawlite

1 FEATURES

(1) Display format : 40 characters × 4 lines

(2) Construction : LCD, Bezel, Yellow-green LED back-light and PCB.

(3) Display type : STN , Yellow-green mode ,Transflective , 6 o'clock view.

(4) Controller : ST7066U-0B or Equivalent.

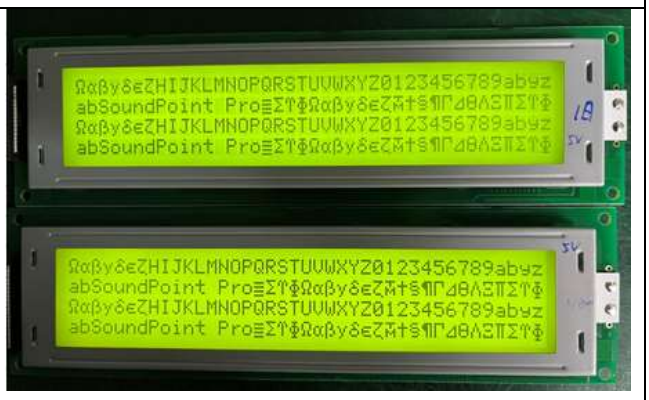
(5) 5V single power input.

(6) Extended temperature type.

(7) Excellent LC : VOP maintains at 5V for whole temp. range, no need extra temp. compensation circuit.

(8) ROHS compliant.

(9) New polarizer :

Top : Old polarizer Bottom: New polarizer	Top : Old polarizer: Polarizer Transmittance 11.96% Bottom: New polarizer. Transmittance 14.51%
	

2 MECHANICAL DATA

Parameter	Stand Value	Unit
Dot size	0.50(W) × 0.55(H)	mm
Dot pitch	0.57(W) × 0.62(H)	mm
Character size	2.78(W) × 4.89(H)	mm
Viewing area	147.0(W) × 29.5(H)	mm
Module size (LED back-light)	190.0(W) × 54.0(H) × 14.0 max (T)	mm

3 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Min	Max	Unit
Logic Circuit Supply Voltage		VDD-VSS	-0.3	7.0	V
LCD Driving Voltage		VDD-VO	-0.3	10.0	V
Input Voltage		VI	-0.3	VDD+0.3	V
Extended temp. type	Operating Temp.	TOP	-20	70	°C
	Storage Temp.	TSTG	-30	80	°C

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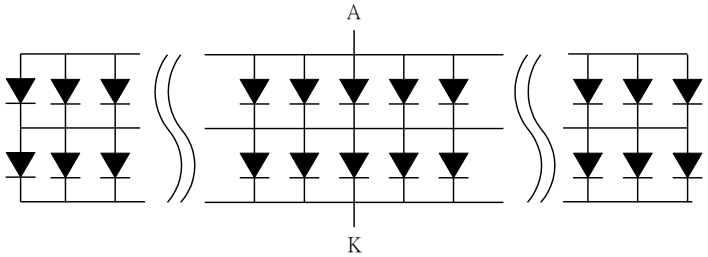
5 ELECTRO-OPTICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
----- Electronic Characteristics -----							
Logic Circuit Supply Voltage	VDD-VSS	--	4.5	5.0	5.5	V	
LCD Driving Voltage	VDD-VO	-20 °C	4.75	5.0	5.25	V	
		25 °C	4.75	5.0	5.25		
		70 °C	4.75	5.0	5.25		
Input Voltage	VIH	--	0.7 VDD	--	VDD	V	
	VIL	--	VSS	--	0.3 VDD	V	
Logic Supply Current	IDD	VDD = 5V	--	3.0	4.0	mA	
----- Optical Characteristics -----							
Contrast	CR	25°C	--	5	--		Note 1
Rise Time	tr	25°C	--	150	200	ms	Note 2
Fall Time	tf	25°C	--	150	200	ms	
Viewing Angle Range	θ f	25°C & CR≥2	--	35	--	Deg.	Note 3
	θ b		--	30	--		
	θ l		--	30	--		
	θ r		--	30	--		
Frame Frequency	fF	25°C	--	64	--	Hz	
----- LED Back-light Characteristics -----							
Forward Voltage	VF	IF=600mA	--	4.1	4.3	V	Supply Voltage between A&K
Forward Current	IF	--	--	600	--	mA	
LCM Luminous intensity		IF=600mA	20	30	--	cd/m^2	

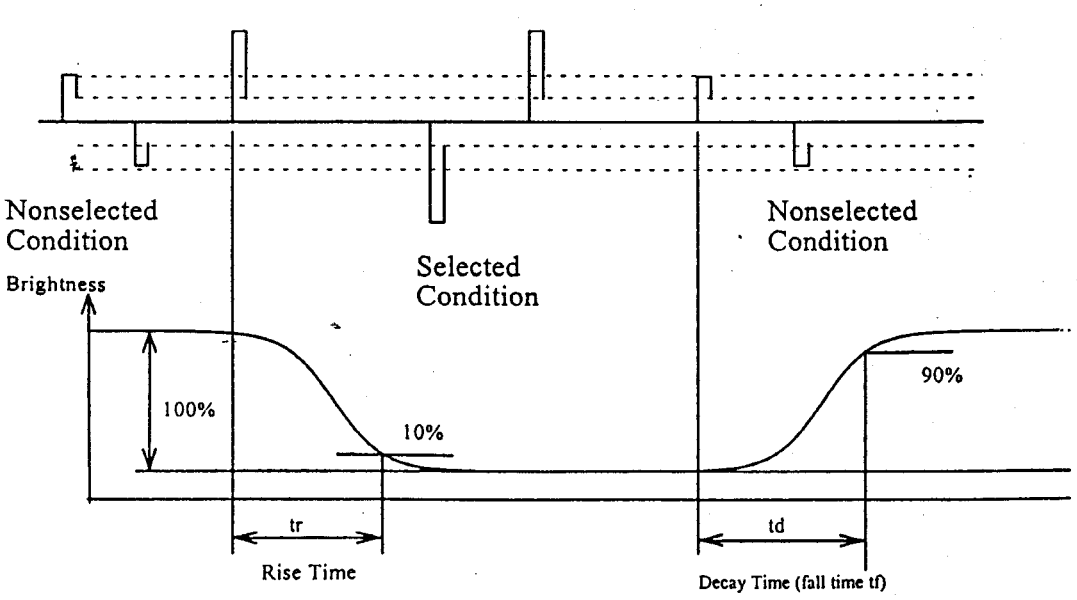
* LED Dice number = 2×60=120



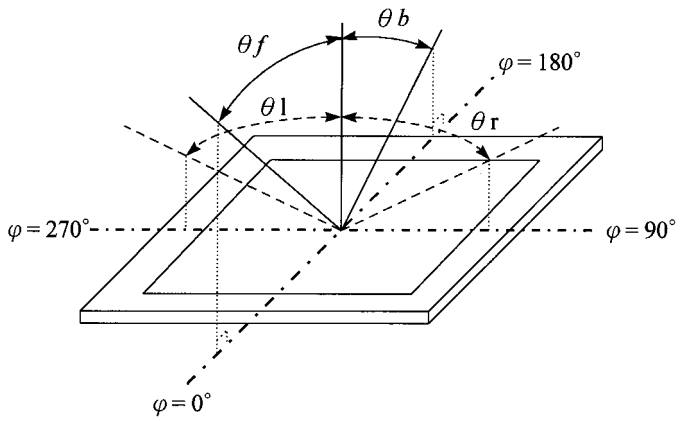
(NOTE 1) Contrast ratio :

CR = (Brightness in ON state) / (Brightness in OFF state)

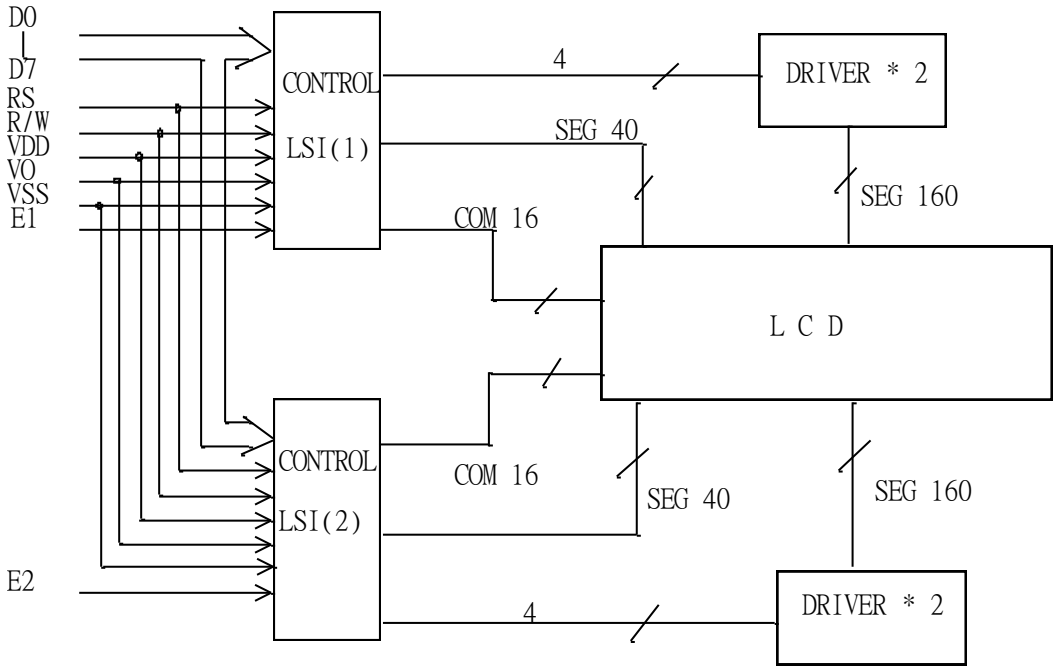
(NOTE 2) Response time :



(NOTE 3) Viewing angle

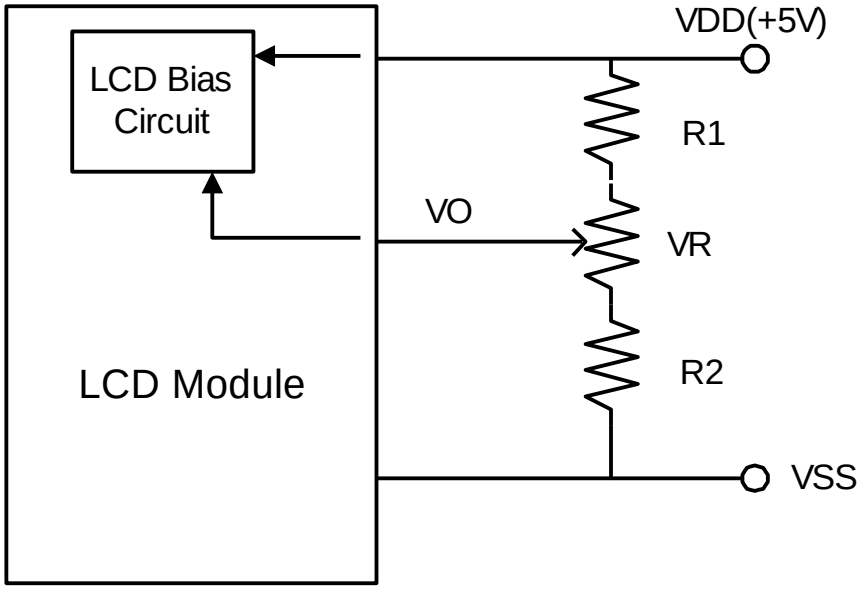


6 BLOCK DIAGRAM & INTERFACE

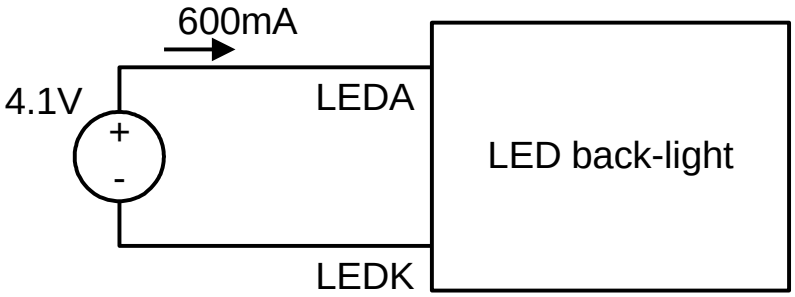


No.	Symbol	Function
1	DB7	Data Bus
2	DB6	Data Bus
3	DB5	Data Bus
4	DB4	Data Bus
5	DB3	Data Bus
6	DB2	Data Bus
7	DB1	Data Bus
8	DB0	Data Bus
9	E1	Enable Signal
10	R/W	Read/Write Select
11	RS	Data/Instruction Select
12	VO	Contrast Adjustment
13	VSS	Ground (0V)
14	VDD	Supply Voltage for Logic (+5V)
15	E2	Enable Signal (no pull-up resistor)
16	NC	No connection

7 POWER SUPPLY



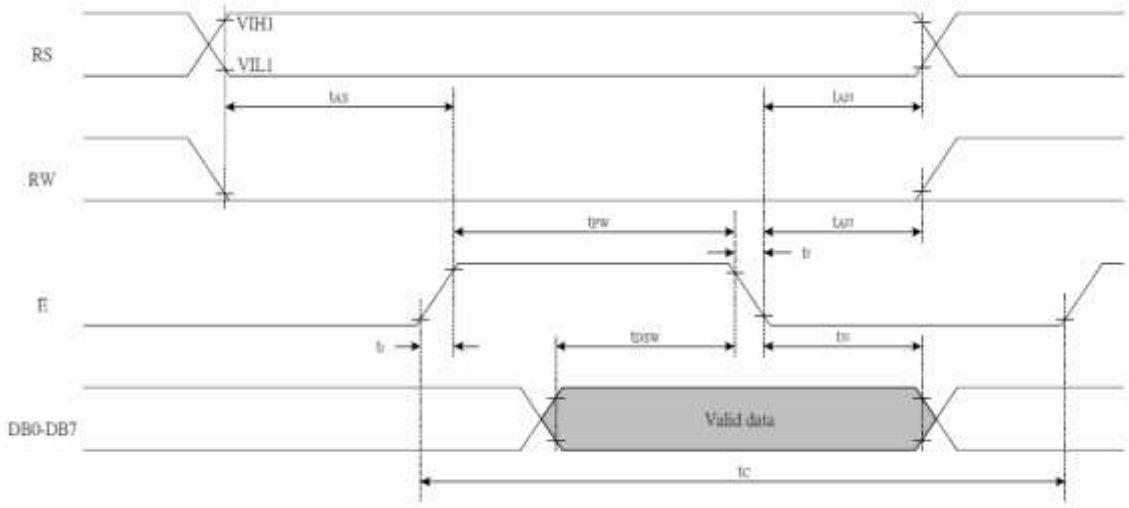
5V Power Input



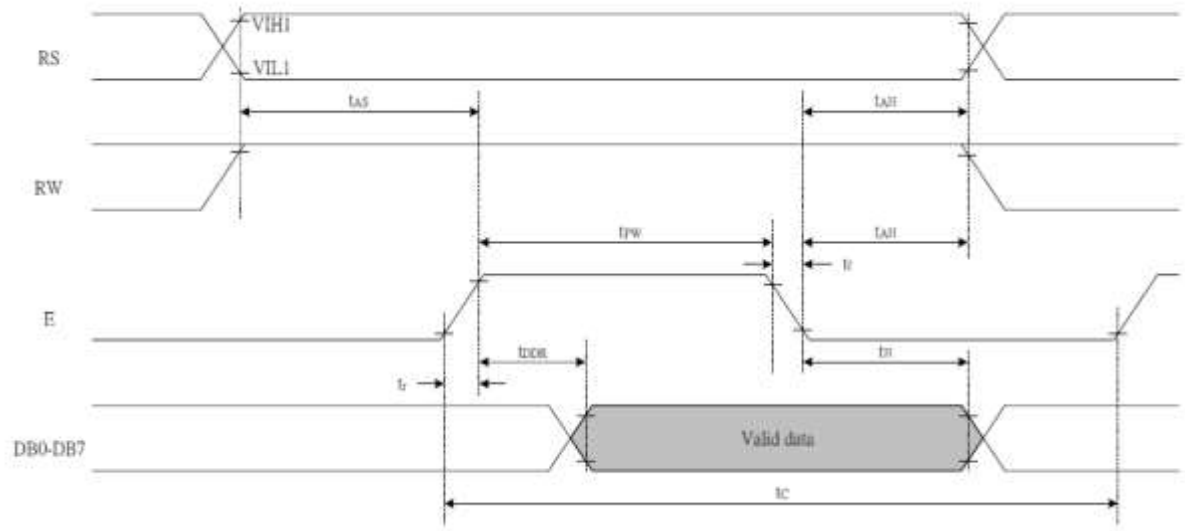
8 TIMING CHARACTERISTICS

7.1 Timing Characteristics

Write Operation



Read Operation



7.2 AC Characteristics

(TA=25℃, VCC = 5V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
Internal Clock Operation						
f_{OSC}	OSC Frequency	R = 91KΩ	190	270	350	KHz
External Clock Operation						
f_{EX}	External Frequency	-	125	270	410	KHz
	Duty Cycle	-	45	50	55	%
T_R, T_F	Rise/Fall Time	-	-	-	0.2	μs
Write Mode (Writing data from MPU to ST7066U)						
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise/Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins: DB0 - DB7	40	-	-	ns
T_H	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
Read Mode (Reading Data from ST7066U to MPU)						
T_C	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise/Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins: DB0 - DB7	-	-	100	ns
T_H	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
Interface Mode with LCD Driver(ST7065)						
T_{CWH}	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
T_{CWL}	Clock Pulse with Low	Pins: CL1, CL2	800	-	-	ns
T_{CST}	Clock Setup Time	Pins: CL1, CL2	500	-	-	ns
T_{SU}	Data Setup Time	Pin: D	300	-	-	ns
T_{DH}	Data Hold Time	Pin: D	300	-	-	ns
T_{DM}	M Delay Time	Pin: M	0	-	2000	ns

9 INSTRUCTION SET

Instruction Table:

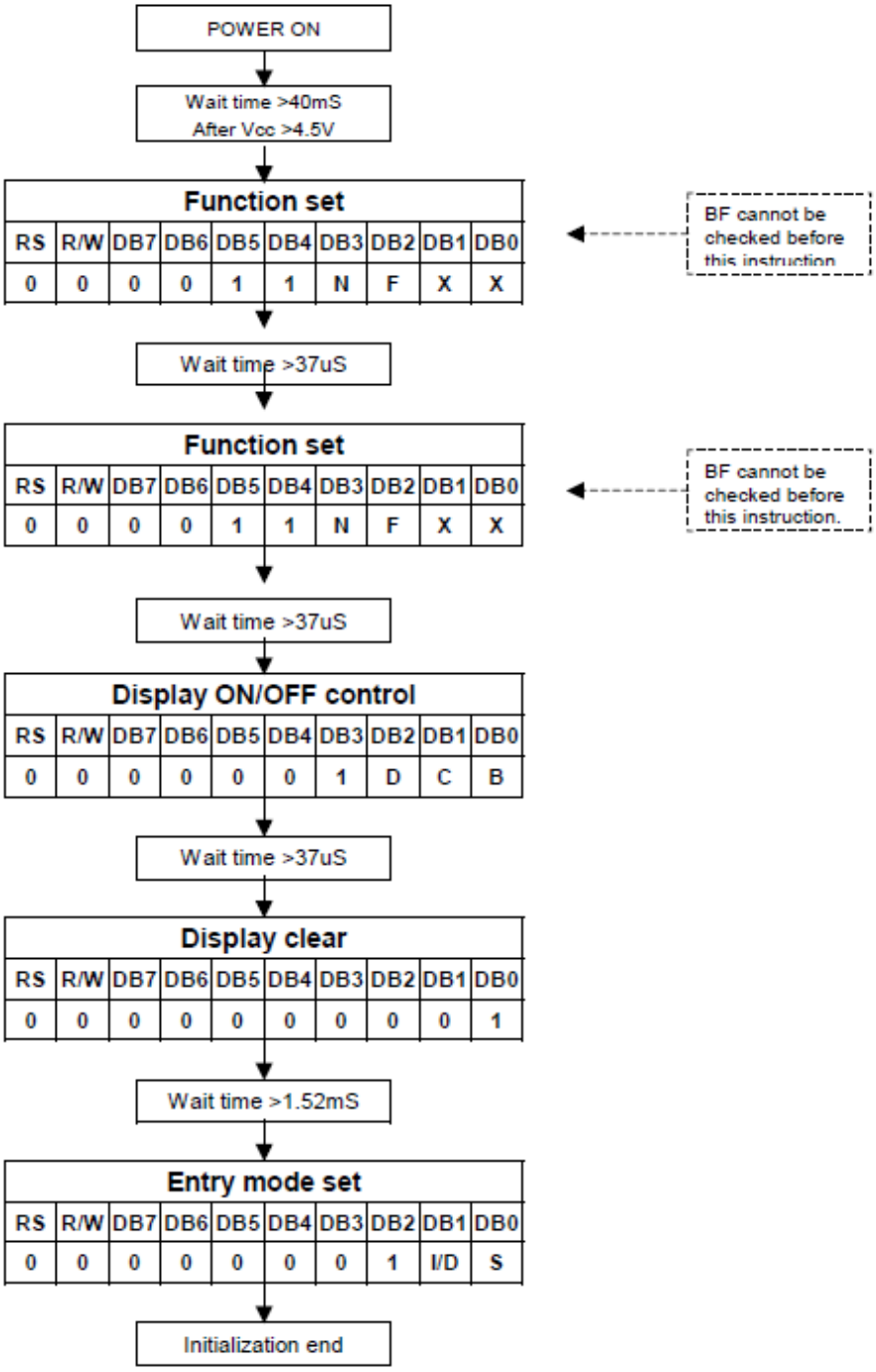
Instruction	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.52 ms
Return Home	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52 ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37 us
Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D=1:entire display on C=1:cursor on B=1:cursor position on	37 us
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	37 us
Function Set	0	0	0	0	1	DL	N	F	x	x	DL:interface data is 8/4 bits N:number of line is 2/1 F:font size is 5x11/5x8	37 us
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter	37 us
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	37 us
Read Busy flag and address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 us
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM)	37 us
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM)	37 us

Note:

Be sure the ST7066U is not in the busy state (BF = 0) before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to Instruction Table for the list of each instruction execution time.

10 INITIALIZATION SEQUENCE

- 8-bit Interface (fosc=270KHz)



11 DD RAM ADDRESS

	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0

Set DDRAM address to AC.

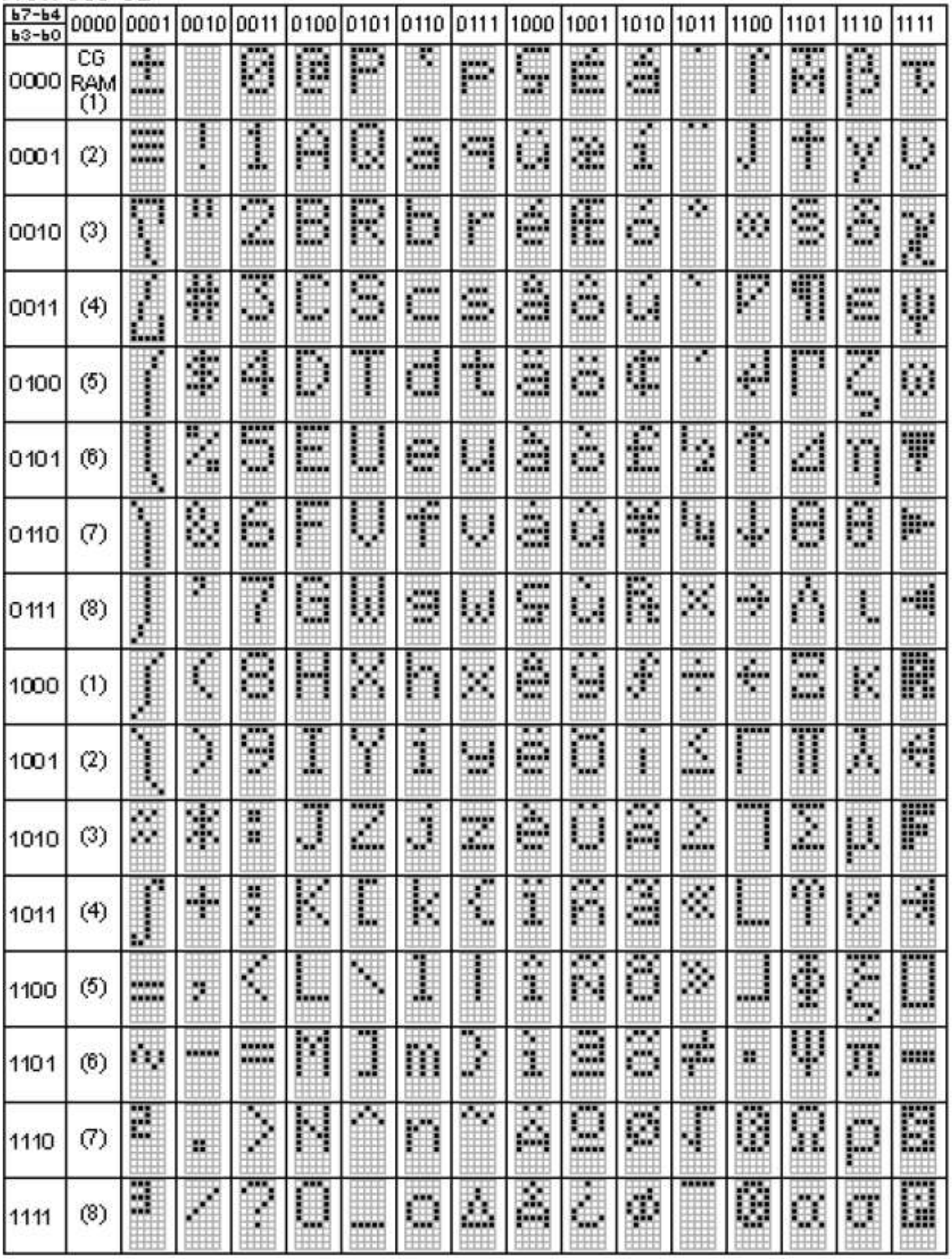
This instruction makes DDRAM data available from MPU.

When 1-line display mode (N = 0), DDRAM address is from "00H" to "4FH".

In 2-line display mode (N = 1), DDRAM address in the 1st line is from "00H" to "27H", and DDRAM address in the 2nd line is from "40H" to "67H".

12 FONT TABLE

NO.7066-0B



13 RELIABILITY

The reliability test items and its conditions are shown below.

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) , 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10~55~10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axes Duration : 30 min/each axis	2

Note 1: Condensation of water is not permitted on the module.

Note 2: The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Note 3: The module shouldn't be tested more than one condition, and all the test conditions are independent.

Note 4: All the reliability tests should be done without protective film on the module.

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

14 HANDLING PRECAUTIONS

(1) A LCD module is a fragile item and should not be subjected to strong mechanical shocks.

(2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in color.

(3) Under no circumstances should the position of the bezel tabs or their shape be modified.

(4) Do not modify the display PCB in either shape or positioning of components.

(5) Do not modify or move location of the zebra or heat seal connectors.

(6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.

(7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.

(8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.

(9) Prior to initial power up input signals should not be applied.

(10) Protect the module against static electricity and observe appropriate anti-static precautions.

(11) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

15 OUTLINE DIMENSION