

1.Description

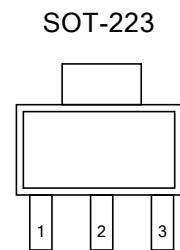
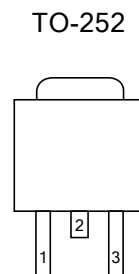
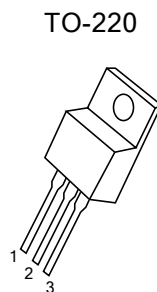
This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 1.5A of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

2.Features

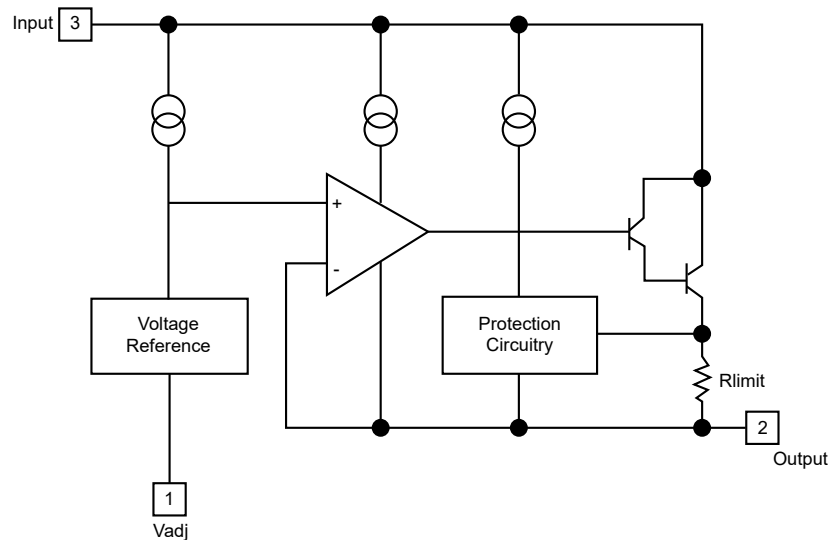
- Internal thermal overload protection
- Internal short circuit current limiting
- Output transistor safe operating area compensation

3.Pinning information

Pin	Symbol	Description
1	A	Adj
2	O	Output
3	I	Input



4.Internal Block Diagram





5. Absolute Maximum Rating

Parameter		Symbol	Value	Units
Input-Output Voltage Differential		$V_I - V_O$	40	V
Lead Temperature		T_{LEAD}	230	°C
Power Dissipation	TO-220	P_D	Internally limited	W
	TO-252		2	W
	SOT-223		1	W
Junction Temperature Range		T_J	-40 to 125	°C
Storage Temperature Range		T_{STG}	-55 to 125	°C
Temperature Coefficient of Output Voltage		$\Delta V_O / \Delta T$	±0.02	%/°C



6. Electrical Characteristics

($V_O - V_I = 5V$, $I_O = 0.5A$, $0^\circ C \leq T_J \leq +125^\circ C$, $I_{MAX} = 1.5A$, $P_{DMAX} = 20W$, unless otherwise specified)

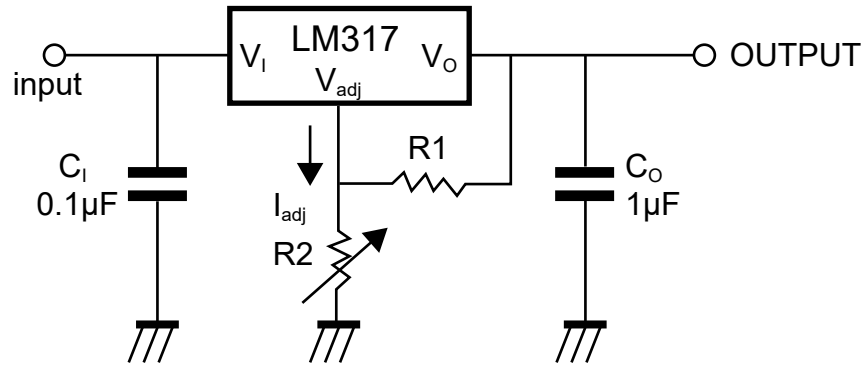
Parameter	Symbol	Conditions	Min	Typ	Max	Units
Line Regulation (note1)	R_{line}	$T_A = 25^\circ C$, $3V \leq V_I - V_O \leq 40V$		0.01	0.04	%V
		$3V \leq V_I - V_O \leq 40V$		0.02	0.07	
Load Regulation (note1)	R_{load}	$T_A = 25^\circ C$, $10mA \leq I_O \leq I_{MAX}$				mV
		$V_O < 5V$		18	25	
		$V_O \geq 5V$		0.4	0.5	
		$10mA \leq I_O \leq I_{MAX}$				%V _O
		$V_O < 5V$		40	70	
		$V_O \geq 5V$		0.8	1.5	
Adjustable Pin Current	I_{ADJ}			46	100	μA
Adjustable Pin Current Change	ΔI_{ADJ}	$3V \leq V_I - V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}$, $P_D \leq P_{MAX}$		2	5	
Reference Voltage	V_{REF}	$3V \leq V_{IN} - V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}$, $P_D \leq P_{MAX}$	1.2	1.25	1.3	V
Temperature Stability	ST_T			0.7		%/V _O
Minimum Load Current to Maintain Regulation	$I_{L(MIN)}$	$V_I - V_O = 40V$		3.5	12	mA
Maximum Output Current	$I_{O(MAX)}$	$V_I - V_O \leq 15V$, $P_D \leq P_{MAX}$	$T_A = 25^\circ C$	2.2		A
		$V_I - V_O \leq 40V$, $P_D \leq P_{MAX}$		0.3		
RMS Noise, % of V _{OUT}	e_N	$T_A = 25^\circ C$, $10Hz \leq f \leq 10KHz$		0.003	0.01	%/V _O
Ripple Rejection	RR	$V_O = 10V$, $f = 120Hz$ without C_{ADJ}	66	60		dB
		$C_{ADJ} = 10\mu F$ (note2)		75		
Long-Term Stability, $T_J = T_{HIGH}$	ST	$T_A = 25^\circ C$ for end point measurements, 1000HR		0.3	1	%
Thermal Resistance Junction to case	$R_{\theta JC}$			25.2		$^\circ C/W$

Notes: 1. Load and line regulation are specified at constant junction temperature. Change in V_D due to heating effects must be taken into account separately. Pulse testing with low duty is used. ($P_{MAX} = 20W$)

2. C_{ADJ} - when used, is connected between the adjustment pin and ground.



7. Typical Application



$$V_O = 1.25V(1 + R_2/R_1) + I_{adj} R_2$$

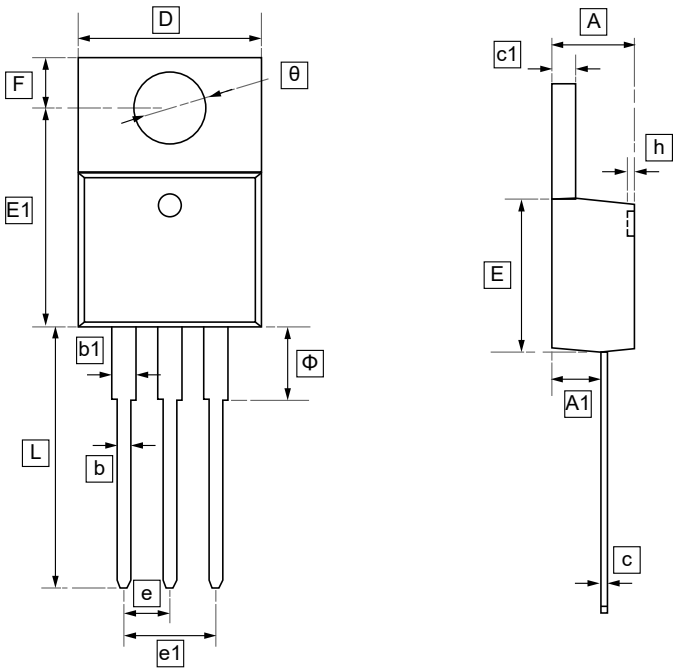
C_i is required when regulator is located an appreciable distance from power supply filter.

C_o is not needed for stability, however, it does improve transient response.

Since I_{ADJ} is controlled to less than $100\mu A$, the error associated with this term is negligible in most applications



8.1TO-220-3L Package Outline Dimensions



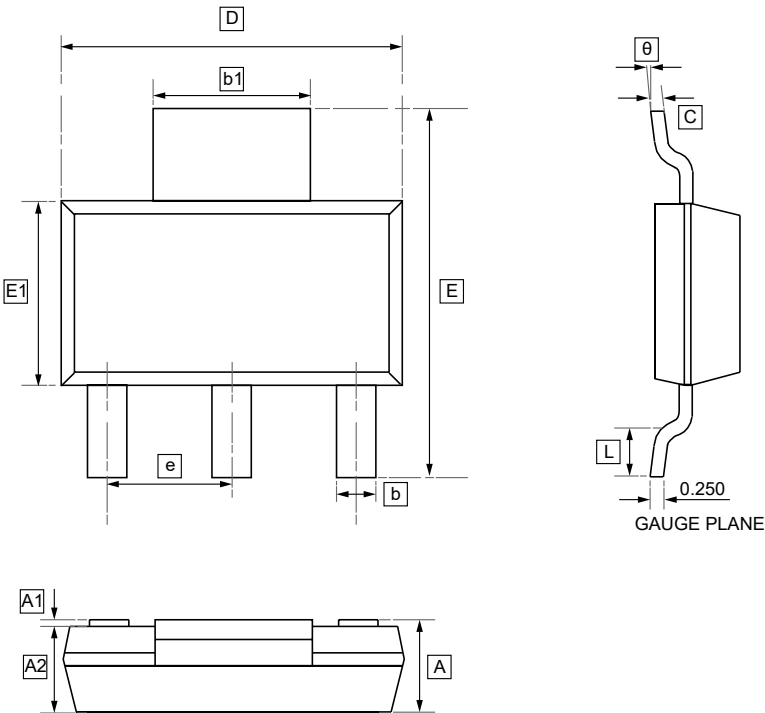
DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	b	b1	c	c1	D	E	E1	e	e1	F	h
Min	4.470	2.520	0.710	1.170	0.310	1.170	10 .010	8.500	12 .060	2 .540	4.980	2.590	0.000
Max	4.670	2.820	0.910	1.370	0.530	1.370	10 .310	8.900	12 .460	TYP	5.180	2.890	0.300

Symbol	L	L1	Φ
Min	13 .400	3.560	3.735
Max	13 .800	3.960	3.935



8.2SOT-223 Package Outline Dimensions

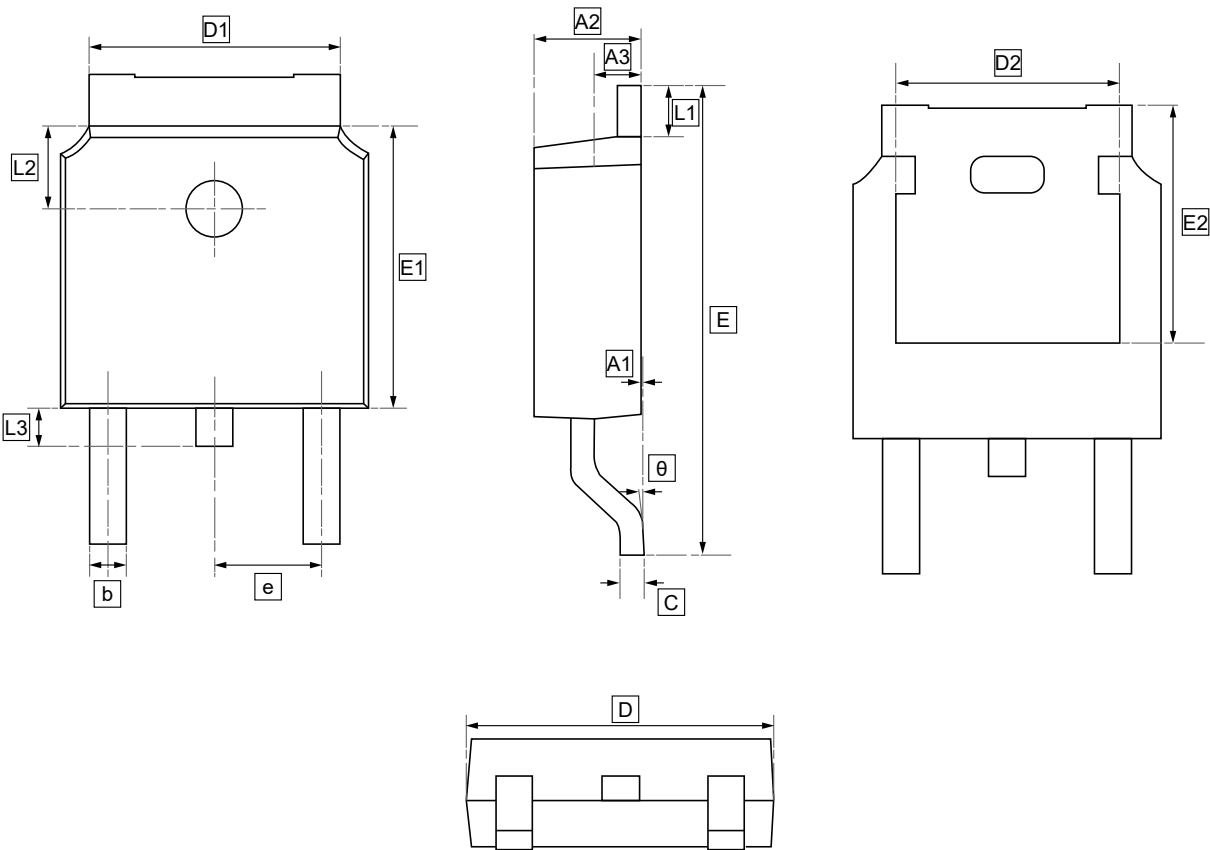


DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	b	b1	c	D	E	E1	e	L	θ
Min	-	0.020	1.500	0.660	2.900	0.230	6.300	6.700	3.300	2.300	0.750	0°
Max	1.800	0.100	1.700	0.840	3.100	0.350	6.700	7.300	3.700	BSC	-	10°



8.3TO-252 Package Outline Dimensions

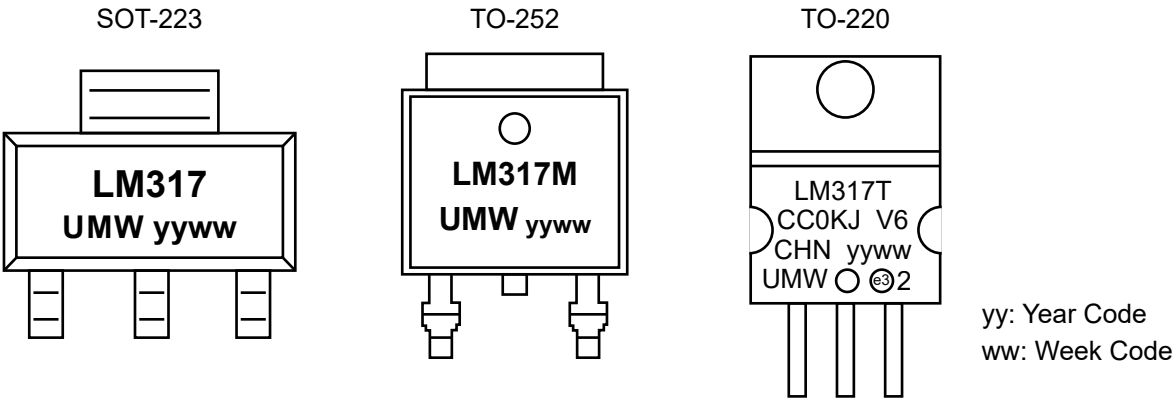


DIMENSIONS (mm are the original dimensions)

Symbol	A1	A2	A3	b	c	D	D1	D2	E	E1	E2	e	L1	L2	L3	θ
Min	0.00	2.18	0.90	0.65	0.46	6.35	4.95	4.32	9.40	5.97	5.21	2.286	0.89	1.70	0.60	0.00
Max	0.13	2.39	1.10	0.85	0.61	6.73	5.46	4.90	10.41	6.22	5.38		BSC	1.27	1.90	1.00



9.Ordering information



Order Code	Marking	Package	Base QTY	Delivery Mode
UMW LM317DCYR	LM317	SOT-223	2500	Tape and reel
UMW LM317G	LM317G	SOT-223	2500	Tape and reel
UMW LM317MDT	LM317M	TO-252	2500	Tape and reel
UMW LM317T	LM317T	TO-220	1000	Tube and box



10.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

When applying our products, please do not exceed the maximum rated values, as this may affect the reliability of the entire system. Under certain conditions, any semiconductor product may experience faults or failures. Buyers are responsible for adhering to safety standards and implementing safety measures during system design, prototyping, and manufacturing when using our products to prevent potential failure risks that could lead to personal injury or property damage.

Unless explicitly stated in writing, UMW products are not intended for use in medical, life-saving, or life-sustaining applications, nor for any other applications where product failure could result in personal injury or death. If customers use or sell the product for such applications without explicit authorization, they assume all associated risks.

When reselling, applying, or exporting, please comply with export control laws and regulations of China, the United States, the United Kingdom, the European Union, and other relevant countries, regions, and international organizations.

This document and any actions by UMW do not grant any intellectual property rights, whether express or implied, by estoppel or otherwise. The product names and marks mentioned herein may be trademarks of their respective owners.