

# 5" 4-Lane LVDS WVGA LCD Display Module With maXTouch<sup>®</sup> Technology

## AC69T88A

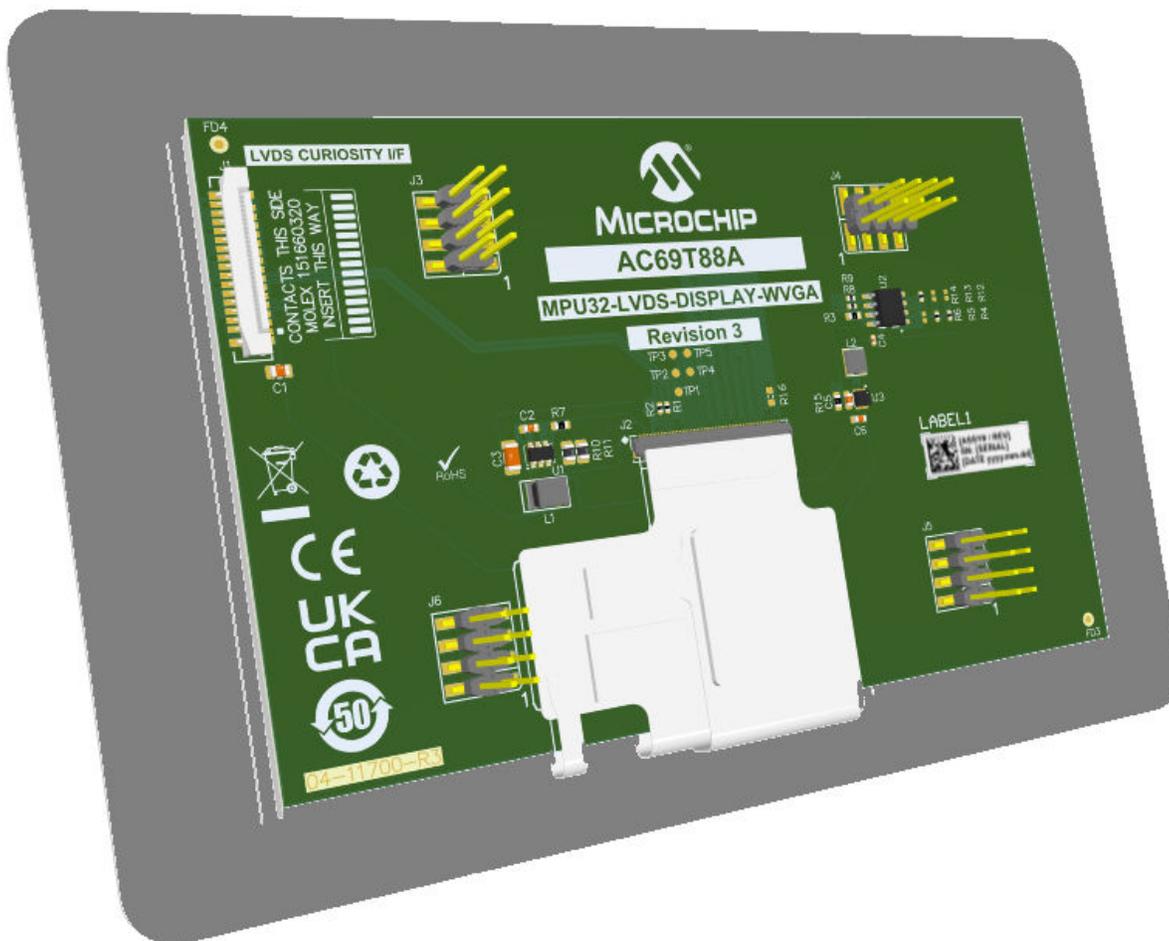


## Preface

The MPU32-LVDS-DISPLAY-WVGA with maXTouch<sup>®</sup> technology (AC69T88A) is designed to evaluate the Microchip's graphics display solution and graphics library for 32-bit microcontrollers and microprocessors.

This AC69T88A is electrically and mechanically compatible with Microchip MPU Curiosity Boards.

The figure below illustrates the AC69T88A maXTouch display module.



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## 1. Introduction

This section describes the features of the Microchip AC69T88A and provides an overview of the 5" 4-Lane LVDS WVGA LCD Display Module kit.

### 1.1 Features

The key features of the Microchip AC69T88A kit are:

- Display
  - 5-inch display module
  - Sitronix ST7262 LCD driver
  - WVGA 800 x 480 resolution
  - 4-lane LVDS serial interface
- Backlight
  - Microchip MIC2289 white LED backlight driver
- Touch
  - Microchip mXT336UD maXTouch capacitive touch screen controller
  - Support for up to 10 touches
  - 1.1 mm soda-lime glass
- Power Supply
  - Microchip MIC23150 buck regulator for 3.3V generation
- Hardware identification system
  - Programmed Microchip 24AA014H 1-Kbit I<sup>2</sup>C EEPROM

#### Kit contents

- One 5-inch display module with its control board
- One 30-way flexible flat cable (FFC)

### 1.2 Kit Overview

The Microchip 5" 4-Lane LVDS WVGA LCD Display Module kit is an extension board for Microchip MPU Curiosity boards, with a 800 x 480 WVGA LCD resolution and a capacitive touch sensor with a maXTouch controller. The LCD is composed of the following interfaces:

- A four-lane LVDS interface
- An I<sup>2</sup>C interface for maXTouch controller and EEPROM identification communications
- A digital backlight brightness control
- Static GPIO for the LCD module control (Module Enable, Display Control and Global Reset)

### 1.3 Additional Resources

For additional information, refer to the information available via these links:

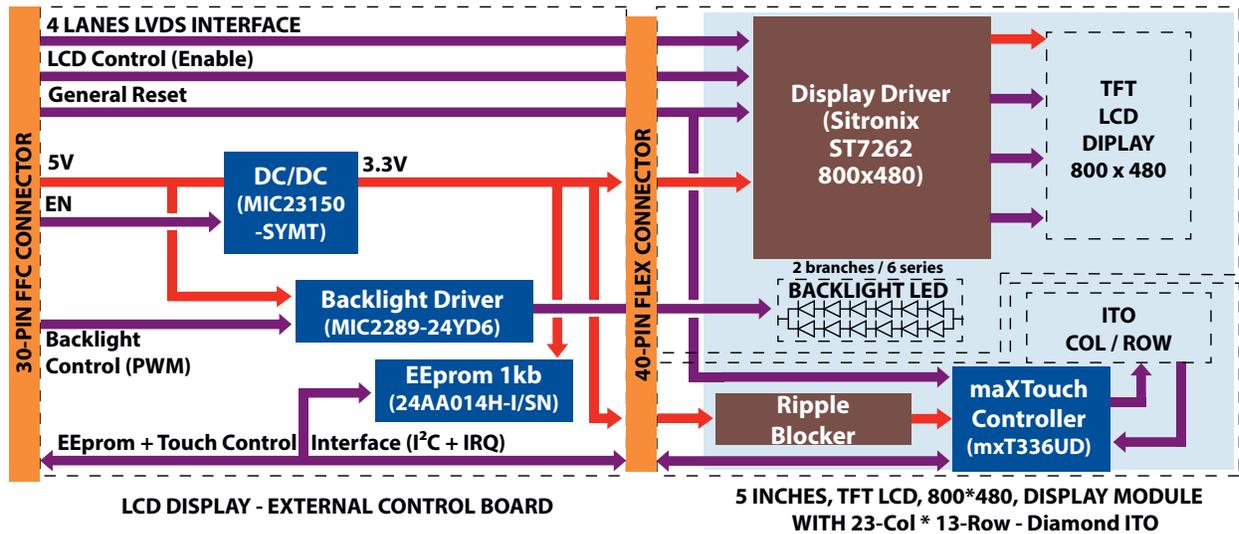
- Sitronix ST7262 LCD Driver IC ([www.sitronix.com.tw/en/index/](http://www.sitronix.com.tw/en/index/))
- Microchip mXT336UD maXTouch<sup>®</sup> Capacitive Touchscreen Controller ([www.microchip.com/en-us/product/ATMXT336UD](http://www.microchip.com/en-us/product/ATMXT336UD))
- Microchip MIC2289 White LED backlight driver ([www.microchip.com/en-us/product/MIC2289](http://www.microchip.com/en-us/product/MIC2289))
- Microchip MIC23150 Synchronous Buck Regulator ([www.microchip.com/en-us/product/MIC23150](http://www.microchip.com/en-us/product/MIC23150))
- Microchip 24AA014H 1 Kbit I<sup>2</sup>C Serial EEPROM ([www.microchip.com/en-us/product/24AA014H](http://www.microchip.com/en-us/product/24AA014H))

## 2. LCD Module Overview

This section covers the specifications of the AC69T88A kit and provides a high-level description of the board's major components and interfaces.

### 2.1 Block Diagram

Figure 2-1. AC69T88A Block Diagram

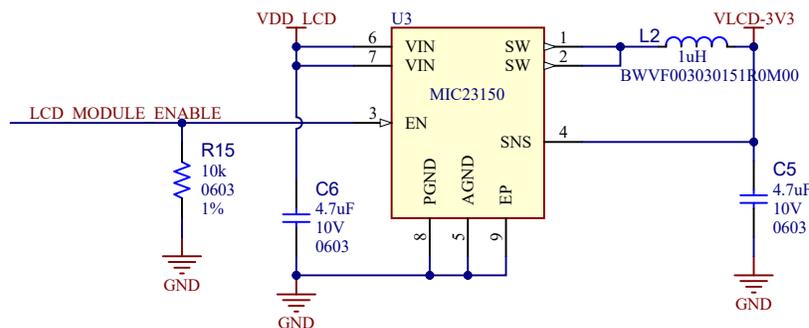


### 2.2 System Power-On/Power-Off

VDD\_LCD is a 5V power input from the external processor board. It must be converted to the 3.3V VLCD\_3V3 power rail for the EEPROM memory and the LVDS display.

The MIC23150 is implemented, which is a synchronous Buck regulator with a HyperLight Load® mode. HyperLight Load technology provides high efficiency at light loads and ultra-fast transient response, making it suitable for supplying processor core voltages.

Figure 2-2. System Power-On/Power-Off



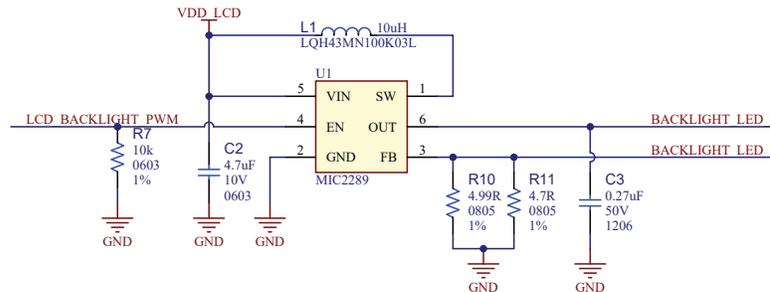
### 2.3 Backlight Control

The backlight management of the LCD Module is ensured by a Microchip MIC2289 PWM boost-switching regulator optimized for constant-current white LED driver applications.

The MIC2289 implements a constant frequency 1.2 MHz PWM control scheme. The added benefit of the constant frequency PWM scheme in comparison to variable frequency is much lower noise and input ripple injected to the input power source.

For more details on the MIC2289 backlight driver, see [Additional Resources](#).

**Figure 2-3.** MIC2289 Backlight Driver Schematic



By default, after MPU NRST is released, the backlight driver is off and a command on LCD\_BACKLIGHT\_PWM must be applied to power on the LCD Module backlight.

## 2.4 maXTouch® Capacitive Touchscreen Controller

The module's touch screen interface is based on the Microchip maXTouch mXT336UD Touch Controller and operates on the touch sensor at connector J2. The touch controller scans the touch sensor and signals the host with an active-low interrupt signal (MXT\_IRQ on J1) when new touch data is available.

Data communication with the maXTouch controller is performed via the I2C interface (LCD\_I2C\_DATA and LCD\_I2C\_CLK on J1). The I2C address of the touch controller is fixed at 0x4A, and is not configurable.

**Note:** The maXTouch has no pull-up resistors featured on the I2C Bus (LCD\_I2C\_DATA and LCD\_I2C\_CLK) or its Interrupt Line (MXT\_IRQ). They must be featured on the host board driving the display.

Details of the maXTouch communication protocol are beyond the scope of this document. This module is pre-loaded with a configuration already optimized for the maXTouch touch sensor and panel, hence the developer will only focus on interfacing with the device. When developing the maXTouch controller interface during evaluation and host development, care should be taken to avoid changing the maXTouch configuration or committing changes to non-volatile storage on the maXTouch controller. To start with host interface development, users need to leverage the existing code available from Linux which is available at [www.microchip.com/linux](http://www.microchip.com/linux) for more details or from MPLAB® Harmony, which is available at [www.microchip.com/mplab/mplab-harmony](http://www.microchip.com/mplab/mplab-harmony).

For additional information regarding maXTouch devices, refer to [www.microchip.com](http://www.microchip.com).

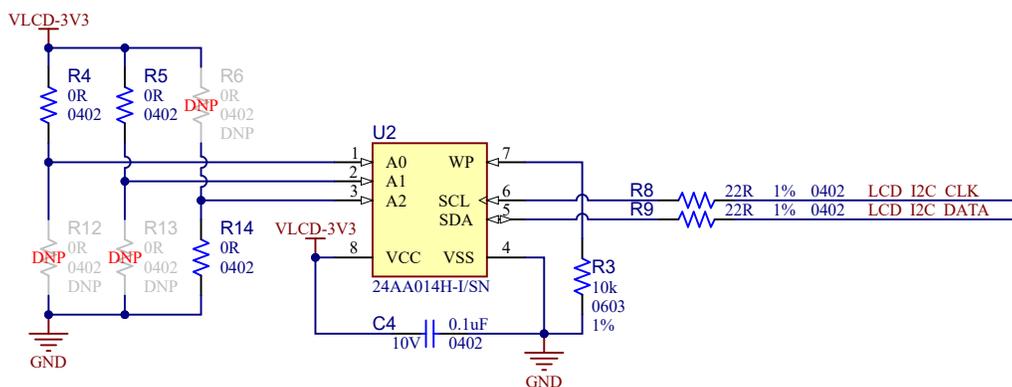
## 2.5 LCD Module Identification With EEPROM

The 5" 4-Lane LVDS WVGA LCD Display Module kit embeds a 24AA014H 1-Kbit EEPROM used for LCD Module identification.

The Microchip 24AA014H is a 1-Kbit serial EEPROM with operation down to 1.7V. The device is organized as a single block of 128 x 8-bit memory with a 2-wire serial interface. Low-current design permits operation with maximum standby and active currents of only 1  $\mu$ A and 400  $\mu$ A, respectively. The device has a page write capability of up to 16 bytes of data.

For more details on the 24AA014H EEPROM memory, refer to [additional resources](#).

Figure 2-4. 24AA014H Module Identification Schematic



## I<sup>2</sup>C Address : 0x53



**Important:** The 24AA014H EEPROM is accessible through the I<sup>2</sup>C bus at 0x53 address. No other components with the same I<sup>2</sup>C address can be connected on the same bus.



**Important:** It is recommended not to erase the data stored in the 24AA014H EEPROM. This data is necessary at system level for the LCD module identification. The data contained is a 42-byte word described in the table below.

Table 2-1. EEPROM Memory Mapping

Byte(s) N°	Type	Information	Default Value
[0]	Decimal	Number of bytes	0x2A (42 bytes)
[5:2]	ASCII String [4]	Microchip division	0x4D505520 (MPU )
[8:7]	ASCII String [2]	Manufacturer country	0x434E (CN)
[21:10]	ASCII String [12]	Ordering code	0x414336395438384120202020 (AC69T88A )
[32:23]	ASCII String [10]	Main device embeds	0x53543732363220202020 (ST7262 )
[34]	Decimal	Manufacturing code (year)	0x16 (2022)
[35]	Decimal	Manufacturing code (week)	0x28 (Week 40)
[37]	ASCII String [1]	Hardware revision	0x31 (Revision 1)
[39]	ASCII String [1]	Mapping revision	0x41 (Revision A)
[41]	Hexa Data	CRC-8 checksum	N/A

**Note:** Unspecified bytes are used as data separators and contain the value 0x2D.

### 3. Connecting AC69T88A to a Microchip MPU Curiosity Board

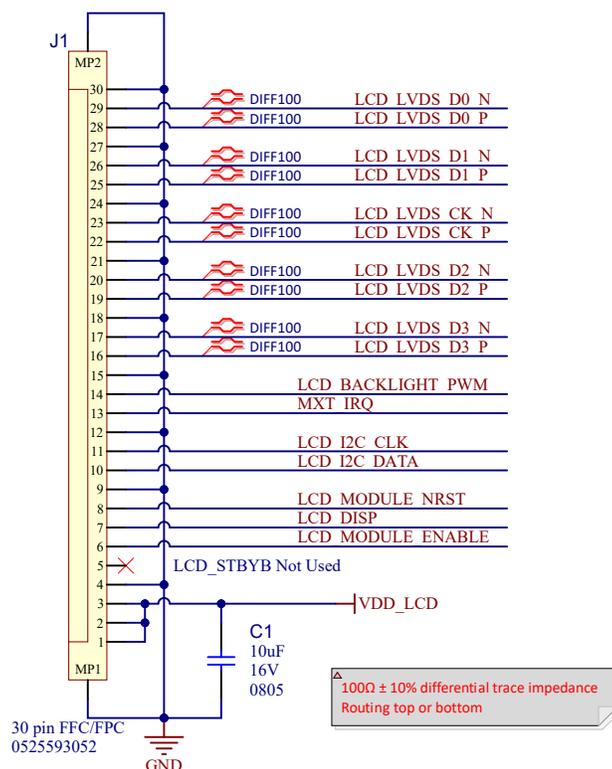
The AC69T88A can be easily mounted on the LVDS display-capable Microchip MPU Curiosity boards. A connection using a flexible flat cable (FFC), which is contained in the box with the display, is necessary to control the LCD Module by software. Four connectors are used between the LCD module and the Microchip MPU Curiosity board for the mechanical connection, thus ensuring stability of the ensemble.

#### 3.1 Microchip MPU Curiosity Board Connector

Microchip MPU Curiosity boards are evaluation platforms that provide a full Microchip embedded microprocessor experience to users. This platform consists of a series of microcontrollers and extension boards, which are supported by Linux and/or by MPLAB® X IDE, MPLAB Harmony drivers, demo code, etc..

The AC69T88A is connected to the MPU host board with an FFC to the J1 connector, which has the schematic and pinout described below.

**Figure 3-1.** Microchip MPU Curiosity Board Connector Schematic



**Table 3-1.** Microchip MPU Curiosity Board Connector Pinout

Pin Number	Signal Name	I/O Type	Description
29	LCD_LVDS_D0_N	Input	Negative LVDS Differential Data Line 0
28	LCD_LVDS_D0_P	Input	Positive LVDS Differential Data Line 0
26	LCD_LVDS_D1_N	Input	Negative LVDS Differential Data Line 1
25	LCD_LVDS_D1_P	Input	Positive LVDS Differential Data Line 1
23	LCD_LVDS_CLK_N	Input	Negative LVDS Differential Clock Line

.....continued

Pin Number	Signal Name	I/O Type	Description
22	LCD_LVDS_CLK_P	Input	Positive LVDS Differential Clock Line
20	LCD_LVDS_D2_N	Input	Negative LVDS Differential Data Line 2
19	LCD_LVDS_D2_P	Input	Positive LVDS Differential Data Line 2
17	LCD_LVDS_D3_N	Input	Negative LVDS Differential Data Line 3
16	LCD_LVDS_D3_P	Input	Positive LVDS Differential Data Line 3
14	LCD_BACKLIGHT_PWM	Input	Backlight Driver Control
13	MXT_IRQ	Output	Touch Screen Controller Interrupt
11	LCD_I2C_CLK	Input	Touch Screen Controller I <sup>2</sup> C Clock Line
10	LCD_I2C_DATA	Bidir	Touch Screen Controller I <sup>2</sup> C Data Line
8	LCD_MODULE_NRST	Input	LCD Module General Reset Line (LCD Driver, Touch screen controller)
7	LCD_DISP	Input	LCD Driver Enable
6	LCD_MODULE_EN	Input	LCD Module Enable
5	NC	NC	Not Connected
3, 2, 1	VDD_LCD	Power	LCD Module 5V Main Supply
30, 27, 24, 21, 18, 15, 12, 9, 4	GND	Power	Ground Supply

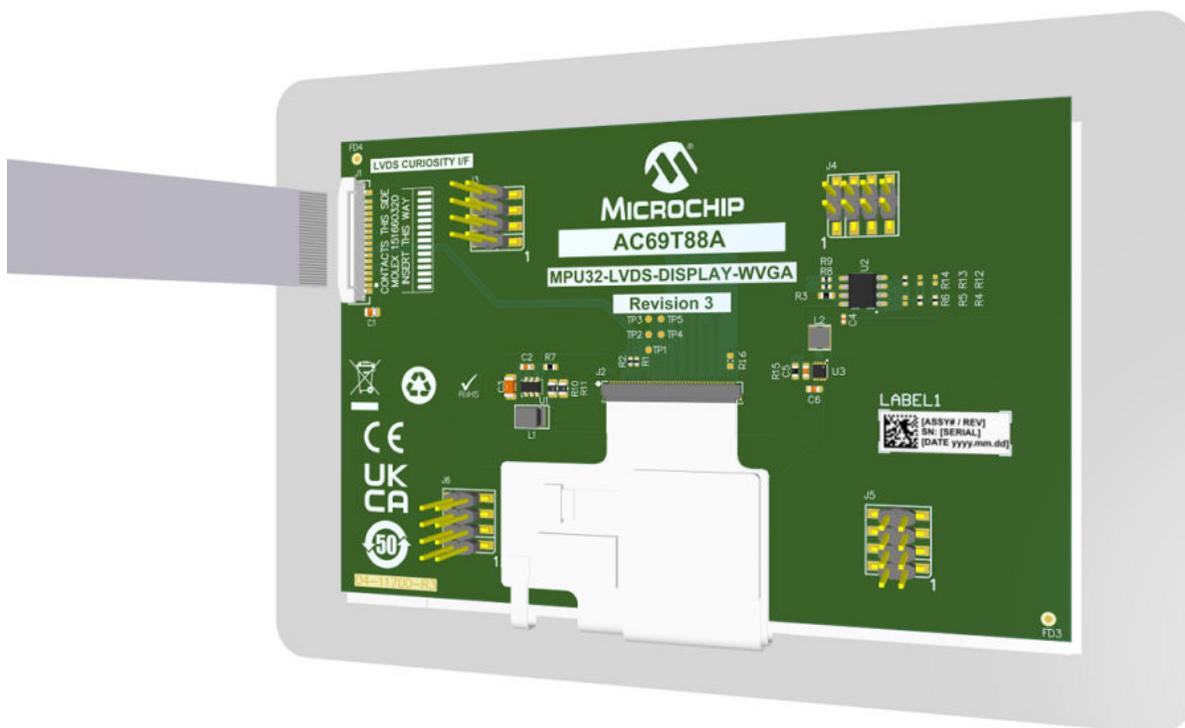
## 3.2 Connecting the LCD Module to the Curiosity Board

### 3.2.1 Connecting the Flexible Flat Cable (FFC)

To connect the LCD module to the host board using the flexible flat cable (FFC), follow the sequence below:

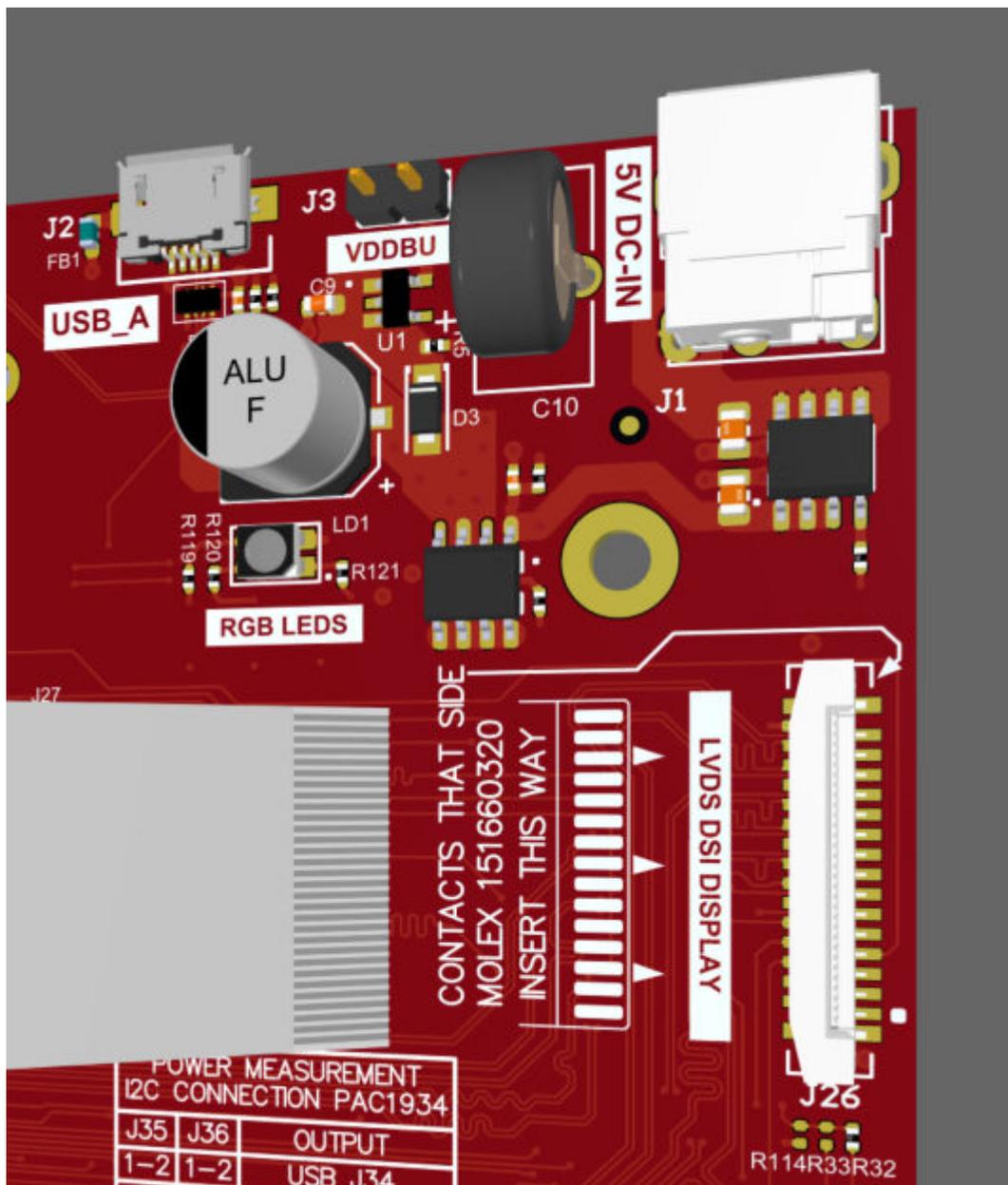
- First :
  - Connect the FFC to the LCD module as illustrated in [Figure 3-2](#).
  - Ensure that the metallic fingers of the FFC are facing towards the interior of the board when the FFC is inserted in the header J1, as shown in [Figure 3-2](#).
  - Verify that Pin 1 of the FFC is aligned with Pin 1 of the J1 connector of the AC69T88A board.

**Figure 3-2.** Connecting the FFC to the LCD Module



- Second:
  - Connect the FFC to the host board as illustrated in [Figure 3-3](#).
  - Ensure that the FFC metallic fingers are facing towards the outside of the board when it is inserted in the connector, as shown in [Figure 3-3](#).
  - Verify that Pin 1 of the FFC is aligned with Pin 1 of the connector.

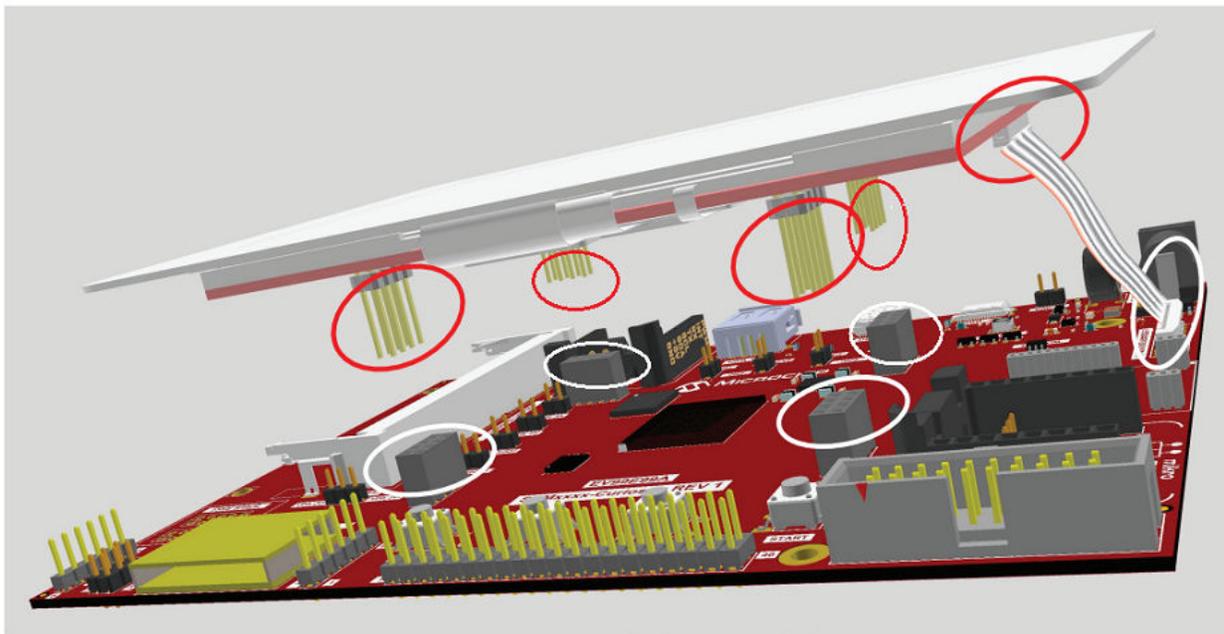
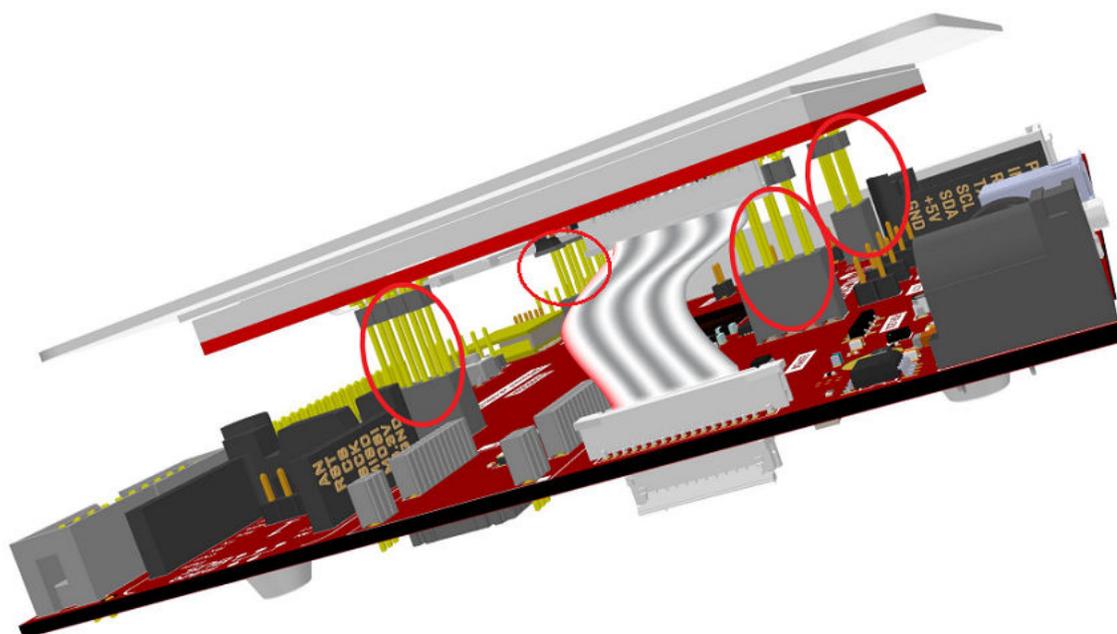
Figure 3-3. Connecting the FFC to the Curiosity Board



### 3.2.2 Mechanical Connection of the LCD Module with the Curiosity Board

Once the FFC connecting the board to the module is in place, follow the steps below to position the AC69T88A correctly on the Curiosity board.

1. Position the LCD module so that the four 2x4-pin headers (circled in red) are above the Curiosity board headers (circled in white) as illustrated in [Figure 3-4](#) below.
2. Be sure that the FFC connecting the two boards is not twisted.
3. Push the LCD module headers into the Curiosity board headers as shown in [Figure 3-5](#).
4. Make sure that all headers are perfectly aligned and mated in the Curiosity board sockets.

**Figure 3-4.** Positioning the LCD Module**Figure 3-5.** Correct Placement of the LCD Module

## 4. LCD Module Specifications

**Table 4-1.** Module Parameters

Parameter	Value
LCD Module size	5 inches
Overall dimensions	102 mm (H) x 148 mm (W) x 21.4 mm (T)
Overall weight	132g ± 5g

**Table 4-2.** Absolute Maximum Specifications

Parameter	Value
Operating temperature	-20°C to +60°C
Storage temperature	-30°C to +70°C
VDD_LCD	-0.5 to +5.5V
Maximum continuous pin current, any control or drive pin	±20 mA
Voltage forced onto any pin	-0.5V to +3.6V



**Important:** Stresses beyond those listed in the above table may cause permanent damage to the device. This is a stress rating only and the functional operation of the device under these conditions or others beyond those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum specification conditions for extended periods may affect device reliability.

**Table 4-3.** Recommended Operating Conditions

Parameter	Value
VDD_LCD (LCD Module input voltage)	5V ± 250mV
Supply ripple + noise	±100 mV

**Table 4-4.** DC Specifications

Parameter	Description	Min.	Typ.	Max.	Unit	Conditions
V <sub>IL</sub>	Low-Input logic level (I <sup>2</sup> C, LCD_DISP, NRST)	-0.5	-	0.3 VDDIO	V	3.2V < VDDIO < 3.4V
V <sub>IH</sub>	High-Input logic level (I <sup>2</sup> C, LCD_DISP, NRST)	0.7 VDDIO	-	-	V	3.2V < VDDIO < 3.4V
V <sub>OL</sub>	Low-Output logic level (MXT_IRQ, I <sup>2</sup> C)	-	-	0.2 VDDIO	V	3.2V < VDDIO < 3.4V
V <sub>OH</sub>	High-Output logic level (MXT_IRQ, I <sup>2</sup> C)	0.8 VDDIO	-	-	V	3.2V < VDDIO < 3.4V
V <sub>IL</sub>	Low-Input logic level (LCD_MODULE_EN)	-	-	0.5	V	4.75V < VDD_LCD < 5.25V
V <sub>IH</sub>	High-Input logic level (LCD_MODULE_EN)	0.5	0.8	1.2	V	4.75V < VDD_LCD < 5.25V
V <sub>IL</sub>	Low-Input logic level (LCD_BACKLIGHT_PWM)	-	-	0.4	V	4.75V < VDD_LCD < 5.25V
V <sub>IH</sub>	High-Input logic level (LCD_BACKLIGHT_PWM)	1.1	-	-	V	4.75V < VDD_LCD < 5.25V

**Table 4-5.** I2C Compatible Bus Specifications

Parameter	Operation
Touchscreen controller address	0x4A
EEPROM memory address	0x53
Maximum bus speed (SCL)	400 kHz
Bus voltage	3.3V

**Table 4-6.** LCD Panel Specifications

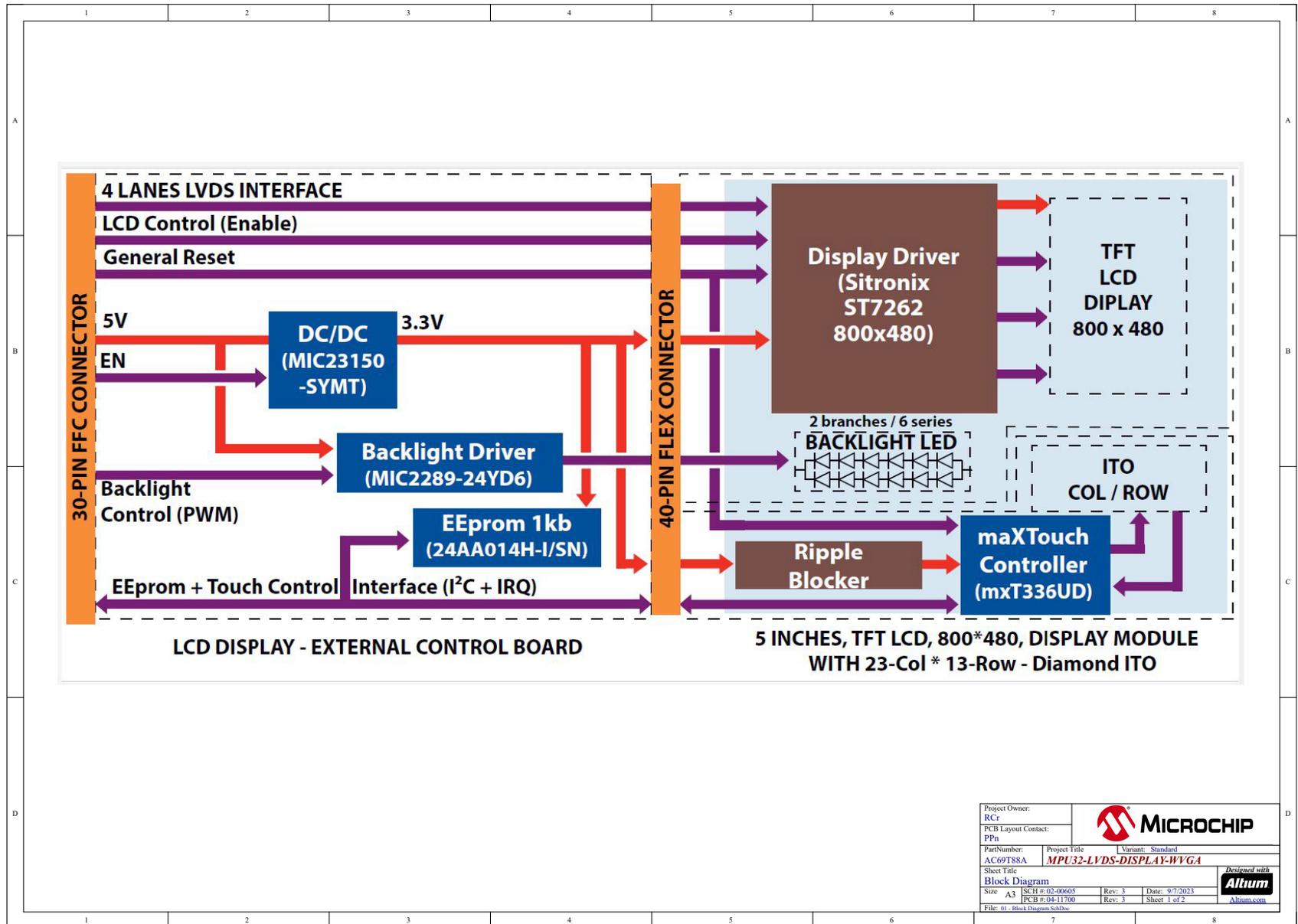
Parameter	Value
Display size	5 inches
LCD type	TFT/Normally Black
Display mode	Transmissive
Resolution	800 x 480 (WVGA)
View direction	FREE
Dimensions	110 mm (L) x 66.8 mm (H) x 2.85 mm (T)
Active area	108 mm x 64.8 mm
Pixel size	0.135 mm x 0.135 mm
Pixel arrangement	Stripe
Display colors	16.7M

**Table 4-7.** Backlight Specifications

Parameter	Description	Min.	Typ.	Max.	Units
$V_F$	Forward voltage ( $T_A = 25^\circ\text{C}$ , $I_F = \text{Typ}$ )	16.2	18	19.8	V
$I_F$	Forward current ( $T_A = 25^\circ\text{C}$ )	-	40	--	mA
	LED configuration	2 branches of 6x white LEDs in series			
$L_V$	Luminance	-	450	-	cd/m <sup>2</sup>
$P_d$	Maximum power dissipation	-	-	800	mW
$V_{AK}$	Backlight driving voltage	4.75	5	5.25	V

## 5. Schematics

Figure 5-1. AC69T88A Block Diagram Schematic





## 6. Ordering Information

Ordering Code	Board Marking
AC69T88A	AC69T88A MPU32-LVDS-DISPLAY-WVGA

## 7. Revision History

### 7.1 DS50003586B - 10/2023

#### Changes

Editorial updates throughout.

Updated [Figure 2-4](#).

Updated [Figure 3-1](#).

### 7.2 DS50003586A - 10/2023

#### Changes

First issue.

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