

INDUSTRIAL PRODUCT DATASHEET

Sipeed Longan Nano - RISC-V GD32VF103CBT6 Development Board with Acrylic Case

Sipeed Longan Nano: RISC-V GD32VF103CBT6 development board with 128KB Flash, 32KB SRAM, timers, communication interfaces, and analog peripherals.

SKU

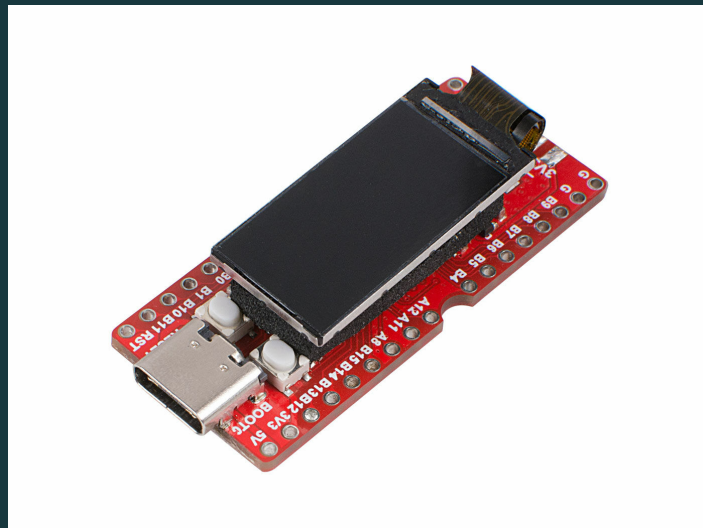
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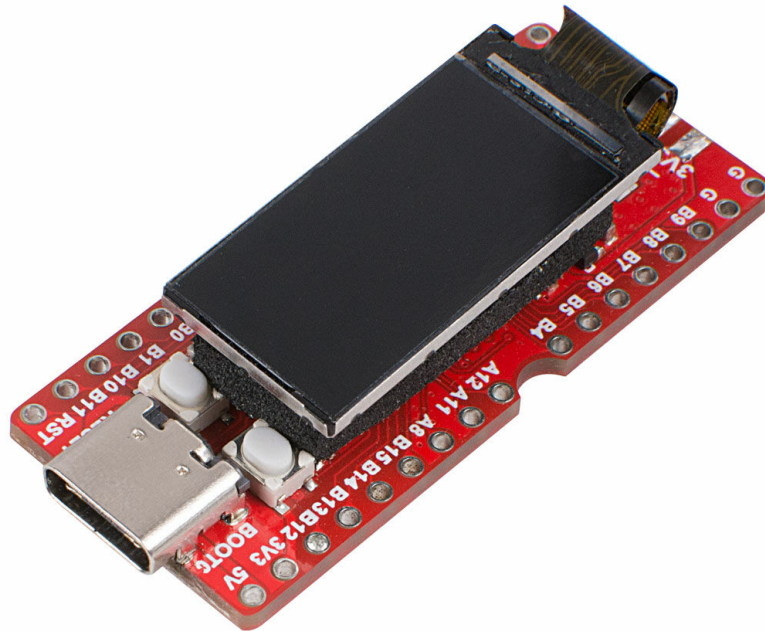
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PRICE

\$4.90(Excl. VAT)



Overview



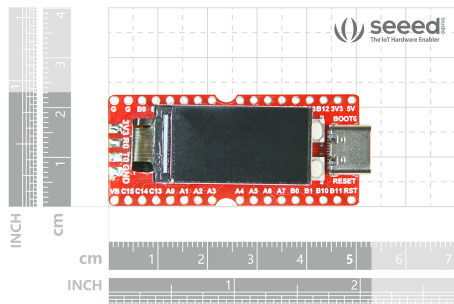
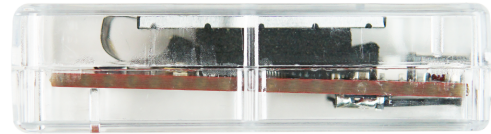
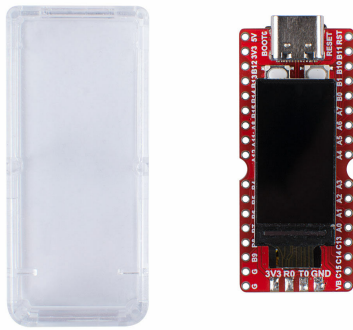
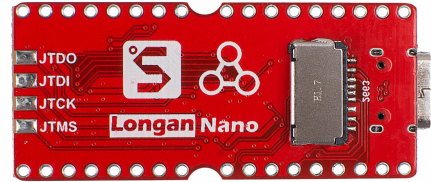
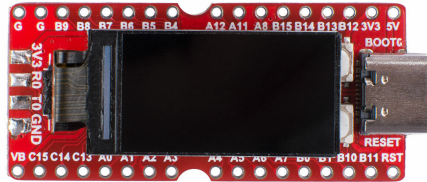
The Sipeed Longan Nano is a development board based on the RISC-V GD32VF103CBT6 microcontroller with 128KB Flash and 32KB SRAM. It includes 4x general-purpose 16-bit timer, 2x basic 16-bit timer, 1x advanced 16-bit timer, Watchdog, RTC, SysTick, 3x USART, 2x I2C, 3x SPI, 2x I2S, 2x CAN, 1x USBFS (OTG), 2x ADC (10 channel), and 2x DAC.

Key Features

Features

- Chip built-in 128KB Flash, 32KB SRAM
- 4 x general purpose 16-bit timer, 2 x basic 16-bit timer, 1 x advanced 16-bit timer
- Watchdog, RTC, SysTick
- 3 x USART, 2 x I2C, 3 x SPI, 2 x I2S, 2 x CAN, 1 x USBFS (OTG)
- 2 x ADC (10 channel), 2 x DAC

Product Gallery



Part List

Part List	
Sipeed Longan Nano Development Board	x1
0.96inch 160x80 IPS RGB LCD	x1
Acrylic transparent case	x1

Compliance & Logistics



HSCODE	8517180050
USHSCODE	8517180050
UPC	
EUHSCODE	8543709099
COO	CHINA

Resource Links

- <https://www.seeedstudio.com/Sipeed-Longan-Nano-RISC-V-GD32VF103CBT6-Development-Board-p-4205.html>
- [Wiki](#)
- [LCD Demo](#)
- [GD32VF103_User_Manual_EN_V1.0.pdf](#)
- [GD32VF103_Datasheet_Rev1.0.pdf](#)
- [Bumblebee core datasheet_en.pdf](#)
- [Bumblebee core intro_en.pdf](#)
- [Bumblebee kernel instruction set documentation](#)
- [Github](#)
- [SDK](#)
- [HDK](#)

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