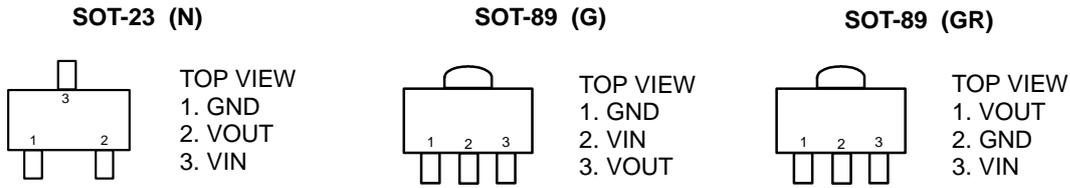




Pin Configuration



Pin Descriptions

| Pin Symbol | Pin Function |
|-------------|---|
| VIN | Power is supplied to the device through this pin and requires an input filter capacitor. In general, an input capacitor in the range of 1 μ F to 10 μ F is sufficient. |
| VOUT | The output supplies power to loads. The output capacitor is required to provide a stable output voltage. The APE8800-3 is stable with an output capacitor of 1 μ F or greater. A larger output capacitor will be required for applications with large transient loads to limit peak voltage transients, and can also reduce output noise, improve stability and PSRR. |
| GND | Common ground pin |

Block Diagram

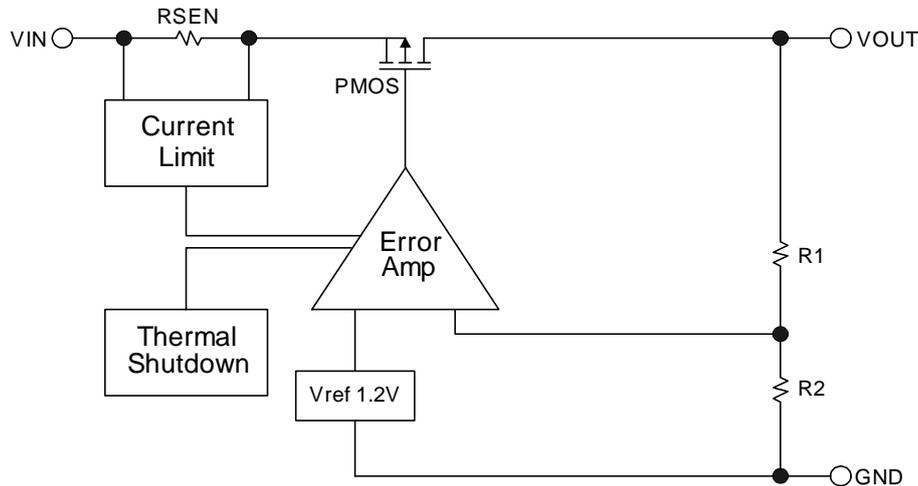


Figure 2. Block diagram of APE8800-3



Application Description

The APE8800-3 series are low dropout linear regulators that can provide 300mA output current with a drop-out voltage of about 2-300mV. Also, current limit and on-chip thermal shutdown features provide protection against any combination of overload or junction temperature that exceeds the shutdown temperature.

1. Output and Input Capacitor

The APE8800-3 regulator is designed to be stable with a wide range of output capacitors. The ESR of the output capacitor affects stability. Larger values of the output capacitor decrease the peak deviations and provide improved transient response for larger current changes.

The various capacitor types (aluminum, ceramic, tantalum) have different characteristics such as temperature and voltage coefficients. All ceramic capacitors are manufactured with a variety of dielectrics, each with different behavior across temperature and applications. Common dielectrics used are X5R, X7R and Y5V. It is recommended to use 1uF to 10uF X5R or X7R dielectric ceramic capacitors with 30mΩ to 50mΩ ESR range between device outputs to ground for transient stability. The APE8800-3 is designed to be stable with low ESR ceramic capacitors, and higher values of capacitors and ESR can improve output stability.

So the ESR of the output capacitor is very important because it generates a zero to provide phase lead for loop stability.

There are no requirements for the ESR on the input capacitor, but its voltage and temperature coefficient have to be considered for the device application environment.

2. Protection Features

In order to prevent overloading or a thermal condition from damaging the device, the APE8800-3 regulator has internal thermal and current-limiting functions designed to protect the device. It will rapidly shut off the internal P-channel MOSFET pass element during overloading or an over-temperature condition.

3. Thermal Consideration

The power handling capability of the device is limited by the maximum operation junction temperature (125°C). The power dissipated by the device can be estimated by $PD = I_{OUT} \times (V_{IN} - V_{OUT})$. This power dissipation must be lower than the maximum power dissipation listed in the "Absolute Maximum Ratings" section.



Typical Performance Characteristics

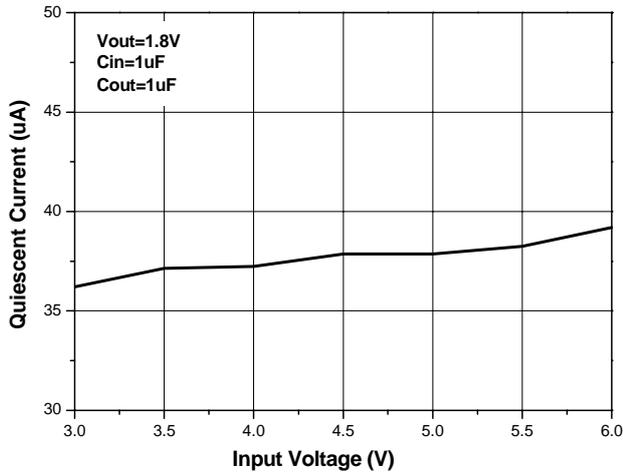


Figure 4. Quiescent Current vs. Input Voltage

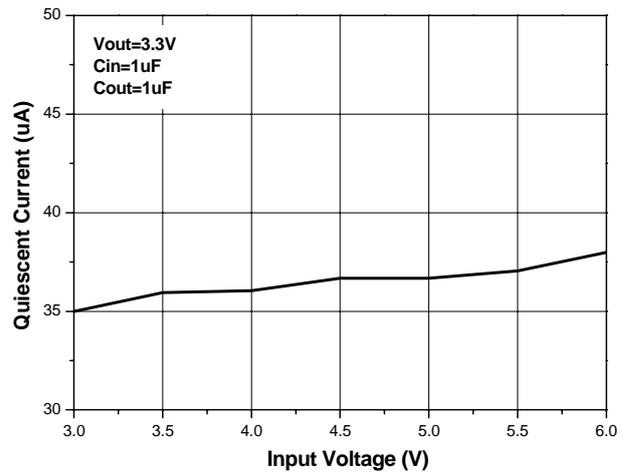


Figure 5. Quiescent Current vs. Input Voltage

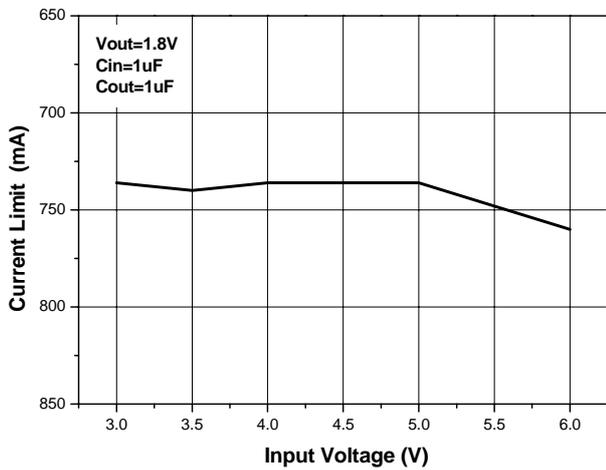


Figure 6. Current limit vs. Input Voltage

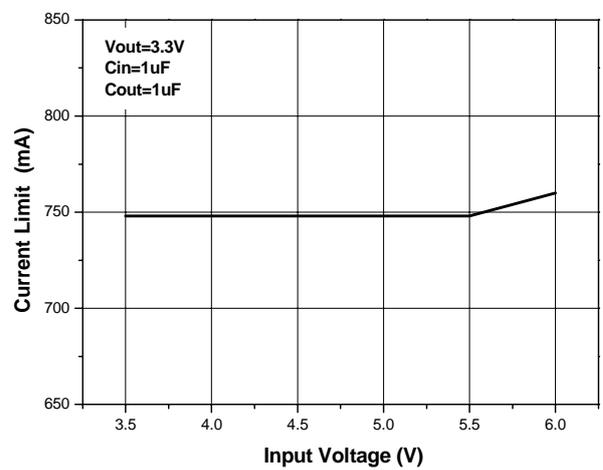


Figure 7. Current Limit vs. Input Voltage

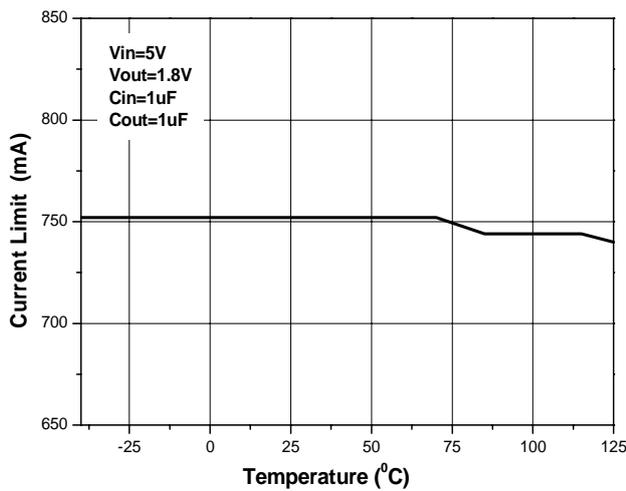


Figure 8. Current limit vs. Temperature

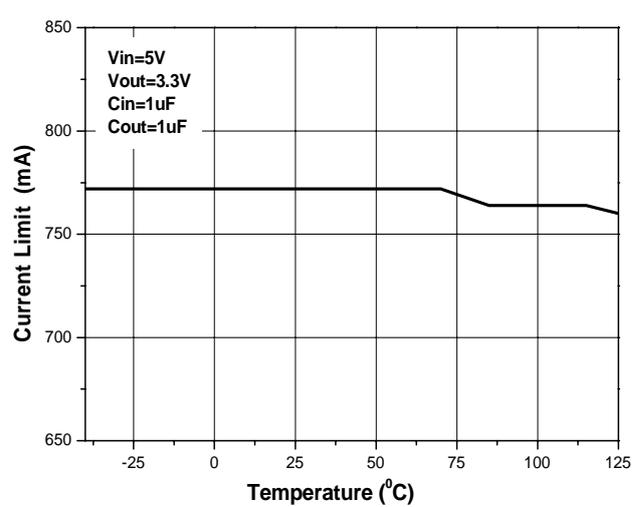


Figure 9. Current limit vs. Temperature



Typical Performance Characteristics

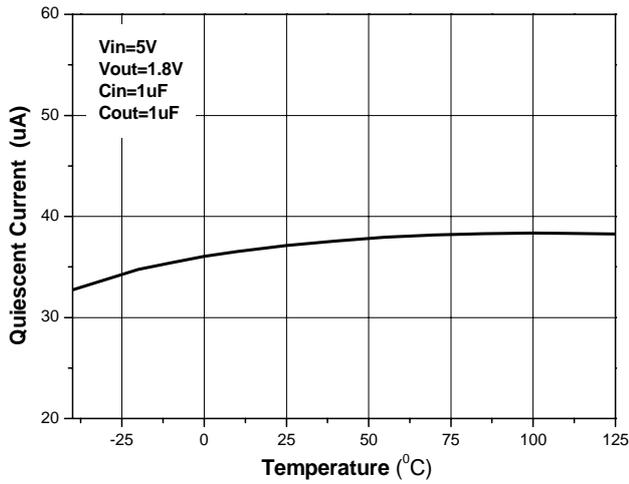


Figure 10. Quiescent Current vs. Temperature

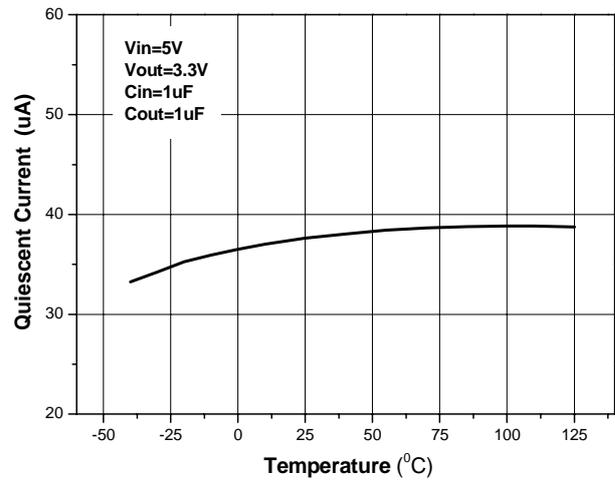


Figure 11. Quiescent Current vs. Temperature

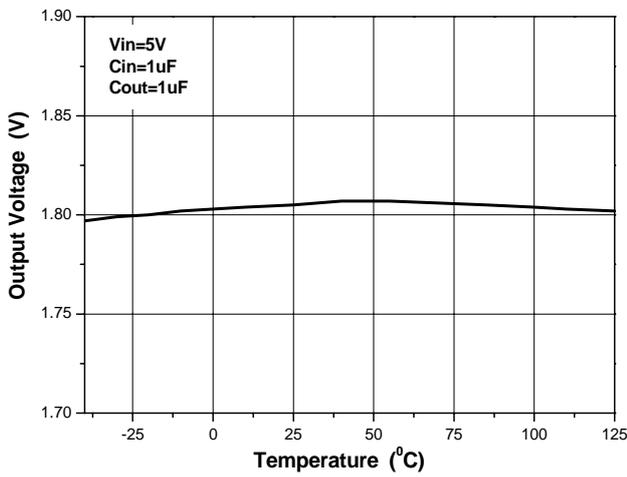


Figure 12. Temperature Stability

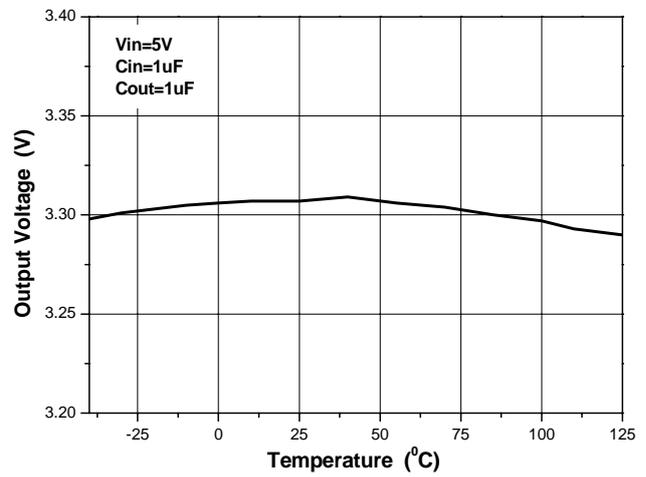


Figure 13. Temperature Stability

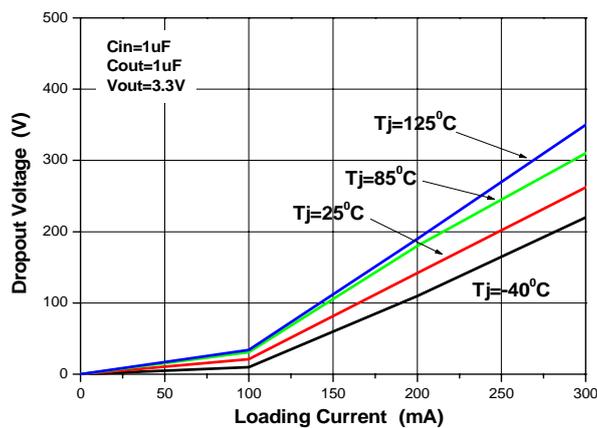


Figure 14. Dropout Voltage vs. Loading Current



Typical Performance Characteristics

$V_{IN}=4V$ $I_{OUT}=1mA$ to $150mA$

$V_{OUT}=3.3V$ $C_{IN}=1\mu F$ $C_{OUT}=1\mu F$

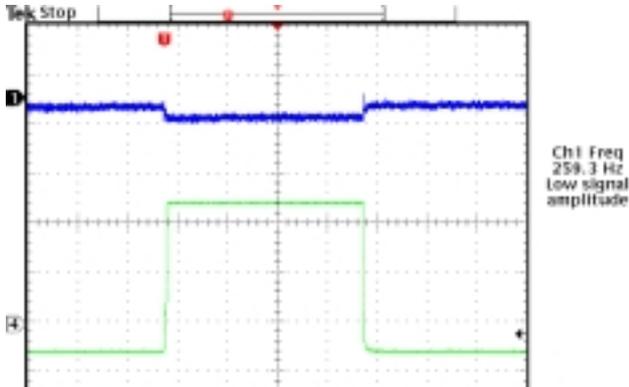


Figure 15. Load Transition Response

$V_{IN}=4V$ $I_{OUT}=1mA$ to $150mA$

$V_{OUT}=3.3V$ $C_{IN}=1\mu F$ $C_{OUT}=4.7\mu F$

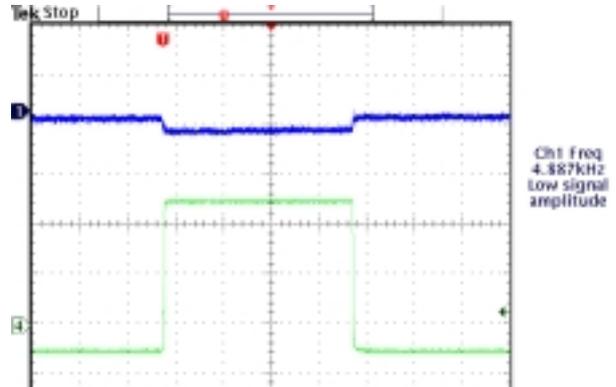


Figure 16. Load Transition Response

$V_{IN}=3V$ to $4V$ $I_{OUT}=10mA$ $V_{OUT}=1.8V$ $C_{IN}=1\mu F$ $C_{OUT}=1\mu F$

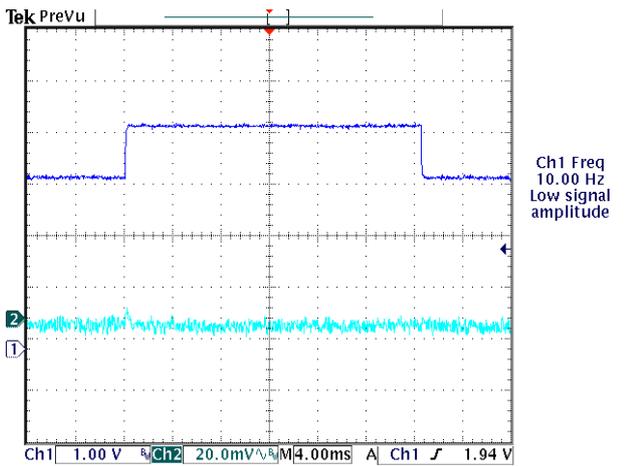


Figure 17. Line Transition Response

$V_{IN}=3V$ to $4V$ $I_{OUT}=10mA$ $V_{OUT}=1.8V$ $C_{IN}=1\mu F$ $C_{OUT}=4.7\mu F$

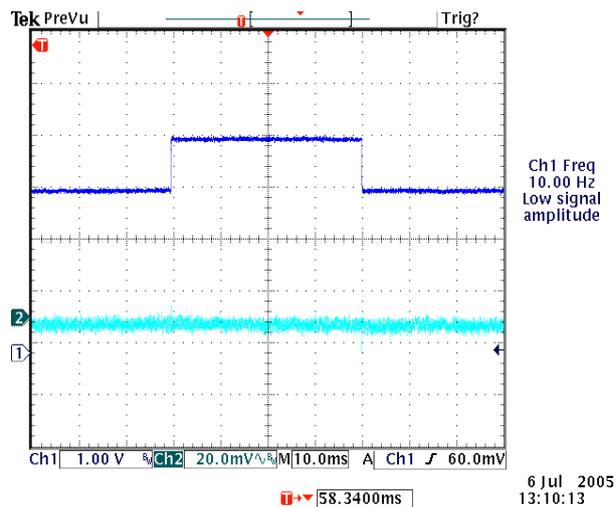
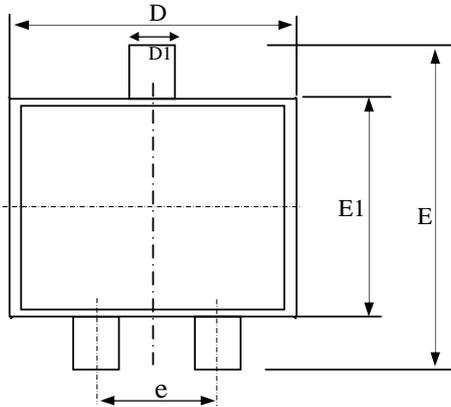


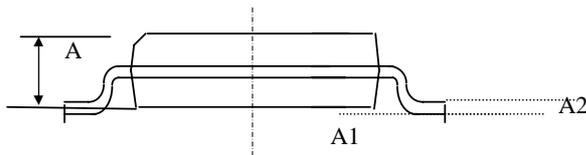
Figure 18. Line Transition Response



Package Dimensions: SOT-23



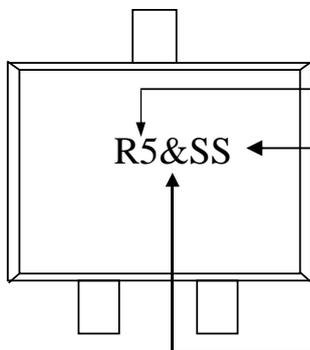
| SYMBOLS | Millimeters | | |
|---------|-------------|------|------|
| | MIN | NOM | MAX |
| A | 1.00 | 1.15 | 1.30 |
| A1 | 0.00 | -- | 0.10 |
| A2 | 0.10 | 0.15 | 0.25 |
| D1 | 0.30 | 0.40 | 0.50 |
| e | 1.70 | 2.00 | 2.30 |
| D | 2.70 | 2.90 | 3.10 |
| E | 2.40 | 2.65 | 3.00 |
| E1 | 1.40 | 1.50 | 1.60 |



1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

Marking Information

Laser Marking



Product: R5 = APE8800N

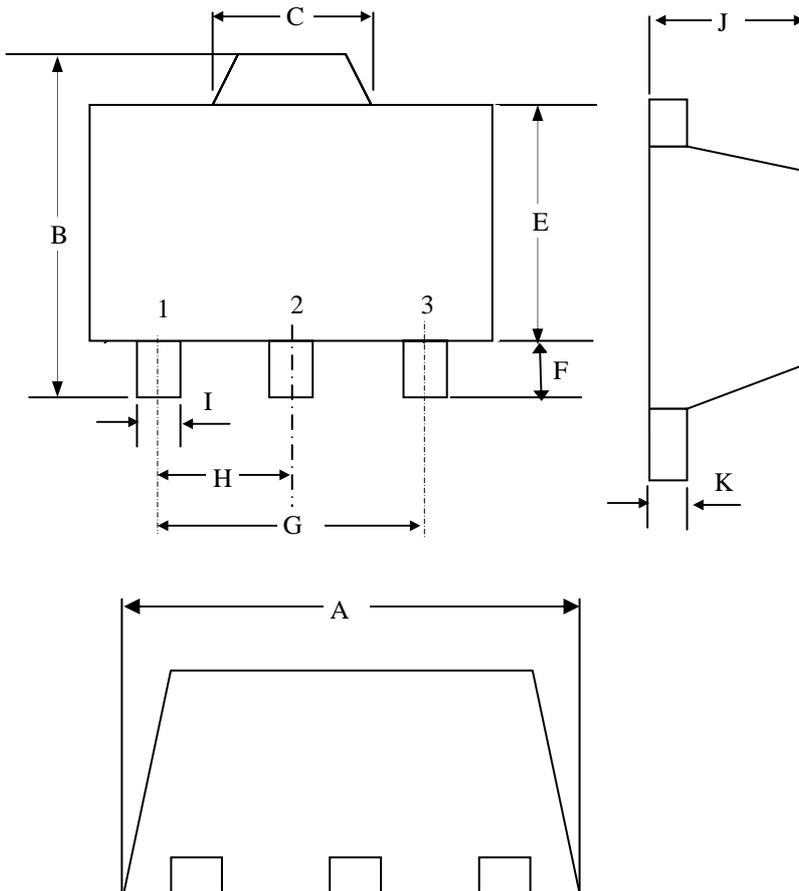
Date/lot code

For details of how to convert this to standard YYWW date code format, please contact us directly.

VOUT : A : 1.5V D : 2.8V H : 3.6V
 B : 1.8V E : 3.0V
 C : 2.5V F : 3.3V



Package Dimensions: SOT-89



| SYMBOLS | Millimeters | | |
|---------|-------------|------|------|
| | MIN | NOM | MAX |
| A | 4.40 | - | 4.60 |
| B | 4.05 | - | 4.25 |
| C | 1.40 | - | 1.75 |
| E | 2.40 | - | 2.60 |
| F | 0.89 | - | 1.20 |
| I | 0.35 | - | 0.55 |
| H | ---- | 1.50 | ---- |
| G | ---- | 3.00 | ---- |
| J | 1.40 | - | 1.60 |
| K | 0.35 | - | 0.43 |

1. All dimensions are in millimeters.
2. Dimensions do not include protrusions.

Marking Information

