





### 80V NPN SILICON PLANAR DARLINGTON TRANSISTOR IN SOT89

#### **Features**

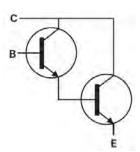
- BV<sub>CEO</sub> > 80V
- High current gain
- Max Continuous Current I<sub>C</sub> = 500mA
- Fast switching
- Lead Free, RoHS Compliant (Note 1)
- Halogen and Antimony Free, "Green" Device (Note 2)
- · Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

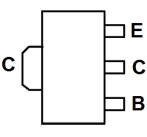
- Case: SOT89
- Moisture Sensitivity: Level 1 per J-STD-020
- UL Flammability Rating 94V-0
- Terminals: Matte Tin Finish
- Weight: 0.052 grams (Approximate)







Device symbol



Top View Pin-out

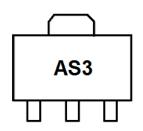
## **Ordering Information** (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
BST52TA	AS3	7	12	1,000

Notes:

- 1. No purposefully added lead.
- 2. Halogen and Antimony Free. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com
- 3. For packaging details, go to our website at http://www.diodes.com

# **Marking Information**



AS3 = Product Type Marking Code



BST52

## Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	90	V
Collector-Emitter Voltage	V <sub>CEO</sub>	80	V
Emitter-Base Voltage	$V_{EBO}$	10	V
Continuous Collector Current	Ic	500	mA
Peak Pulse Current	I <sub>CM</sub>	1.5	Α
Base Current	I <sub>B</sub>	100	mA

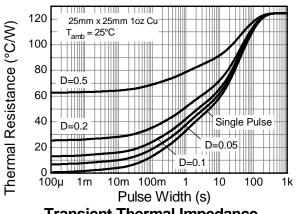
# Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

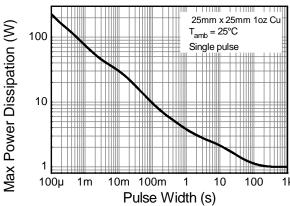
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient (Note 4)	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction to Leads (Note 5)	$R_{ heta JL}$	8.66	°C/W
Operating and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 to +150	°C

Notes:

- 4. For a device surface mounted on 25mm X 25mm FR4 PCB with high coverage of single sided 1 oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 5. Thermal resistance from junction to solder-point (on the exposed collector pad).

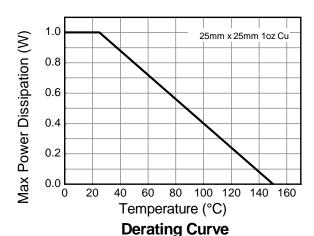
## Thermal Characteristics





## **Transient Thermal Impedance**









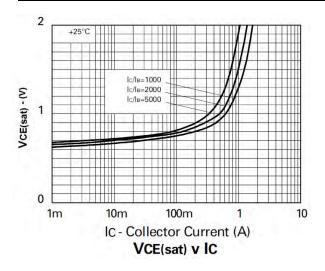
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

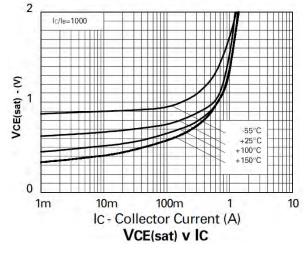
Characteristic	Symbol	Min	Тур.	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	$BV_CBO$	90	-	-	V	$I_C = 10\mu A$
Collector-Emitter Breakdown Voltage (Notes 6)	$BV_CEO$	80	-	-	V	I <sub>C</sub> = 10mA
Emitter-Base Breakdown Voltage	$BV_{EBO}$	10	-	-	V	$I_E = 10\mu A$
Collector Cutoff Current	I <sub>CES</sub>	-	-	10	μA	V <sub>CE</sub> = 80V
Emitter Cutoff Current	I <sub>EBO</sub>	-	-	10	μA	$V_{EB} = 8V$
DC current transfer Static ratio (Notes 6)	h <sub>FE</sub>	1000 2000	-	ı		I <sub>C</sub> = 150mA, V <sub>CE</sub> = 10V I <sub>C</sub> = 500mA, V <sub>CE</sub> = 10V
Collector-Emitter Saturation Voltage (Notes 6)	V <sub>CE(sat)</sub>	-	·	1.3 1.3	V	$I_C = 500$ mA, $I_B = 0.5$ mA $I_C = 500$ mA, $I_B = 0.5$ mA, $T_J = 150$ °C
Base-Emitter Saturation Voltage (Notes 6)	$V_{BE(sat)}$	-	-	1.9	V	$I_C = 500 \text{mA}, I_B = 0.5 \text{mA}$
Turn On Time	t <sub>ON</sub>		0.4		us	I <sub>C</sub> = 500mA,
Turn Off Time	t <sub>OFF</sub>	-	1.5	-	μδ	$I_{Bon} = I_{Boff} = 0.5 mA$

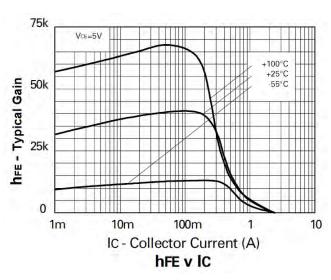
Notes: 6. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤2%.

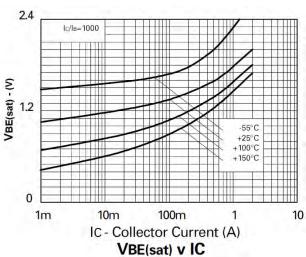


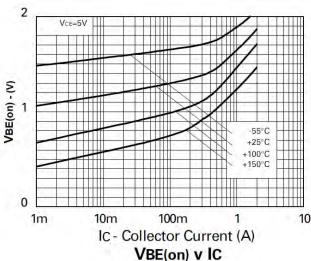
# **Typical Electrical Characteristics**





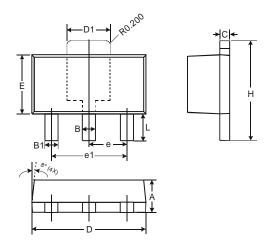






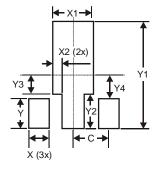


# **Package Outline Dimensions**



SOT89				
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
С	0.35	0.43		
D	4.40	4.60		
D1	1.52	1.83		
Е	2.29	2.60		
е	1.50 Typ			
e1	3.00 Typ			
Η	3.94 4.25			
L	0.89	1.20		
All Dimensions in mm				

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Х	0.900
X1	1.733
X2	0.416
Υ	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
C	1 500





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